

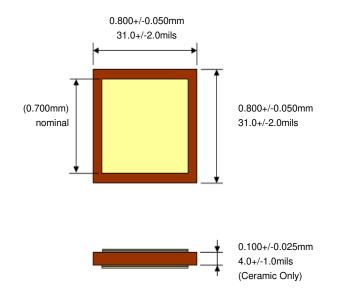
TECDIA CO., LTD.

2F Tamachi Kiyota Bldg.

4-3-4 Shibaura Minato-ku, Tokyo 108-0023 Japan

TEL: 81-3-5765-5400 FAX: 81-3-5765-5404

SPECIFICATION SHEET



Manufactured to metric dimensions. Imperial units are for reference only.

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BMS151K2P					
2800					
150[pF] @ 1kHz, 1 Vrms, 25°C, No DC Bias					
K (tolerance: ± 10%)					
2.5% Max @ 1kHz, 1 Vrms, 25°C, No DC Bias					
50 V					
100,000MΩ Min @ 50Vdc, 25°C					
No breakdown @ 125Vdc x 2sec, 25°C					
±15% (X7R @ -55°C to +125°C)@ No DC Bias					
TiW - Au 2.5μm Min					
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NOTES:

Other specifications not listed are available at www.tecdia.com.
 Specifications may be subject to change without prior notice.

that can occur when solder die mounting.

- · RoHS compliant.
- Wire bonding location should be 25um or further from edges of the electrode to avoid electrode peeling.
- Capacitance, Temperature Coefficient and Dissipation Factor are measured before any AC or DC bias has been applied.
- Recommended Storage Conditions (Waffle Packaging): 23 +/- 10°C @ 60% RH Max
- Guaranteed Shelf Life: 1 year after delivery under recommended storage conditions.
- Epoxy attachment is recommended. Successful wire bonding and die attachment are dependent
 on the types of bonding tools and conditions used. Please check the wire bonding and die attach
 conditions of your site to prevent the wire/electrode from peeling or detaching.
 Tecdia is not responsible for mechanical issues such as cracking or detaching

PREPARED BY:		DESCRIPTION:				
M. Simpson	2020/1/14	CHIP CAPACITOR	Scale:	Not to	o Sca	le
APPROVED BY:		TECDIA PART NUMBER:	SHEET:			
T. Yoshikawa	2020/1/14	BMS151K2P	1	of	1	