Product Specification

PE4270

SPST CATV UltraCMOS™ Switch
1 - 3000 MHz

Product Description

The PE4270 is a is a high-isolation switch designed for CATV applications, covering a broad frequency range from 1 to 3000 MHz. This single-supply SPST switch offers a single-pin CMOS control interface with industry leading CTB performance. It also provides low insertion loss, high isolation and extremely low bias requirements while operating on a single 3-volt supply. In a typical CATV application, the PE4270 provides for a cost effective and manufacturable solution vs. mechanical relays.

The PE4270 is manufactured on Peregrine's UltraCMOS™ process, a patented variation of silicon-on-insulator (SOI) technology on a sapphire substrate, offering the performance of GaAs with the economy and integration of conventional CMOS.

Features

- Integrated 0.25 watt terminations
- CTB performance of 90 dBc
- High isolation: 90 dB at 5 MHz, 63 dB at 1000 MHz
- Low insertion loss: 0.5 dB at 50 MHz, 0.70 dB at 1000 MHz
- High input IP2: >80 dBm
- CMOS/TTL single-pin control
- Single +3-volt supply operation
- Extremely low bias: 8 µA @ 3 V
- Available in a 6-lead DFN package

Figure 1. Functional Diagram

Peregrine Specification 71/0010

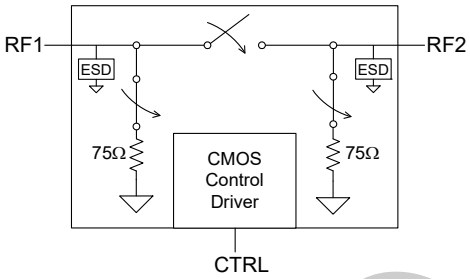


Figure 2. Package Type

6-lead DFN

Table 1. Electrical Specifications @ +25 °C ($Z_S = Z_L = 75\ \Omega$)

Parameter	Condition	Minimum	Typical	Maximum	Units
Operating Frequency [1]		1		3000	MHz
Insertion Loss	1 – 50 MHz 1000 MHz		0.50 0.70	0.65 0.85	dB
Isolation	1 – 50 MHz 1000 MHz	85 60	90 63		dB
Return Loss	5 - 1000 MHz, V_{CTRL} = 3.0V	15	16		dB
1 dB Compression [2,4]	1000 MHz	28	30		dBm
CTB / CSO	77 & 110 channels; PO = 44 dBmV		-90		dBc
Input IP2 [2]	1000 MHz	80			dBm
Input IP3 [2]	1000 MHz	50			dBm
Video Feedthrough [3]				15	mV_{pp}
Switching Time			2		µS

Notes: 1. Device linearity will begin to degrade below 1 MHz.
2. Measured in a 50 Ω system.
3. Measured with a 1 ns risetime, 0/3 V pulse and 500 MHz bandwidth.

Figure 3. Pin Configuration

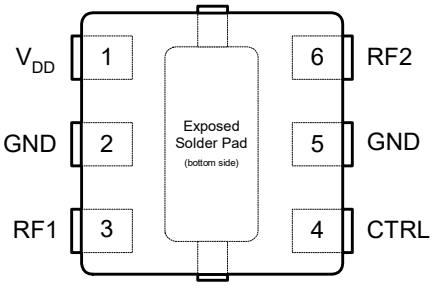


Table 2. Pin Descriptions

Pin No.	Pin Name	Description
1	V_{DD}	Nominal 3 V supply connection.
2	GND	Ground connection. [2]
3	RF1	RF port. [1]
4	CTRL	CMOS or TTL logic level: High = RF1 to RF2 signal path Low = RF1 isolated from RF2
5	GND	Ground connection. [3]
6	RF2	RF port. [1]

Notes: 1. Both RF pins must be held at 0 V_{DC} or require external DC blocking capacitors
2. The exposed pad must be soldered to the ground plane for proper switch performance.

Table 3. Absolute Maximum Ratings

Symbol	Parameter/Condition	Min	Max	Unit
V_{DD}	Power supply voltage	-0.3	4.0	V
V_I	Voltage on CTRL input	-0.3	5.5	V
T_{ST}	Storage temperature	-65	150	°C
P_{IN}	Input power (50 Ω), CTRL=1/CTRL=0		33/24	dBm
V_{ESD}	ESD voltage (Human Body Model)		500	V

Exceeding absolute maximum ratings may cause permanent damage. Operation should be restricted to the limits in the Operating Ranges table. Operation between operating range maximum and absolute maximum for extended

Moisture Sensitivity Level

The Moisture Sensitivity Level rating for the PE4270 in the 6-lead 3x3 DFN package is MSL1.

Table 4. Operating Ranges

Parameter	Min	Typ	Max	Unit
V_{DD} Power Supply	2.7	3.0	3.3	V
I_{DD} Power Supply Current		8	20	µA
T_{OP} Operating temperature	-40		85	°C
Control Voltage High	0.7xV_{DD}		5	V
Control Voltage Low	0		0.3xV_{DD}	V

Table 5. Control Logic Truth Table

Control Voltage (CTRL)	Signal Path (RF1 to RF2)
High[1]	ON
Low	OFF

Notes: 1. CTRL accepts both CMOS and TTL voltage leads.

Electrostatic Discharge (ESD) Precautions

When handling this UltraCMOS™ device, observe the same precautions that you would use with other ESD-sensitive devices. Although this device contains circuitry to protect it from damage due to ESD, precautions should be taken to avoid exceeding the rating specified in Table 3.

Latch-Up Avoidance

Unlike conventional CMOS devices, UltraCMOS™ devices are immune to latch-up.

Device Description

The *PE4270* high isolation SPST CATV switch is designed to support CATV applications such as premise disconnect of a CATV signal path. This function is typically performed by bulky and expensive mechanical relays. The high isolation characteristics, high compression point, and integrated 75-ohm terminations make the *PE4270* an ideal, cost effective and manufacturable product of choice.

The control logic input pin (CTRL) is typically driven by a 3-volt CMOS logic level signal, and has a threshold of 50% of V_{DD}. For flexibility to support systems that have 5-volt control logic drivers, the control logic input has been designed to handle a 5-volt logic HIGH signal. (A minimal current will be sourced out of the V_{DD} pin when the

Typical Performance Data @ -40 °C to 85 °C (Unless Otherwise Noted)
(75 Ω impedance except as indicated)

Figure 4. Insertion Loss - RF1 to RF2

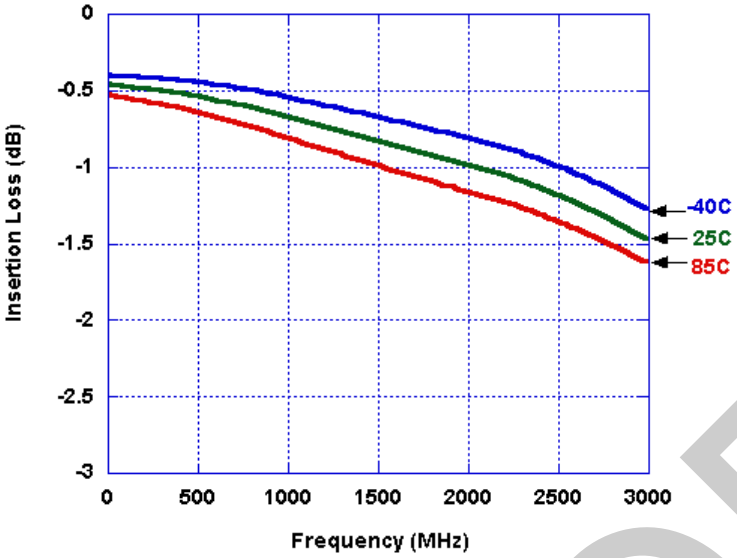


Figure 5. 1dB Compression & 3rd Order Intercept Point (T = 25°C)

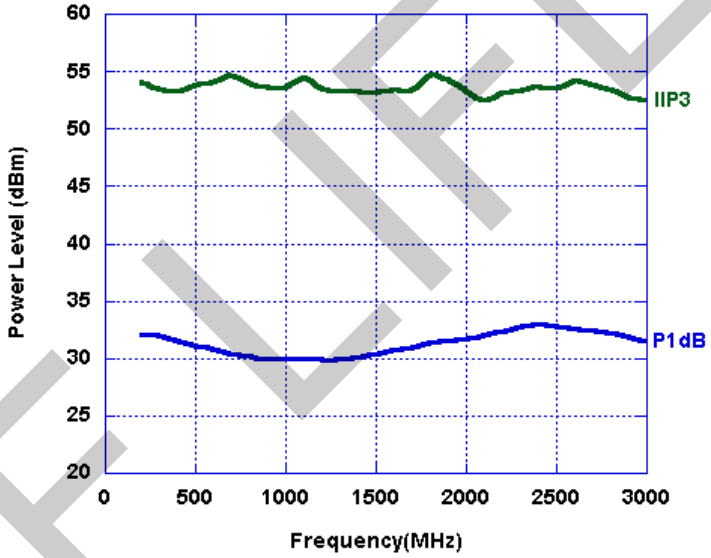
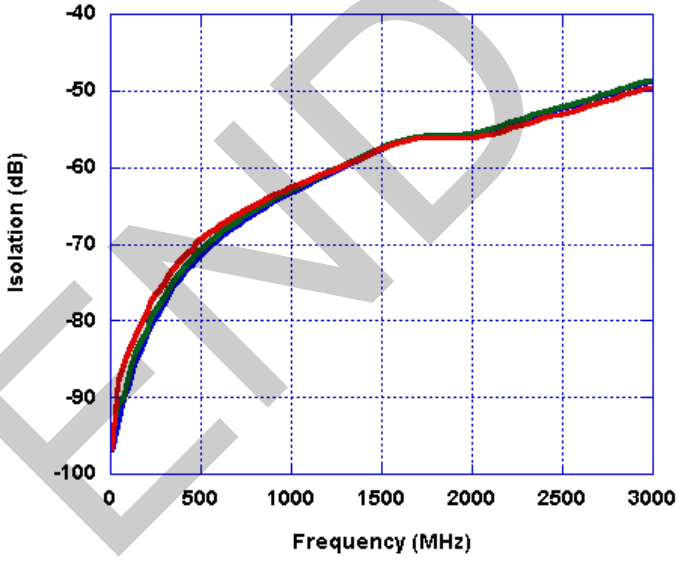


Figure 6. Isolation - RF1 to RF2



Typical Performance Data @ -40 °C to 85 °C (Unless Otherwise Noted)
(75-ohm impedance)

Figure 7. RF1 Return Loss (Switch = ON)

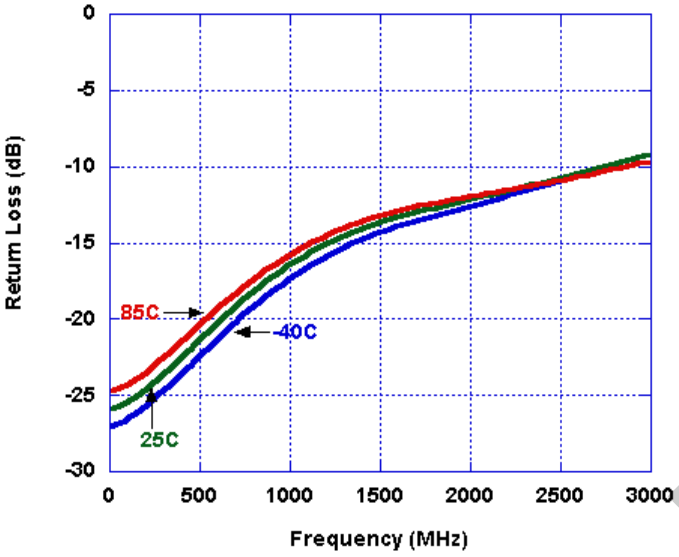


Figure 8. RF1 Return Loss (Switch = OFF)

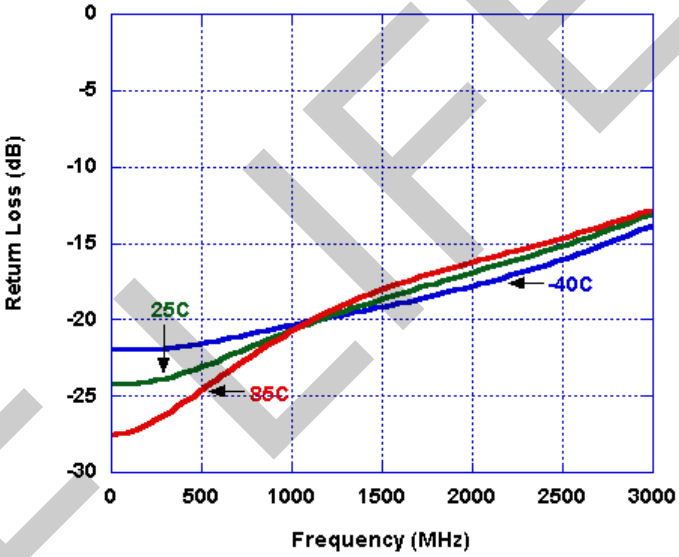


Figure 9. RF2 Return Loss (Switch = ON)

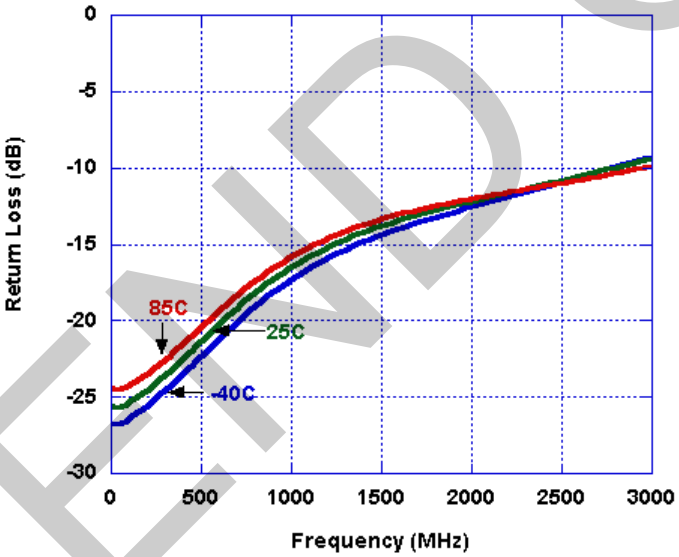
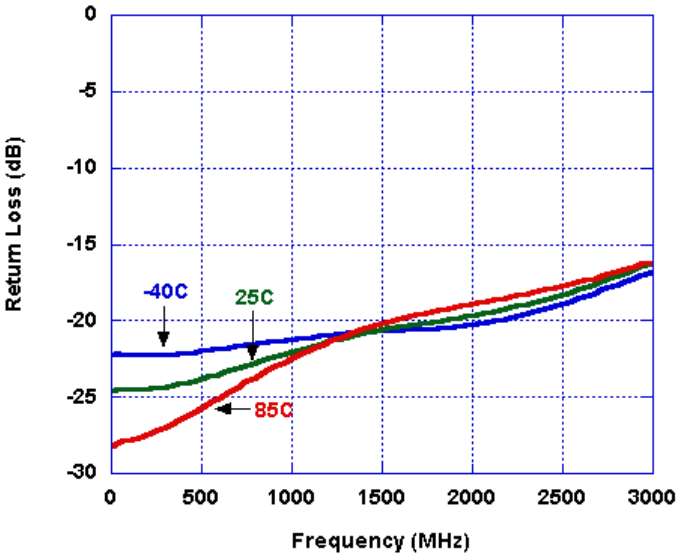


Figure 10. RF2 Return Loss (Switch = OFF)



Evaluation Kit

The *PE4270* EK board was designed to ease customer evaluation of Peregrine's high performance SPST CATV MOSFET switch. RF1 is connected through a 75 Ω transmission line via the top left F connector, J1. RF2 is connected through a 75 Ω transmission line via the top right F connector, J2. A 75 Ω through transmission line is available via F connectors J3 and J4. This transmission line can be used to estimate the loss of the PCB over the environmental conditions being evaluated. V_{DD} is supplied via J6-2, while the control logic voltage is supplied via J5-2. It is the responsibility of the customer to determine proper supply decoupling for their design application. It has been observed that by removing C1 and C2 from the evaluation board has not shown to degrade RF performance.

The board is constructed of a two metal layer FR4 material with a total thickness of 0.031". The bottom layer provides ground for the RF transmission lines. The transmission lines were designed using a coplanar waveguide model with trace width of 0.021", trace gaps of 0.030", dielectric thickness of 0.028", metal thickness of 0.0021" and ε_r of 4.6. Note that the predominate mode for these transmission lines is coplanar waveguide with a ground plane.

Figure 11. Evaluation Board Layouts
Peregrine Specification 101/0167 (with F connectors)

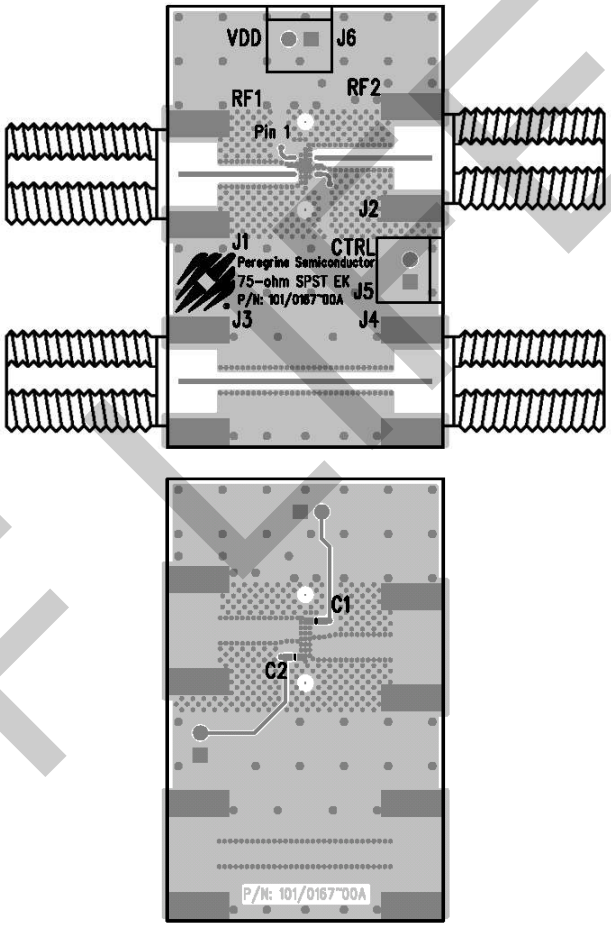


Figure 12. Evaluation Board Schematic
Peregrine Specification 102/0224

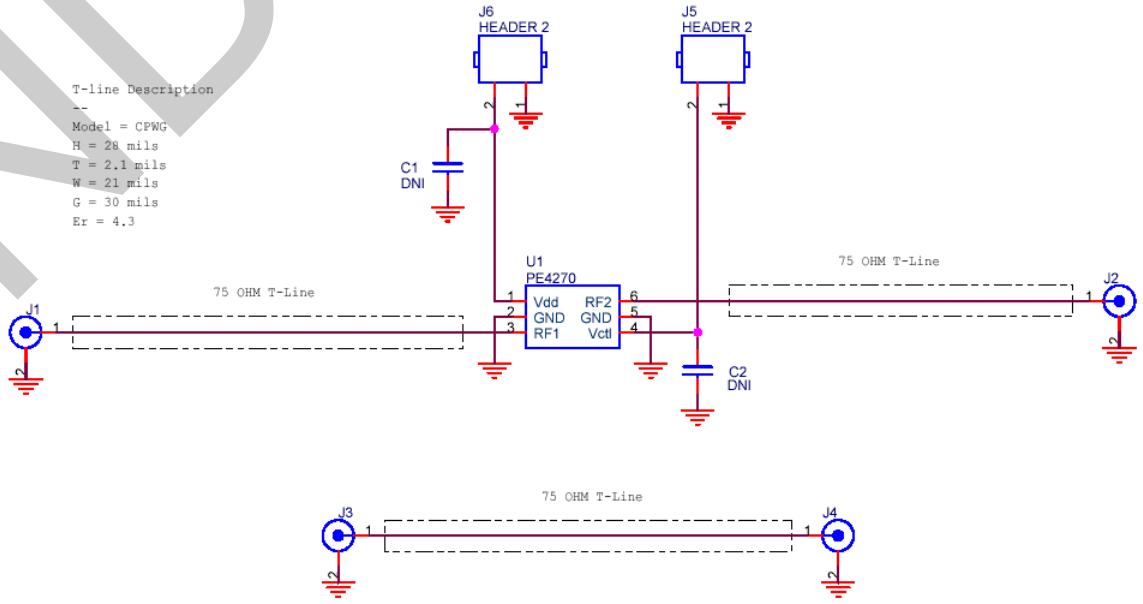
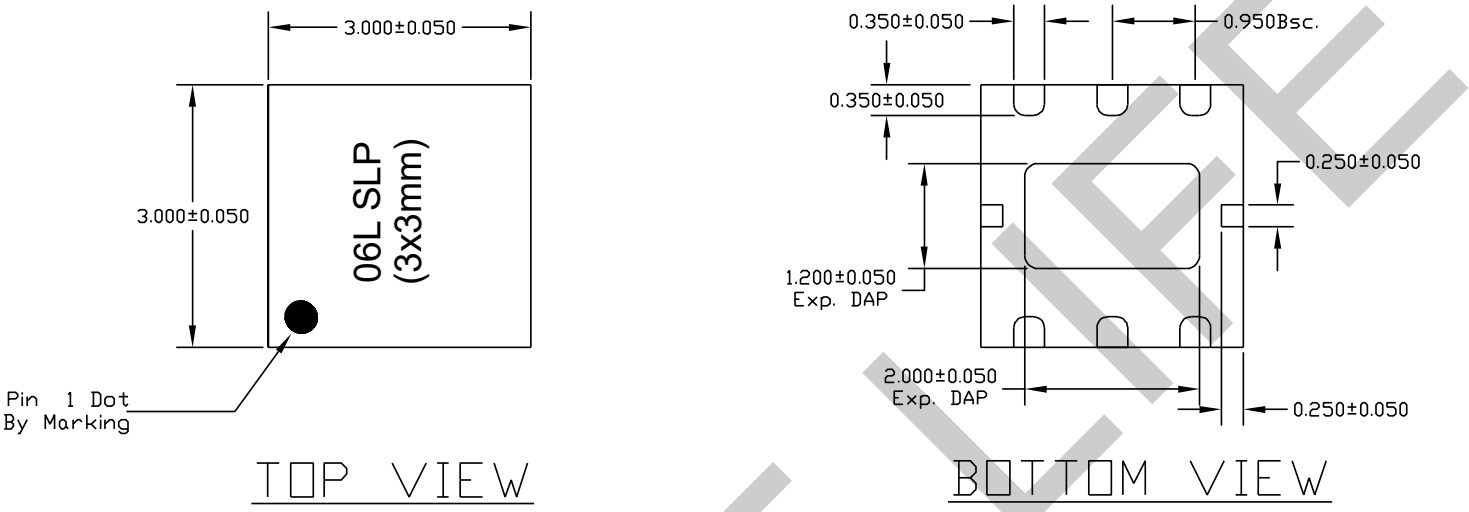


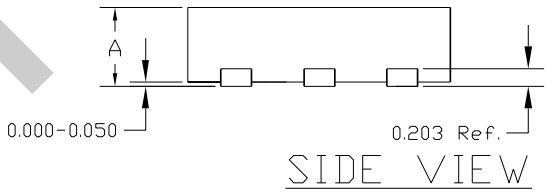
Figure 13. Package Drawing

6-lead DFN



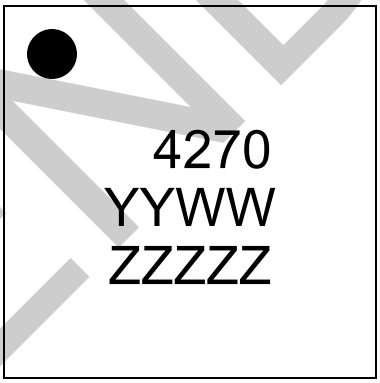
NOTE:
1) TSLP AND SLP SHARE THE SAME EXPOSE OUTLINE
BUT WITH DIFFERENT THICKNESS:

		TSLP	SLP
A	MAX.	0.800	0.900
	NOM.	0.750	0.850
	MIN.	0.700	0.800



NOTE: The exposed solder pad (on the bottom of the package) is not electrically connected to any other pin (isolated).

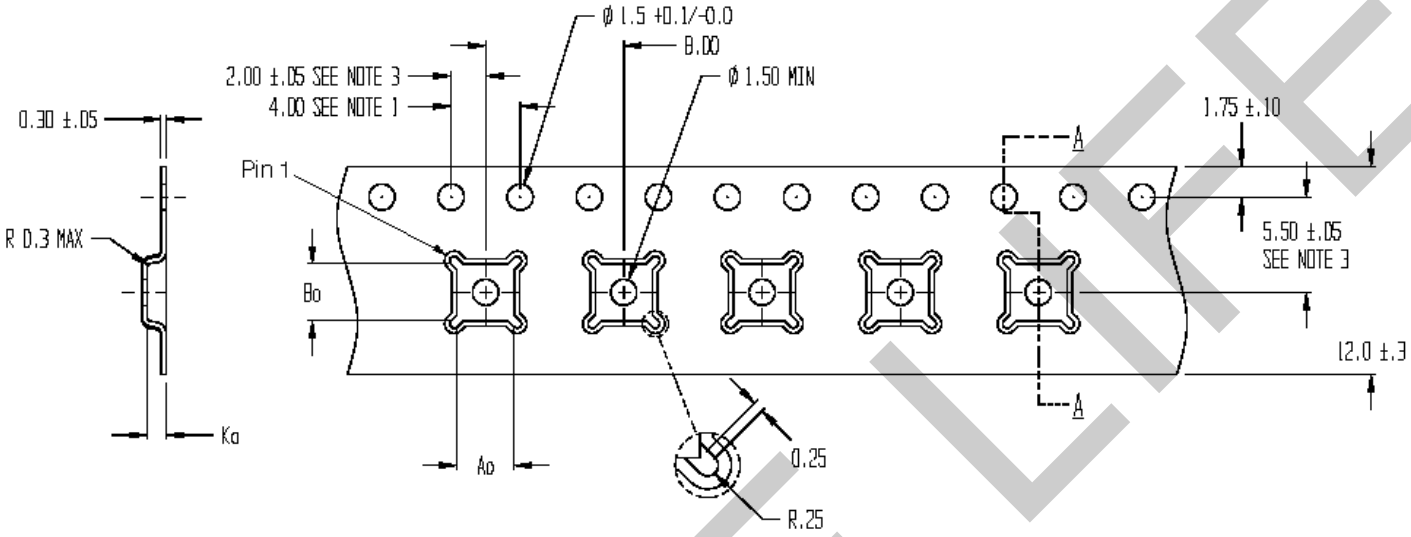
Figure 14. Marking Specifications

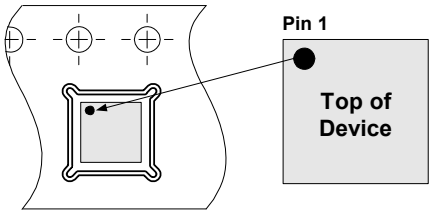


YYWW = Date Code (last two digits of year and work week)
ZZZZZ = Last five digits of Lot Number

Figure 15. Tape and Reel Specifications

6-lead DFN





Device Orientation in Tape

NOTES:
1. 10 SPROCKET HOLE PITCH CUMULATIVE TOLERANCE ±0.2
2. CAMBER IN COMPLIANCE WITH EIA 481
3. POCKET POSITION RELATIVE TO SPROCKET HOLE MEASURED AS TRUE POSITION OF POCKET, NOT POCKET HOLE

Table 6. Dimensions

Dimension	DFN 3x3 mm
Ao	3.23 ± 0.1
Bo	3.17 ± 0.1
Ko	1.37 ± 0.1
P	4 ± 0.1
W	8 +0.3, -0.1
T	0.254 ± 0.02
R7 Quantity	3000
R13 Quantity	N.A.

Note: R7 = 7 inch Lock Reel, R13 = 13 inch Lock Reel

Table 7. Ordering Information

Order Code	Part Marking	Description	Package	Shipping Method
4270-51	4270	PE4270G-06DFN 3x3mm-12800F	Green 6-lead 3x3 mm DFN	Tape or loose
4270-52	4270	PE4270G-06DFN 3x3mm-3000C	Green 6-lead 3x3 mm DFN	3000 units / T&R
4270-00	PE4270-EK	PE4270-06DFN 3x3mm-EK	Evaluation Kit	1 / Box

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Data Sheet Identification

Advance Information

The product is in a formative or design stage. The data sheet contains design target specifications for product development. Specifications and features may change in any manner without notice.

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Product Specification

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