

Versal™ Premium Series VPK120 Evaluation Kit

OVERVIEW

The VPK120 Evaluation Kit features the Versal Premium series VP1202 device, offering networked, power-optimized cores, including 600G Ethernet cores, 400G High-Speed Crypto Engines, and 100G multirate Ethernet cores.

The kit comes with a breadth of connectivity options, development tools, and example designs to accelerate prototyping the most demanding compute and networking systems across multiple markets.

HIGHLIGHTS

Versal Premium Series Capabilities

- > High I/O bandwidth and logic density
- > PCIe® Gen5 with built-in DMA & CCIX
- > 600G Ethernet cores
- > 100G multirate Ethernet cores
- > 400G High-Speed Cryptographic (HSC) engines

Breadth of Onboard Connectivity Options

- > PCIe Gen5x8 edge connector
- > QSFP-DD ports supporting up to 112Gb/s over copper and optical
- > FPGA Mezzanine Card (FMC+) interface for I/O expansion
- > 1GbE USB3.0 MicroSD card reader
- > 12GB of onboard LPDDR4 memory

Development Tools and Enhanced Debug Methodology

- > Co-optimized with Vivado™ ML Design Suite and the Vitis™ unified software platform
- > Shipped with pre-built boot images



TARGET APPLICATIONS

Wired Communication

- > Metro Core Transport
- > Data Center Interconnect
- > Security Appliances

Data Center

- > Search
- > Recommendation
- > Video Analytics

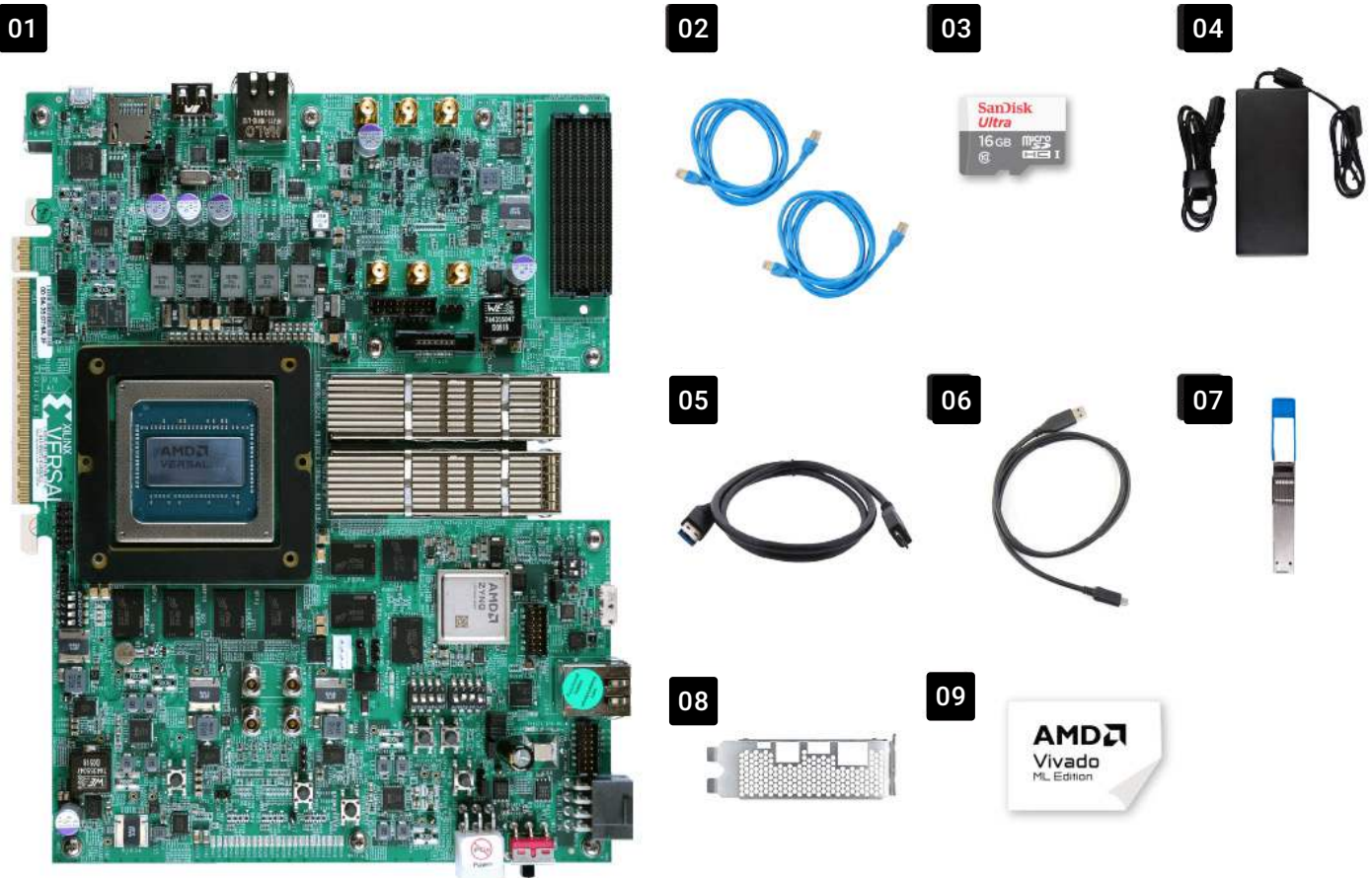
Test and Measurement

- > Network Testers
- > PCIe Protocol Analyzers

Aerospace and Defense

- > Avionics
- > Secure Communications

KIT CONTENTS



- | | | | |
|----|-------------------------|----|---------------------------------------|
| 01 | VPK120 Evaluation Board | 06 | Micro USB Cable |
| 02 | 2 Ethernet Cables | 07 | QSFP-DD Loopback Module |
| 03 | MicroSD Card | 08 | PCIe® Bracket |
| 04 | 180W (12V) Power Supply | 09 | Vivado™ ML Enterprise Edition Voucher |
| 05 | USB 3.0 Cable | | |

TAKE THE NEXT STEP

For more information, documents, and reference designs, or to purchase, visit xilinx.com/vpk120

DISCLAIMERS

(The information contained herein is for informational purposes only and is subject to change without notice. While every precaution has been taken in the preparation of this document, it may contain technical inaccuracies, omissions and typographical errors, and AMD is under no obligation to update or otherwise correct this information. Advanced Micro Devices, Inc. makes no representations or warranties with respect to the accuracy or completeness of the contents of this document, and assumes no liability of any kind, including the implied warranties of noninfringement, merchantability or fitness for purposes, with respect to the operation or use of AMD hardware, software or other products described herein. No license, including implied or arising by estoppel, to any intellectual property rights is granted by this document. Terms and limitations applicable to the purchase or use of AMD's products are as set forth in a signed agreement between the parties or in AMD's Standard Terms and Conditions of Sale.

COPYRIGHT NOTICE

© Copyright 2023 Advanced Micro Devices, Inc. All rights reserved. Xilinx, the Xilinx logo, AMD, the AMD Arrow logo, Alveo, Artix, Kintex, Kria, Spartan, Versal, Vitis, Virtex, Vivado, Zynq, and other designated brands included herein are trademarks of Advanced Micro Devices, Inc. Other product names used in this publication are for identification purposes only and may be trademarks of their respective companies. AMBA, AMBA Designer, ARM, ARM1176JZ-S, CoreSight, Cortex, and PrimeCell are trademarks of ARM in the EU and other countries. PCIe, and PCI Express are trademarks of PCI-SIG and used under license. PID1806901