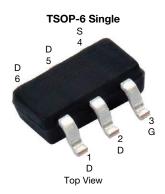


Vishay Siliconix

Automotive N-Channel 30 V (D-S) 175 °C MOSFET



PRODUCT SUMMARY				
V _{DS} (V)	30			
$R_{DS(on)}(\Omega)$ at $V_{GS} = 10 \text{ V}$	0.0175			
$R_{DS(on)}(\Omega)$ at $V_{GS} = 4.5 \text{ V}$	0.0213			
I _D (A)	8			
Configuration	Single			

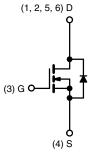
FEATURES

- TrenchFET® Power MOSFET
- AEC-Q101 Qualifiedd
- 100 % R_a and UIS Tested
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>









N-Channel MOSFET

ORDERING INFORMATION	
Package	TSOP-6
Lead (Pb)-free and halogen-free	SQ3410EV-T1 (for detailed order number please see www.vishay.com/doc?79771)

ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C, unless otherwise noted)						
PARAMETER	SYMBOL	LIMIT	UNIT			
Drain-Source Voltage	V_{DS}	30				
Gate-Source Voltage	V_{GS}	± 20	V			
Continuous Drain Current	T _C = 25 °C ^a	1	8			
Continuous Drain Current	T _C = 125 °C	I _D	6.8			
Continuous Source Current (Diode Conduction)	Is	6.3	Α			
Pulsed Drain Current ^b	I _{DM}	32				
Single Pulse Avalanche Current	L = 0.1 mH	I _{AS}	22			
Single Pulse Avalanche Energy	L = 0.1 IIII	E _{AS}	24	mJ		
Maximum Power Dissipation ^b	T _C = 25 °C	В	5	W		
Maximum Fower Dissipation	T _C = 125 °C	P_D	1.6	VV		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to + 175	°C		

THERMAL RESISTANCE RATINGS						
PARAMETER		SYMBOL	LIMIT	UNIT		
Junction-to-Ambient	PCB Mount ^c	R_{thJA}	110	°C/W		
Junction-to-Foot (Drain)		R _{thJF}	30	G/W		

Notes

- a. Package limited
- b. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %
- c. When mounted on 1" square PCB (FR-4 material)
- d. Parametric verification ongoing



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PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT
Static	1				·		ı
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$		30	-	-	V
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \mu A$		1.5	2.0	2.5	V
Gate-Source Leakage	I _{GSS}	V _{DS} =	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$		-	± 100	nA
		V _{GS} = 0 V	V _{DS} = 30 V	-	-	1	
Zero Gate Voltage Drain Current	I _{DSS}	V _{GS} = 0 V	V _{DS} = 30 V, T _J = 125 °C	-	-	50	μΑ
		V _{GS} = 0 V	V _{DS} = 30 V, T _J = 175 °C	-	-	150	
On-State Drain Current ^a	I _{D(on)}	V _{GS} = 10 V	V _{DS} ≥ 5 V	10	-	-	Α
		V _{GS} = 10 V	I _D = 5 A	-	0.014	0.0175	Ω
Drain Course On State Resistance	В	V _{GS} = 10 V	I _D = 5 A, T _J = 125 °C	-	-	0.030	
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = 10 V	I _D = 5 A, T _J = 175 °C	-	-	0.036	
		V _{GS} = 4.5 V	I _D = 4 A	-	0.017	0.0213	
Forward Transconductanceb	9 _{fs}	V _{DS} = 15 V, I _D = 5 A		-	25	-	S
Dynamic ^b							
Input Capacitance	C _{iss}		V _{DS} = 15 V, f = 1 MHz	-	804	1005	pF
Output Capacitance	C _{oss}	$V_{GS} = 0 V$		-	175	219	
Reverse Transfer Capacitance	C _{rss}	1		-	68	85	
Total Gate Charge ^c	Qg			-	14	21	
Gate-Source Charge ^c	Q _{gs}	V _{GS} = 10 V	$V_{DS} = 15 \text{ V}, I_{D} = 5 \text{ A}$	-	2.4	-	nC
Gate-Drain Charge ^c	Q _{gd}]		-	2.2	-	
Gate Resistance	R _g	f = 1 MHz		1.5	3.91	7	Ω
Turn-On Delay Time ^c	t _{d(on)}			-	9	14	
Rise Time ^c	t _r	$V_{DD} = 15 \text{ V, } R_L = 3 \Omega$ $I_D \cong 5 \text{ A, } V_{GEN} = 10 \text{ V, } R_g = 1 \Omega$		-	12	18	ns
Turn-Off Delay Time ^c	t _{d(off)}			-	20	30	
Fall Time ^c	t _f			-	7	11	
Source-Drain Diode Ratings and Char-	acteristics ^b						
Pulsed Current ^a	I _{SM}			=	-	32	Α
Forward Voltage	V _{SD}	I _F = 5 A, V _{GS} = 0 V		-	0.8	1.2	V

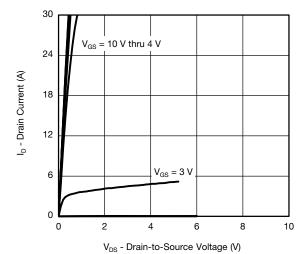
Notes

- a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

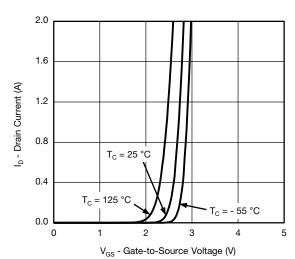


TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)

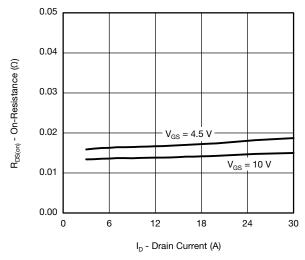


50 0 1

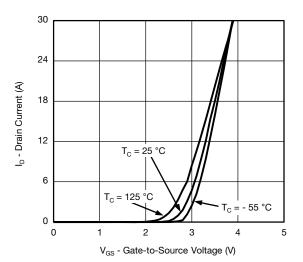




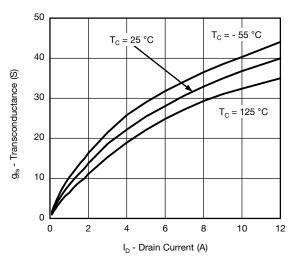
Transfer Characteristics



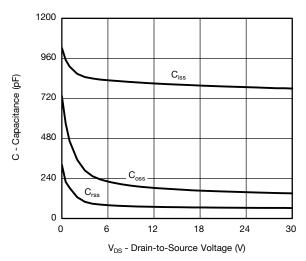
On-Resistance vs. Drain Current



Transfer Characteristics



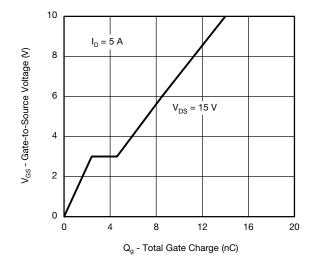
Transconductance



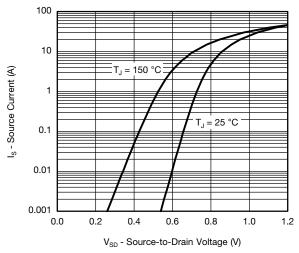
Capacitance



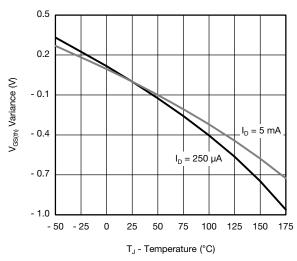
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



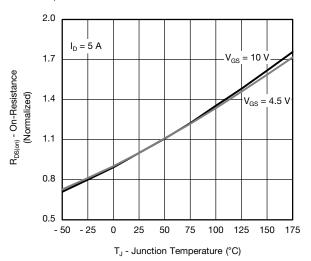
Gate Charge



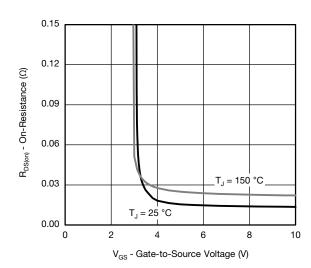
Source Drain Diode Forward Voltage



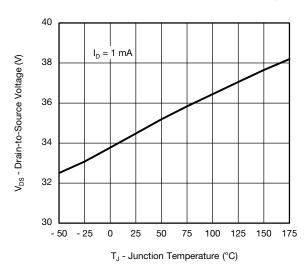
Threshold Voltage



On-Resistance vs. Junction Temperature



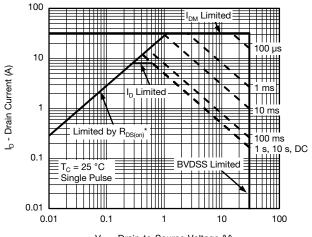
On-Resistance vs. Gate-to-Source Voltage



Drain Source Breakdown vs. Junction Temperature

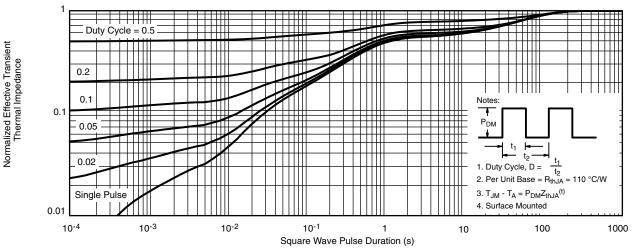


THERMAL RATINGS (T_A = 25 °C, unless otherwise noted)



 $V_{DS} \text{ - Drain-to-Source Voltage (V)} \\ ^*V_{GS} \text{ > minimum } V_{GS} \text{ at which } R_{DS(on)} \text{ is specified}$

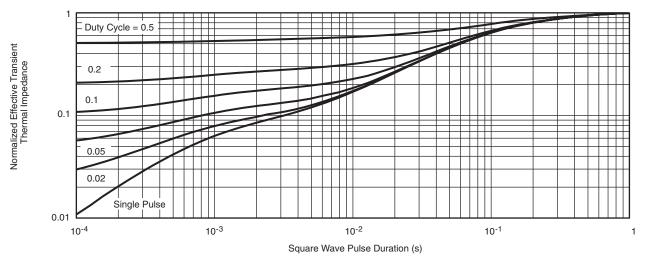
Safe Operating Area



Normalized Thermal Transient Impedance, Junction-to-Ambient



THERMAL RATINGS ($T_A = 25$ °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Foot

Note

- The characteristics shown in the two graphs
 - Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)
 - Normalized Transient Thermal Impedance Junction-to-Foot (25 °C) are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

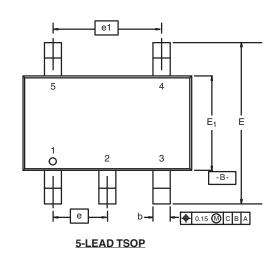
Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg267342.

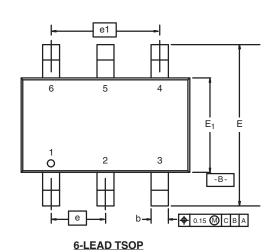


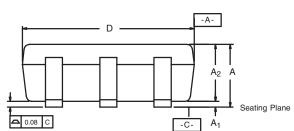


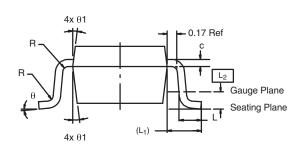
TSOP: 5/6-LEAD

JEDEC Part Number: MO-193C









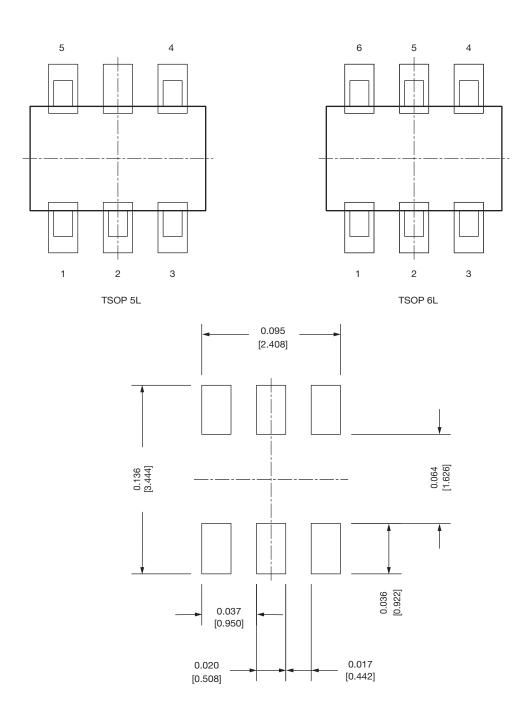
	MILLIMETERS			INCHES			
Dim	Min	Nom	Max	Min	Nom	Max	
Α	0.91	-	1.10	0.036	-	0.043	
A ₁	0.01	-	0.10	0.0004	-	0.004	
A ₂	0.90	-	1.00	0.035	0.038	0.039	
b	0.30	0.32	0.45	0.012	0.013	0.018	
С	0.10	0.15	0.20	0.004	0.006	0.008	
D	2.95	3.05	3.10	0.116 0.120		0.122	
E	2.70	2.85	2.98	0.106	0.112	0.117	
E ₁	1.55	1.65	1.70	0.061	0.065	0.067	
е		0.95 BSC		0.0374 BSC			
e ₁	1.80	1.90	2.00	0.071	0.075	0.079	
L	0.32	-	0.50	0.012	-	0.020	
L ₁		0.60 Ref			0.024 Ref		
L ₂	0.25 BSC			0.010 BSC			
R	0.10	-	-	0.004	-	-	
θ	0°	4°	8°	0°	4°	8°	
θ_1	7° Nom			7° Nom			
ECN: C-06593-Rev. I, 18-Dec-06 DWG: 5540							

Document Number: 71200

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Recommended Land Pattern For TSOP-5L / TSOP-6L



Note

• All dimensions are in inches (millimeter)

ECN: C22-0860-Rev. B, 24-Oct-2022 DWG: 3010



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