

# FDP46N30

## 300V N-Channel MOSFET

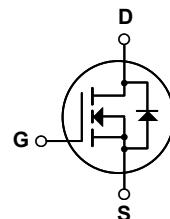
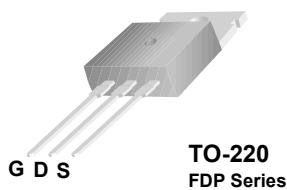
### Features

- 46A, 300V,  $R_{DS(on)} = 0.079\Omega$  @  $V_{GS} = 10$  V
- Low gate charge (typical 58 nC)
- Low  $C_{rss}$  (typical 60 pF)
- Fast switching
- 100% avalanche tested
- Improved dv/dt capability

### Description

These N-Channel enhancement mode power field effect transistors are produced using Fairchild's proprietary, planar stripe, DMOS technology.

This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficient switched mode power supplies and active power factor correction.



### Absolute Maximum Ratings

Symbol	Parameter	FDP46N30	Unit
$V_{DSS}$	Drain-Source Voltage	300	V
$I_D$	Drain Current	46	A
	- Continuous ( $T_C = 25^\circ C$ )	27.6	A
	- Continuous ( $T_C = 100^\circ C$ )		
$I_{DM}$	Drain Current	184	A
$V_{GSS}$	Gate-Source voltage	$\pm 30$	V
$E_{AS}$	Single Pulsed Avalanche Energy	1205	mJ
$I_{AR}$	Avalanche Current	46	A
$E_{AR}$	Repetitive Avalanche Energy	41.7	mJ
$dv/dt$	Peak Diode Recovery $dv/dt$	4.5	V/ns
$P_D$	Power Dissipation ( $T_C = 25^\circ C$ )	417	W
	- Derate above $25^\circ C$	3.3	$W/^\circ C$
$T_J, T_{STG}$	Operating and Storage Temperature Range	-55 to +150	$^\circ C$
$T_L$	Maximum Lead Temperature for Soldering Purpose, 1/8" from Case for 5 Seconds	300	$^\circ C$

### Thermal Characteristics

Symbol	Parameter	Min.	Max.	Unit
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case	--	0.30	$^\circ C/W$
$R_{\theta CS}$	Thermal Resistance, Case-to-Sink	0.5	--	$^\circ C/W$
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	--	62.5	$^\circ C/W$

## Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FDP46N30	FDP46N30	TO-220	-	-	50

## Electrical Characteristics

$T_C = 25^\circ\text{C}$  unless otherwise noted

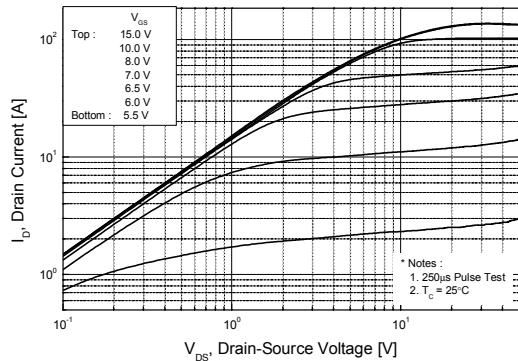
Symbol	Parameter	Conditions	Min.	Typ.	Max	Units
<b>Off Characteristics</b>						
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}} = 0\text{V}$ , $I_D = 250\mu\text{A}$	300	--	--	V
$\Delta \text{BV}_{\text{DSS}} / \Delta T_J$	Breakdown Voltage Temperature Coefficient	$I_D = 250\mu\text{A}$ , Referenced to $25^\circ\text{C}$	--	0.3	--	$\text{V}/^\circ\text{C}$
$I_{\text{DSS}}$	Zero Gate Voltage Drain Current	$V_{\text{DS}} = 300\text{V}$ , $V_{\text{GS}} = 0\text{V}$ $V_{\text{DS}} = 240\text{V}$ , $T_C = 125^\circ\text{C}$	-- --	-- 10	1 10	$\mu\text{A}$ $\mu\text{A}$
$I_{\text{GSSF}}$	Gate-Body Leakage Current, Forward	$V_{\text{GS}} = 30\text{V}$ , $V_{\text{DS}} = 0\text{V}$	--	--	100	nA
$I_{\text{GSSR}}$	Gate-Body Leakage Current, Reverse	$V_{\text{GS}} = -30\text{V}$ , $V_{\text{DS}} = 0\text{V}$	--	--	-100	nA
<b>On Characteristics</b>						
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{DS}} = V_{\text{GS}}$ , $I_D = 250\mu\text{A}$	3.0	--	5.0	V
$R_{\text{DS(on)}}$	Static Drain-Source On-Resistance	$V_{\text{GS}} = 10\text{V}$ , $I_D = 23\text{A}$	--	0.067	0.079	$\Omega$
$g_{\text{FS}}$	Forward Transconductance	$V_{\text{DS}} = 40\text{V}$ , $I_D = 23\text{A}$	(Note 4)	--	37	--
<b>Dynamic Characteristics</b>						
$C_{\text{iss}}$	Input Capacitance	$V_{\text{DS}} = 25\text{V}$ , $V_{\text{GS}} = 0\text{V}$ , $f = 1.0\text{MHz}$	--	2600	3380	pF
$C_{\text{oss}}$	Output Capacitance		--	500	650	pF
$C_{\text{rss}}$	Reverse Transfer Capacitance		--	60	90	pF
<b>Switching Characteristics</b>						
$t_{\text{d(on)}}$	Turn-On Delay Time	$V_{\text{DD}} = 150\text{V}$ , $I_D = 46\text{A}$ $R_G = 25\Omega$	--	41	92	ns
$t_r$	Turn-On Rise Time		--	216	442	ns
$t_{\text{d(off)}}$	Turn-Off Delay Time		--	124	258	ns
$t_f$	Turn-Off Fall Time		--	171	352	ns
$Q_g$	Total Gate Charge	$V_{\text{DS}} = 240\text{V}$ , $I_D = 46\text{A}$ $V_{\text{GS}} = 10\text{V}$	--	58	75	nC
$Q_{\text{gs}}$	Gate-Source Charge		--	20	--	nC
$Q_{\text{gd}}$	Gate-Drain Charge		--	28	--	nC
<b>Drain-Source Diode Characteristics and Maximum Ratings</b>						
$I_S$	Maximum Continuous Drain-Source Diode Forward Current		--	--	46	A
$I_{\text{SM}}$	Maximum Pulsed Drain-Source Diode Forward Current		--	--	184	A
$V_{\text{SD}}$	Drain-Source Diode Forward Voltage	$V_{\text{GS}} = 0\text{V}$ , $I_S = 46\text{A}$	--	--	1.4	V
$t_{\text{rr}}$	Reverse Recovery Time	$V_{\text{GS}} = 0\text{V}$ , $I_S = 46\text{A}$ $dI_F/dt = 100\text{A}/\mu\text{s}$	--	315	--	ns
$Q_{\text{rr}}$	Reverse Recovery Charge		(Note 4)	--	3.9	$\mu\text{C}$

### NOTES:

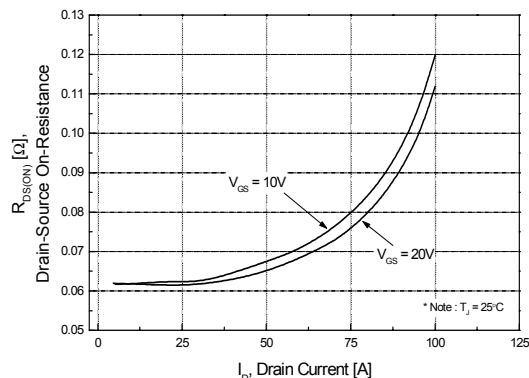
1. Repetitive Rating: Pulse width limited by maximum junction temperature
2.  $L = 0.95\text{mH}$ ,  $I_{AS} = 46\text{A}$ ,  $V_{DD} = 50\text{V}$ ,  $R_G = 25\Omega$ , Starting  $T_J = 25^\circ\text{C}$
3.  $I_{SD} \leq 46\text{A}$ ,  $di/dt \leq 200\text{A}/\mu\text{s}$ ,  $V_{DD} \leq \text{BV}_{\text{DSS}}$ , Starting  $T_J = 25^\circ\text{C}$
4. Pulse Test: Pulse width  $\leq 300\mu\text{s}$ , Duty Cycle  $\leq 2\%$
5. Essentially Independent of Operating Temperature Typical Characteristics

## Typical Performance Characteristics

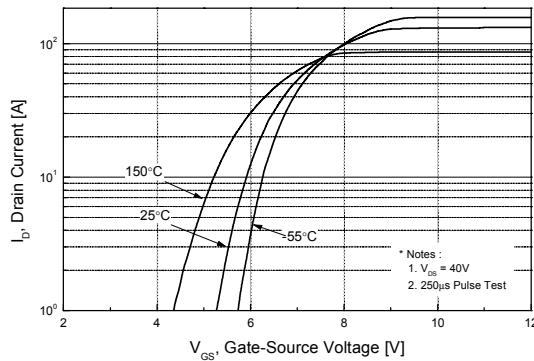
**Figure 1. On-Region Characteristics**



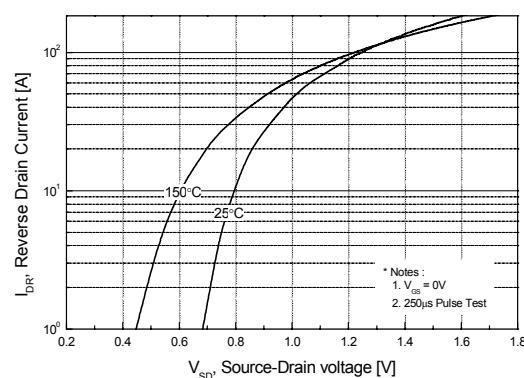
**Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage**



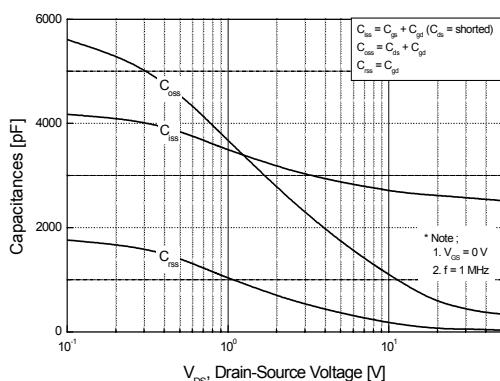
**Figure 2. Transfer Characteristics**



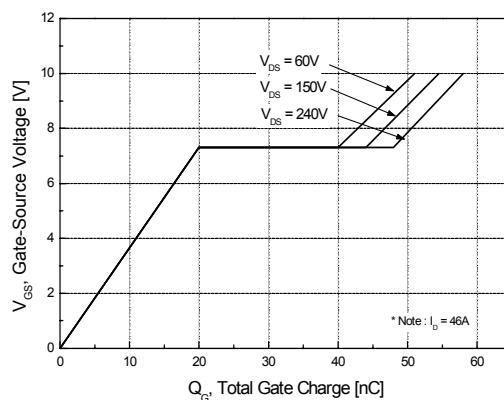
**Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature**



**Figure 5. Capacitance Characteristics**

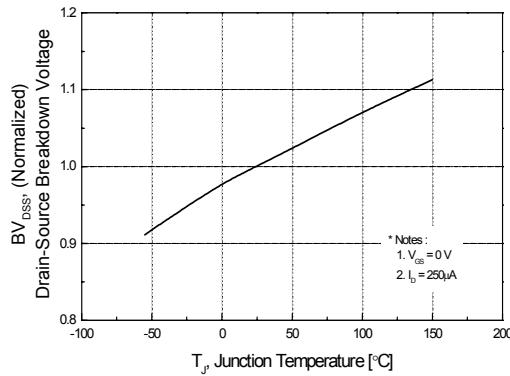


**Figure 6. Gate Charge Characteristics**

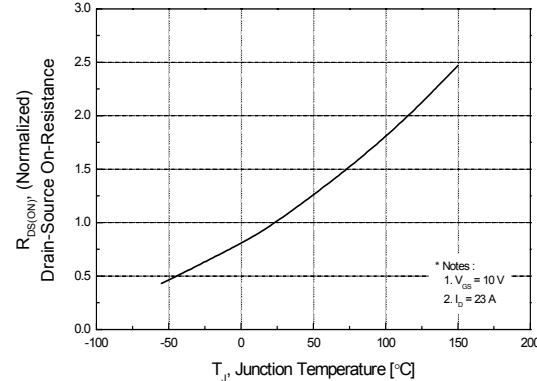


## Typical Performance Characteristics (Continued)

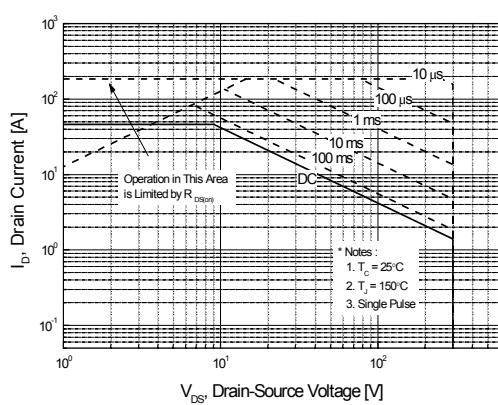
**Figure 7. Breakdown Voltage Variation vs. Temperature**



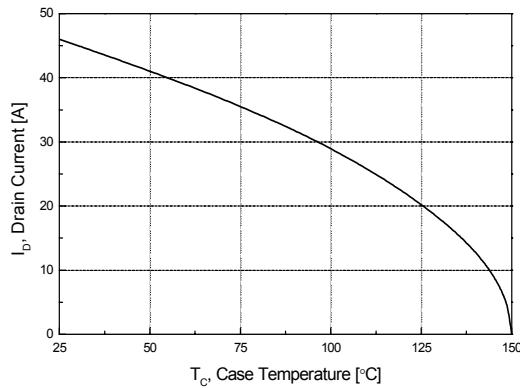
**Figure 8. On-Resistance Variation vs. Temperature**



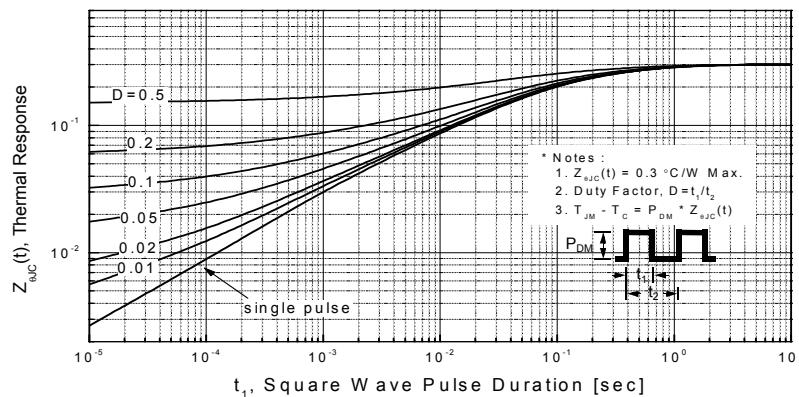
**Figure 9. Maximum Safe Operating Area**



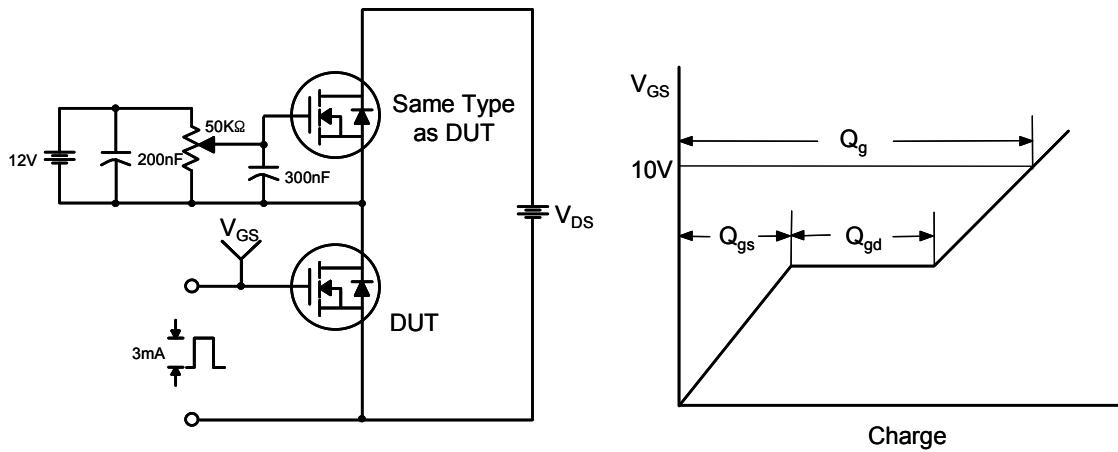
**Figure 10. Maximum Drain Current vs. Case Temperature**



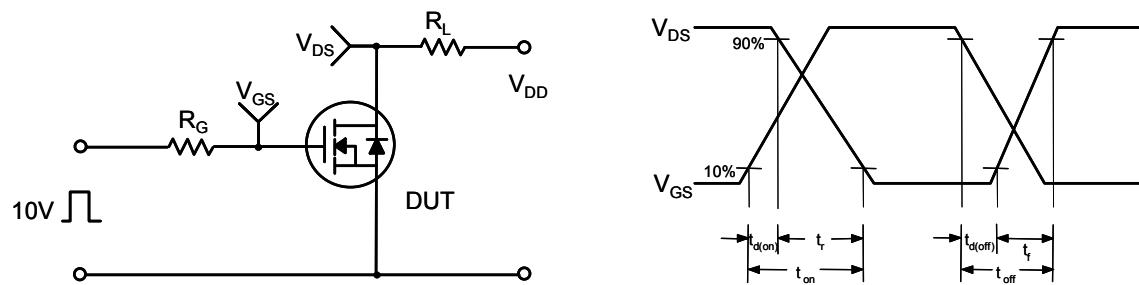
**Figure 11. Transient Thermal Response Curve**



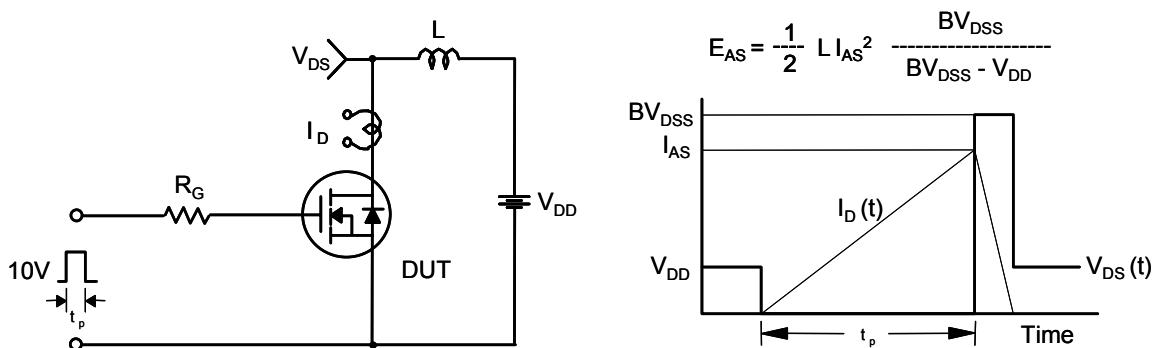
### Gate Charge Test Circuit & Waveform



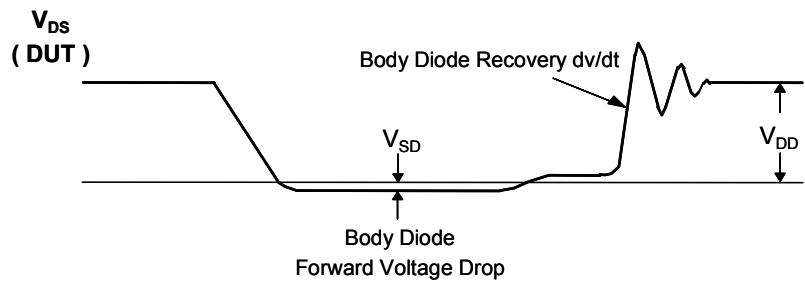
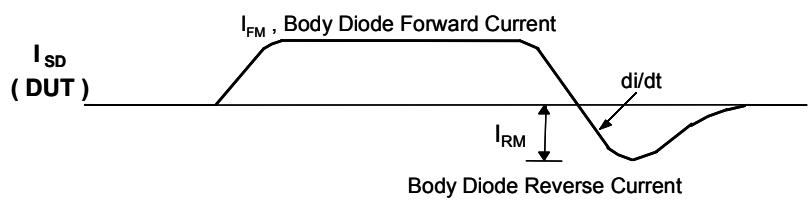
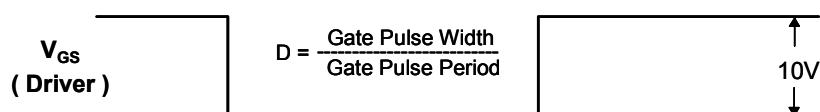
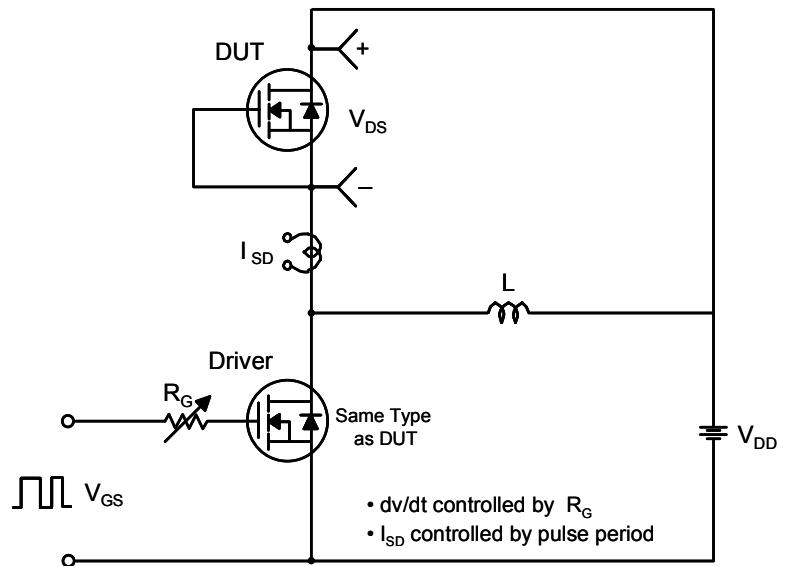
### Resistive Switching Test Circuit & Waveforms



### Unclamped Inductive Switching Test Circuit & Waveforms

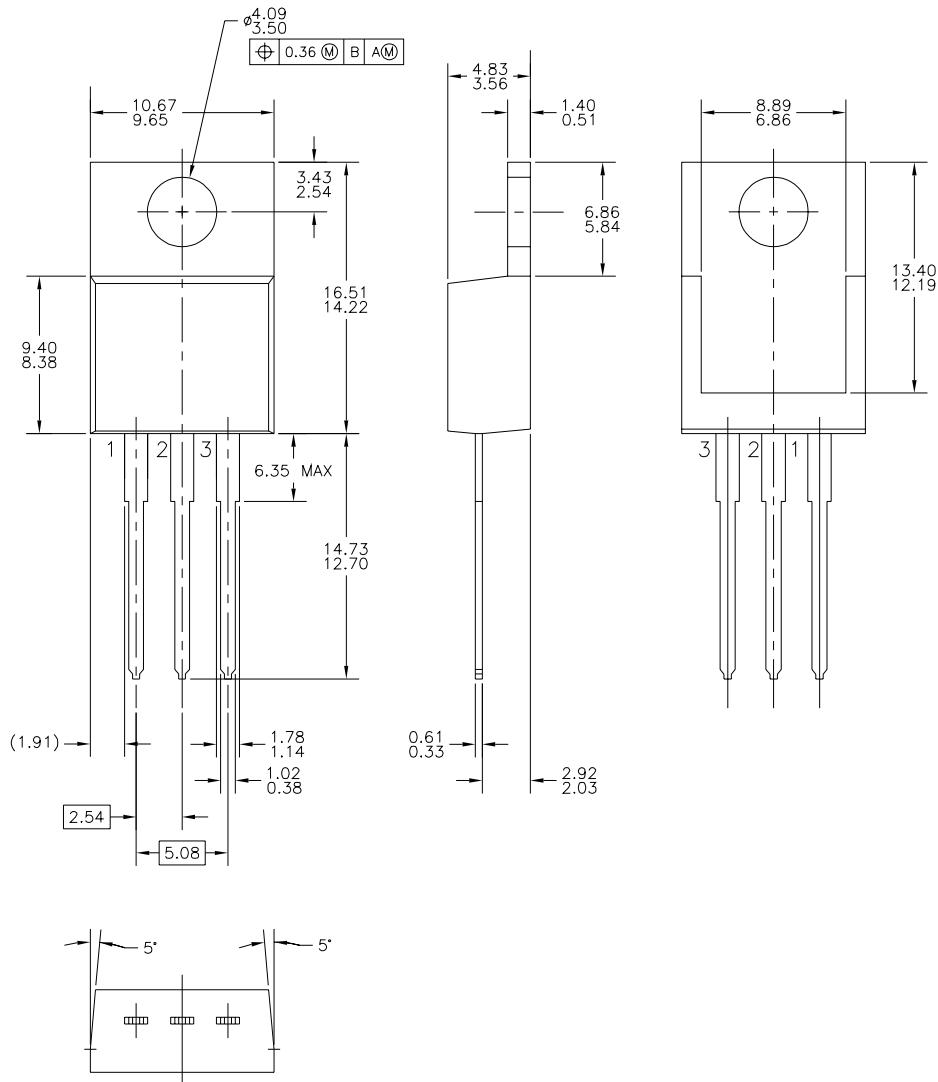


## Peak Diode Recovery dv/dt Test Circuit &amp; Waveforms



## Mechanical Dimensions

TO-220



Dimensions in Millimeters

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