

Product Summary

BV_{DSS}	$R_{DS(ON)}$ Max	I_D Max $T_A = +25^\circ C$
60V	8.5m Ω @ $V_{GS} = 10V$	12.1A
	12m Ω @ $V_{GS} = 4.5V$	10.2A

Description and Applications

This MOSFET is designed to minimize the on-state resistance ($R_{DS(ON)}$), yet maintain superior switching performance, making it ideal for high efficiency power management applications.

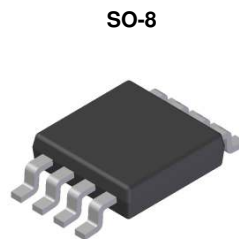
- High Frequency Switching
- Synchronous Rectification
- DC-DC Converters

Features and Benefits

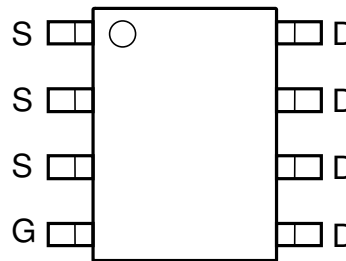
- High Conversion Efficiency
- Low $R_{DS(ON)}$ – Minimizes On-State Losses
- Low Input Capacitance
- Fast Switching Speed
- ESD Protected Gate
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**

Mechanical Data

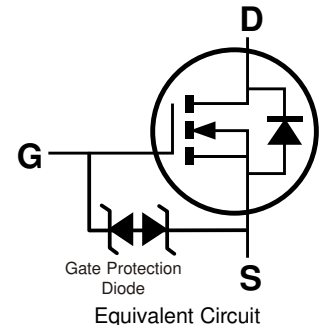
- Case: SO-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Finish - Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 $\text{\textcircled{E}}$
- Weight: 0.074 grams (Approximate)



Top View



Top View
Internal Schematic



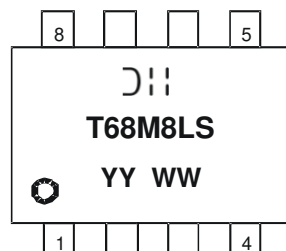
Equivalent Circuit

Ordering Information (Note 4)

Part Number	Case	Packaging
DMT68M8LSS-13	SO-8	2,500/Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

Marking Information



DII = Manufacturer's Marking
 T68M8LS = Product Type Marking Code
 YYWW = Date Code Marking
 YY or YY = Year (ex: 17 = 2017)
 WW = Week (01 to 53)

Maximum Ratings (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Drain-Source Voltage	V_{DSS}	60	V
Gate-Source Voltage	V_{GSS}	± 20	V
Continuous Drain Current (Note 6) $V_{GS} = 10\text{V}$	$T_A = +25^\circ\text{C}$	12.1	A
	$T_A = +70^\circ\text{C}$	9.7	A
	$T_C = +25^\circ\text{C}$	28.9	A
	$T_C = +70^\circ\text{C}$	9.7	A
Pulsed Drain Current (10 μs Pulse, Duty Cycle = 1%)	I_{DM}	100	A
Maximum Continuous Body Diode Forward Current (Note 6)	I_S	20	A
Pulsed Body Diode Forward Current (10 μs Pulse, Duty Cycle = 1%)	I_{SM}	100	A
Avalanche Current, $L = 0.3\text{mH}$	I_{AS}	19	A
Avalanche Energy, $L = 0.3\text{mH}$	E_{AS}	54.2	A

Thermal Characteristics (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	P_D	1.3	W
Thermal Resistance, Junction to Ambient (Note 5)	$R_{\theta JA}$	93	$^\circ\text{C/W}$
Total Power Dissipation (Note 6)	P_D	1.9	W
Thermal Resistance, Junction to Ambient (Note 6)	$R_{\theta JA}$	67	$^\circ\text{C/W}$
Thermal Resistance, Junction to Case (Note 6)	$R_{\theta JC}$	11.7	$^\circ\text{C/W}$
Operating and Storage Temperature Range	T_J, T_{STG}	-55 to +150	$^\circ\text{C}$

Electrical Characteristics ($T_A = +25^\circ\text{C}$, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV_{DSS}	60	—	—	V	$V_{GS} = 0\text{V}, I_D = 1\text{mA}$
Zero Gate Voltage Drain Current	I_{DSS}	—	—	1	μA	$V_{DS} = 48\text{V}, V_{GS} = 0\text{V}$
Gate-Source Leakage	I_{GSS}	—	—	± 10	μA	$V_{GS} = \pm 20\text{V}, V_{DS} = 0\text{V}$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	$V_{GS(TH)}$	1	—	3	V	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$
Static Drain-Source On-Resistance	$R_{DS(ON)}$	—	6.7	8.5	m Ω	$V_{GS} = 10\text{V}, I_D = 13.5\text{A}$
		—	8.9	12		$V_{GS} = 4.5\text{V}, I_D = 11.5\text{A}$
Diode Forward Voltage	V_{SD}	—	0.9	1.2	V	$V_{GS} = 0\text{V}, I_S = 20\text{A}$
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C_{iss}	—	2107	—	pF	$V_{DS} = 30\text{V}, V_{GS} = 0\text{V}, f = 1\text{MHz}$
Output Capacitance	C_{oss}	—	634	—		
Reverse Transfer Capacitance	C_{rss}	—	48	—		
Gate Resistance	R_g	—	1.8	—	Ω	$V_{DS} = 0\text{V}, V_{GS} = 0\text{V}, f = 1\text{MHz}$
Total Gate Charge ($V_{GS} = 10\text{V}$)	Q_g	—	31.8	—	nC	$V_{DD} = 30\text{V}, I_D = 20\text{A}$
Total Gate Charge ($V_{GS} = 4.5\text{V}$)	Q_g	—	15.6	—		
Gate-Source Charge	Q_{gs}	—	3.4	—		
Gate-Drain Charge	Q_{gd}	—	6.6	—		
Turn-On Delay Time	$t_{D(ON)}$	—	4.6	—	ns	$V_{DD} = 30\text{V}, V_{GS} = 10\text{V}, I_D = 20\text{A}, R_g = 3.3\Omega$
Turn-On Rise Time	t_R	—	7.9	—		
Turn-Off Delay Time	$t_{D(OFF)}$	—	25.2	—		
Turn-Off Fall Time	t_F	—	13.9	—		
Body Diode Reverse Recovery Time	t_{RR}	—	19.3	—	ns	$I_F = 15\text{A}, di/dt = 500\text{A}/\mu\text{s}$
Body Diode Reverse Recovery Charge	Q_{RR}	—	38.1	—	nC	

- Notes:
- Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
 - Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
 - Short duration pulse test used to minimize self-heating effect.
 - Guaranteed by design. Not subject to product testing.

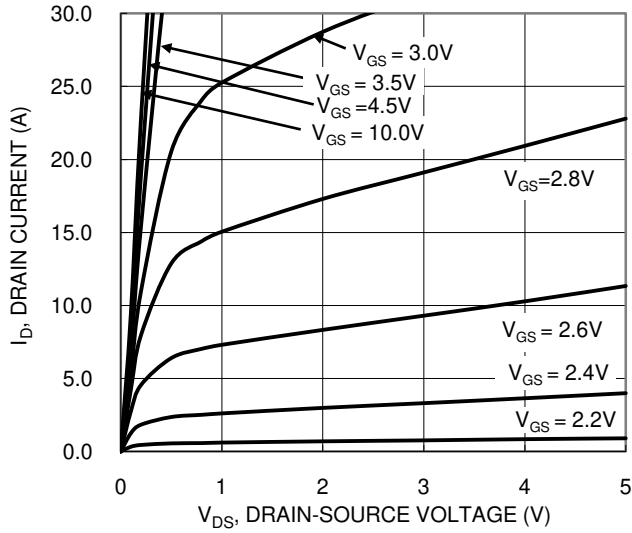


Figure 1. Typical Output Characteristic

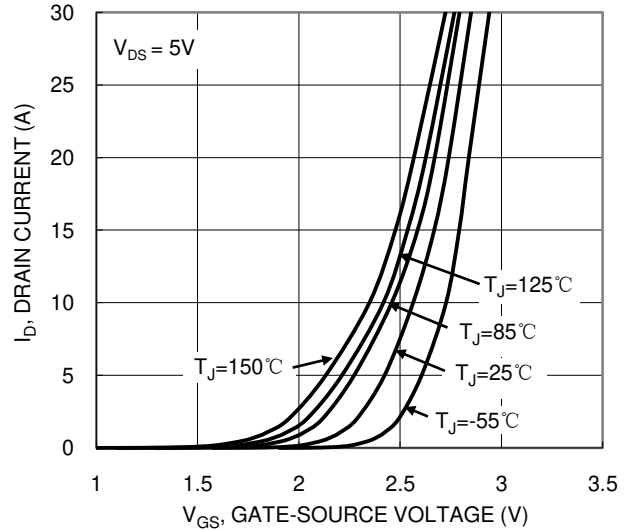


Figure 2. Typical Transfer Characteristic

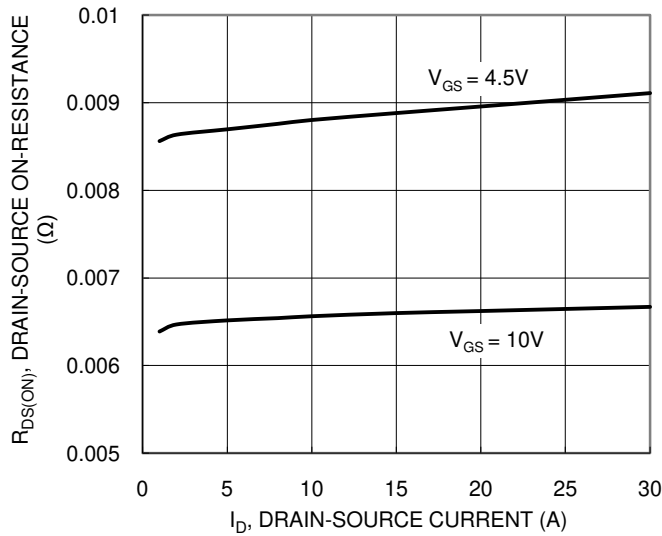


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

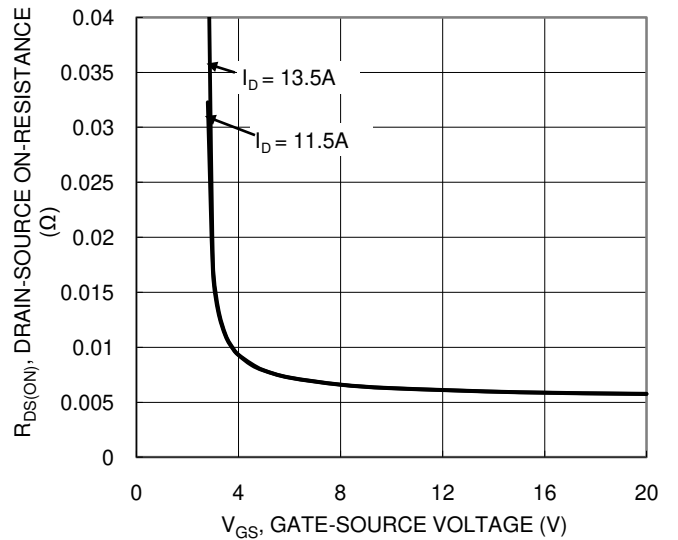


Figure 4. Typical Transfer Characteristic

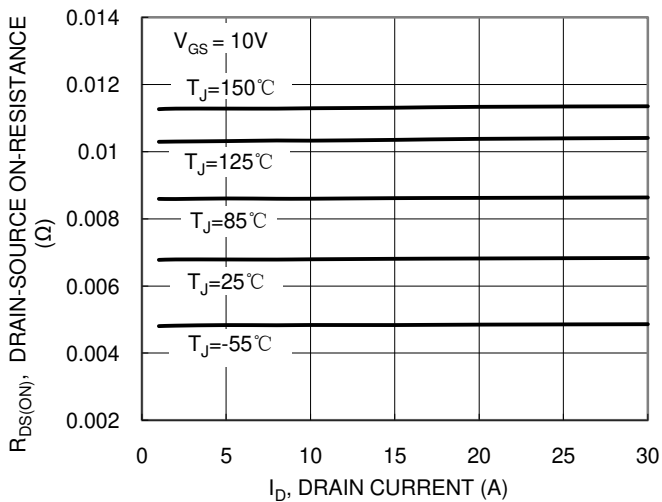


Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature

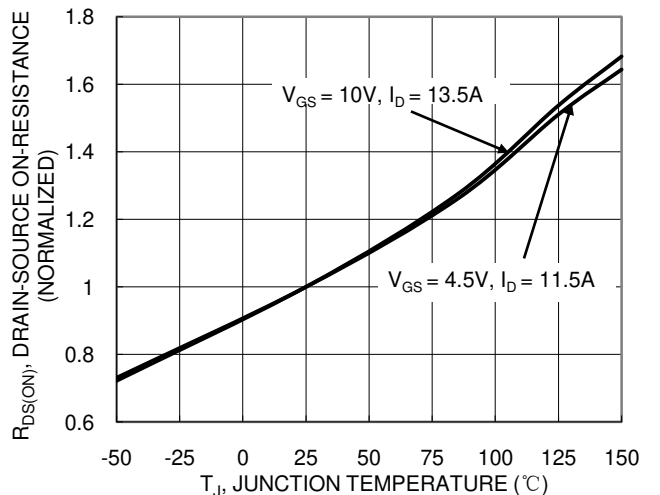


Figure 6. On-Resistance Variation with Junction Temperature

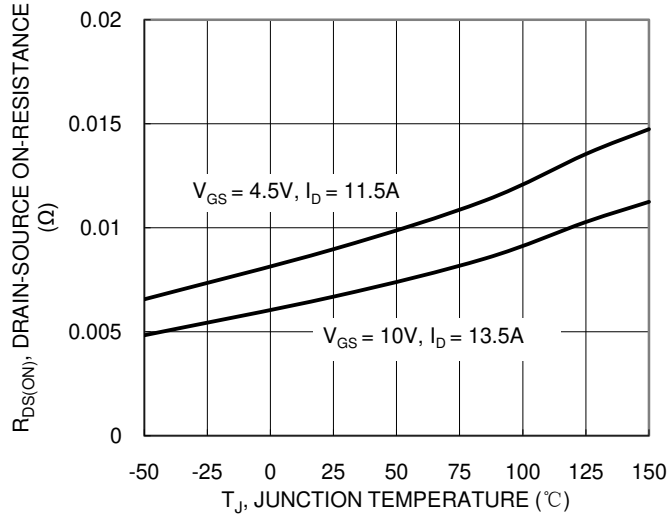


Figure 7. On-Resistance Variation with Junction Temperature

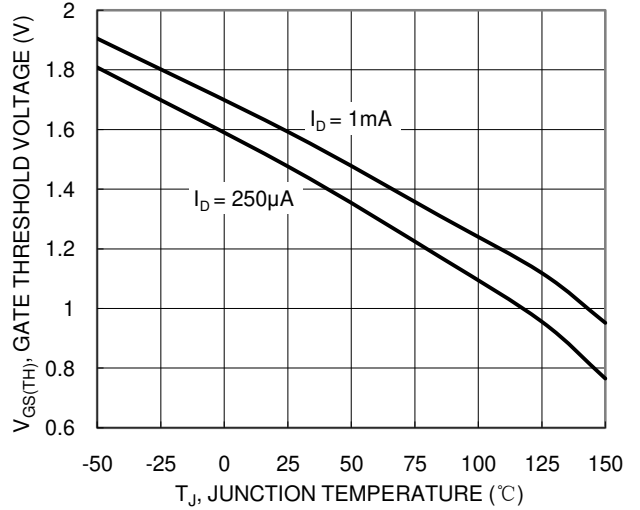


Figure 8. Gate Threshold Variation vs. Junction Temperature

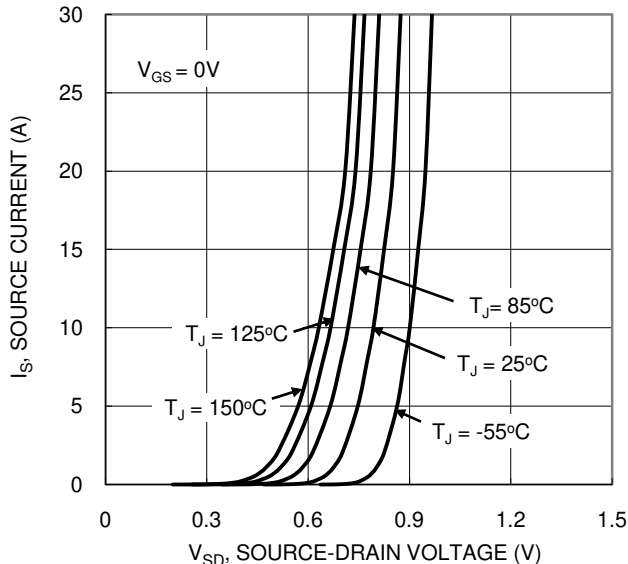


Figure 9. Diode Forward Voltage vs. Current

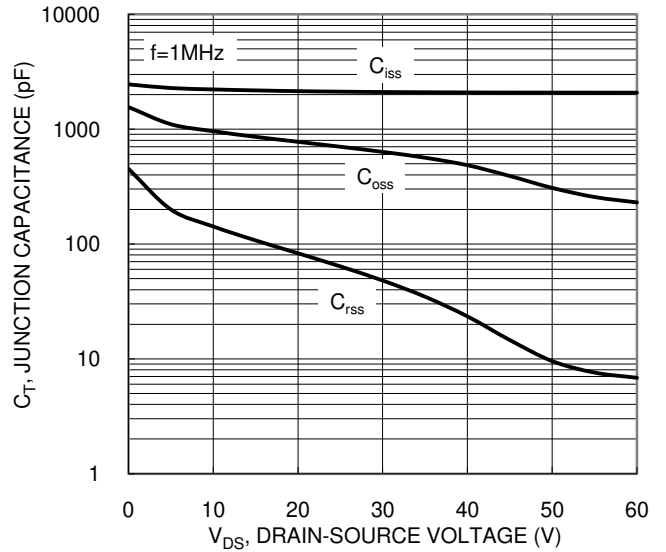


Figure 10. Typical Junction Capacitance

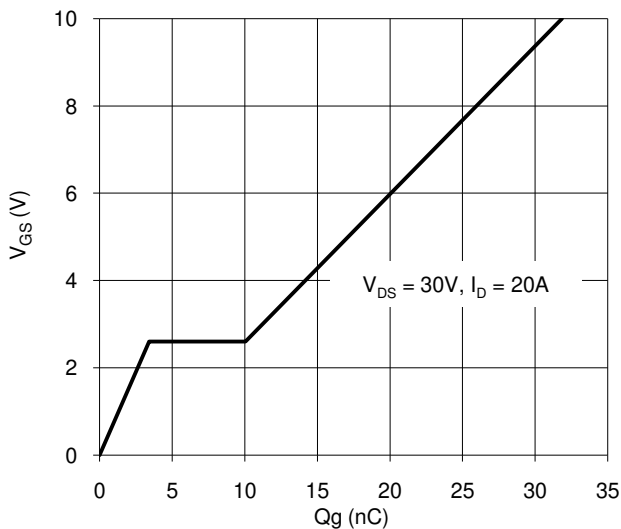


Figure 11. Gate Charge

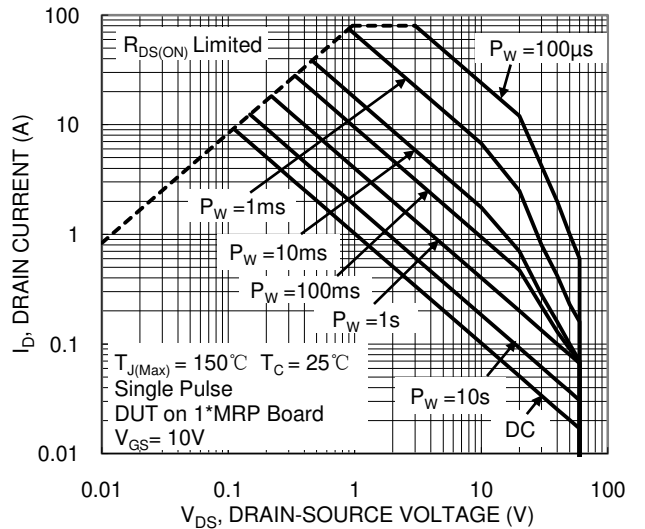


Figure 12. SOA, Safe Operation Area

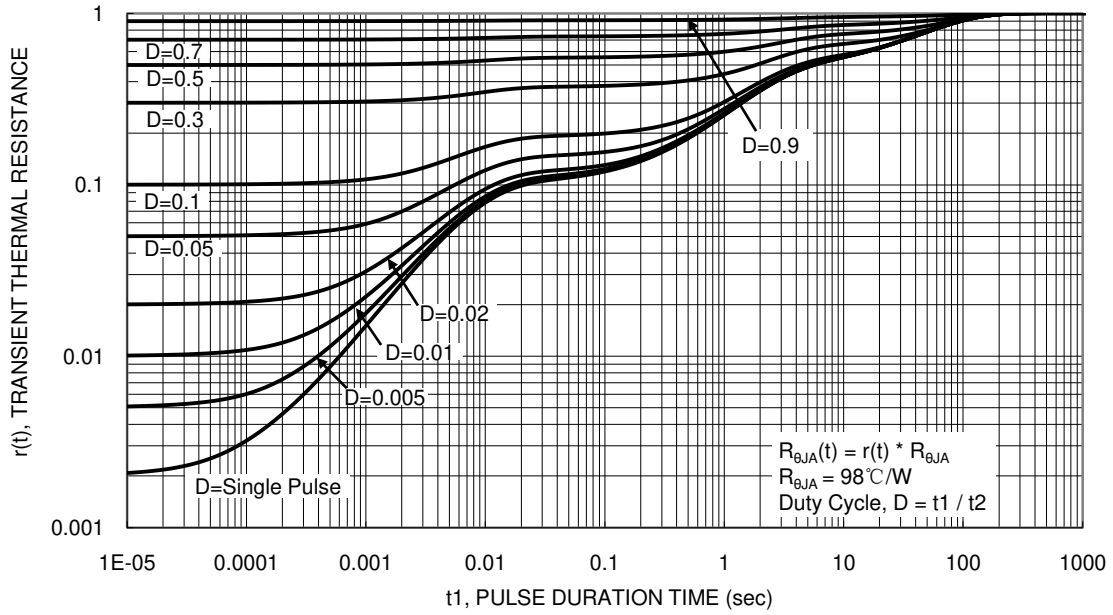
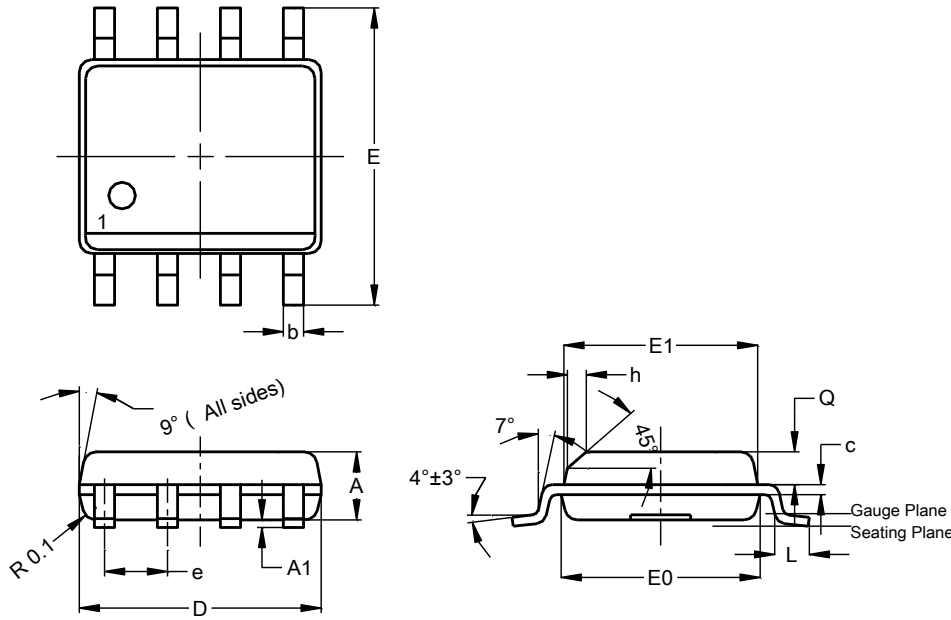


Figure 13. Transient Thermal Resistance

Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SO-8

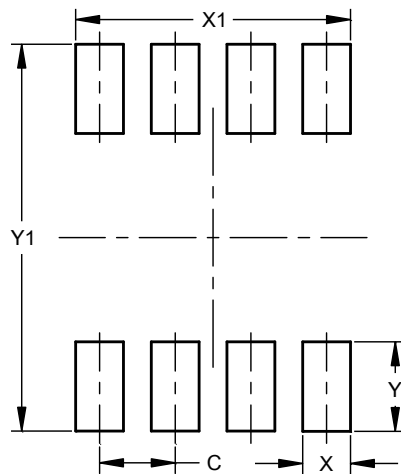


SO-8			
Dim	Min	Max	Typ
A	1.40	1.50	1.45
A1	0.10	0.20	0.15
b	0.30	0.50	0.40
c	0.15	0.25	0.20
D	4.85	4.95	4.90
E	5.90	6.10	6.00
E1	3.80	3.90	3.85
E0	3.85	3.95	3.90
e	--	--	1.27
h	-	--	0.35
L	0.62	0.82	0.72
Q	0.60	0.70	0.65
All Dimensions in mm			

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SO-8



Dimensions	Value (in mm)
C	1.27
X	0.802
X1	4.612
Y	1.505
Y1	6.50

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