Product data sheet

1. General description

Single low leakage current switching diode, encapsulated in a leadless ultra small DFN1006-2 (SOD882) Surface-Mounted Device (SMD) plastic package.

2. Features and benefits

- Switching time typical: t_{rr} = 0.8 μs
- Low leakage current typical: I_R = 3 pA
- Repetitive peak reverse voltage: V_{RRM} ≤ 85 V
- Low capacitance typical: C_d = 2 pF
- · Leadless ultra small SMD plastic package
- Low package height of 0.48 mm
- AEC-Q101 qualified

3. Applications

- · Low-leakage current applications
- · General-purpose switching

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V_{RRM}	repetitive peak reverse voltage	T _j = 25 °C		-	-	85	V
I _F	forward current	T _{amb} = 25 °C	[1]	-	-	325	mA
V_R	reverse voltage	T _j = 25 °C		-	-	75	V
V _F	forward voltage	I _F = 150 mA; T _j = 25 °C		-	-	1.25	V
I _R	reverse current	V _R = 75 V; T _j = 25 °C		-	0.003	5	nA
t _{rr}	reverse recovery time	I_F = 10 mA; I_R = 10 mA; $I_{R(meas)}$ = 1 mA; R_L = 100 Ω ; T_{amb} = 25 °C		-	0.8	3	μs

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.



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5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K	cathode		12
2	Α	anode	1 2	006aab040
			Transparent top view	
			DFN1006-2 (SOD882)	

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
BAS116L	DFN1006-2	DFN1006-2: leadless ultra small plastic package; 2 terminals	SOD882

7. Marking

Table 4. Marking codes

Type number	Marking code
BAS116L	J6

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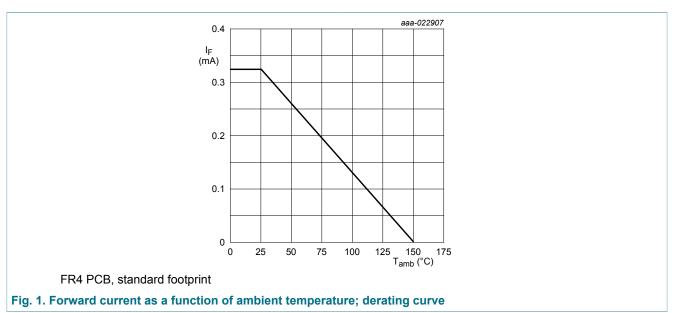
8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _R	reverse voltage	T _j = 25 °C		-	75	V
V_{RRM}	repetitive peak reverse voltage			-	85	V
l _F	forward current	T _{amb} = 25 °C	[1]	-	325	mA
I _{FRM}	repetitive peak forward current	$t_p \le 0.5 \text{ ms}; \delta \le 0.25 ; T_j = 25 \text{ °C}$		-	700	mA
I _{FSM}	non-repetitive peak forward current	t_p = 100 µs; $T_{j(init)}$ = 25 °C; square wave		-	4	Α
		t_p = 1 ms; $T_{j(init)}$ = 25 °C; square wave		-	1.5	Α
		t _p = 1 s; T _{j(init)} = 25 °C; square wave		-	0.5	Α
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	335	mW
			[2]	-	610	mW
T _j	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C

- [1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².



Low-leakage diode

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R _{th(j-a)}	thermal resistance from junction to ambient	_	[1]	-	-	375	K/W
			[2]	_	_	205	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point		[3]	-	-	40	K/W

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².
- [3] Soldering point of cathode tab.

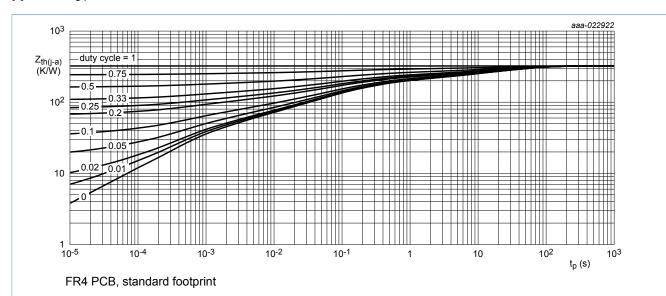


Fig. 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

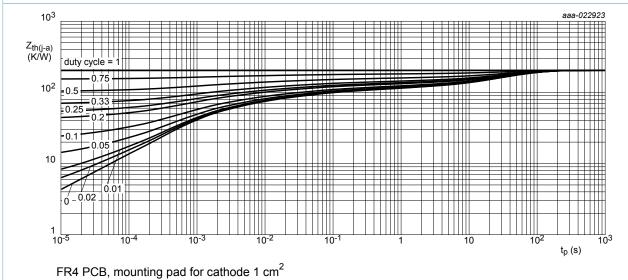


Fig. 3. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

Low-leakage diode

10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _F	forward voltage	I _F = 1 mA; T _j = 25 °C	-	-	0.9	V
		I _F = 10 mA; T _j = 25 °C	-	-	1	V
		I _F = 50 mA; T _j = 25 °C	-	-	1.1	V
		I _F = 150 mA; T _j = 25 °C	-	-	1.25	V
I _R	reverse current	V _R = 75 V; T _j = 25 °C	-	0.003	5	nA
		V _R = 75 V; T _j = 150 °C	-	3	80	nA
C _d	diode capacitance	V _R = 0 V; f = 1 MHz; T _{amb} = 25 °C	-	2	-	pF
t _{rr}	reverse recovery time	I_F = 10 mA; I_R = 10 mA; $I_{R(meas)}$ = 1 mA; R_L = 100 Ω; T_{amb} = 25 °C	-	0.8	3	μs

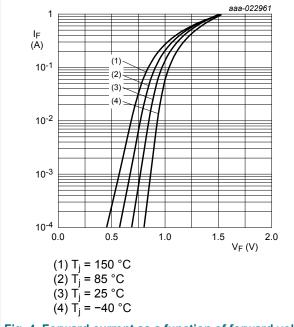


Fig. 4. Forward current as a function of forward voltage; typical values

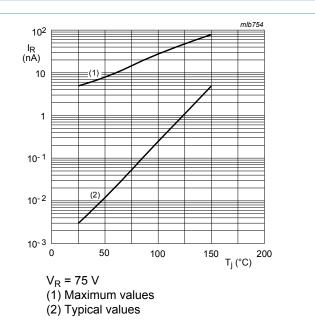


Fig. 5. Reverse current as a function of junction temperature

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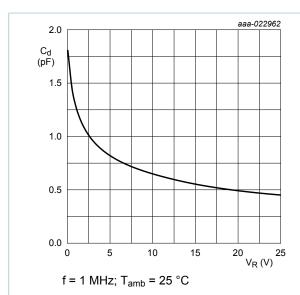


Fig. 6. Diode capacitance as a function of reverse voltage; typical values

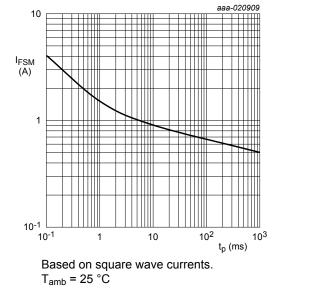
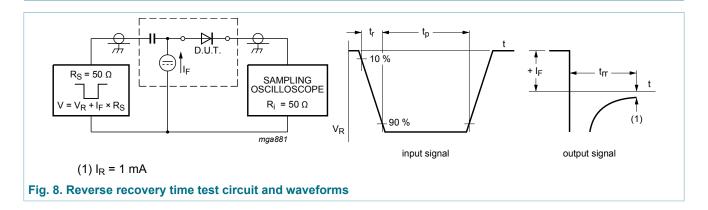


Fig. 7. Non-repetitive forward current as a function of pulse duration; maximum values

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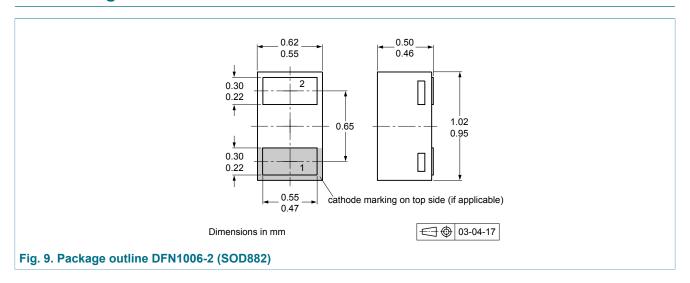
11. Test information



Quality information

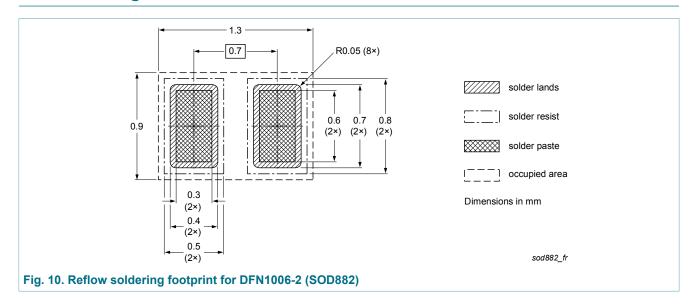
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 - Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline



Low-leakage diode

13. Soldering



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14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
BAS116L v.1	20160504	Product data sheet	-	-

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15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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BAS116L

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