



DMTH10H005SCT

Product Summary

BV _{DSS}	Rds(on)	Ι _D T _C = +25°C
100V	5mΩ @V _{GS} = 10V	140A

Description

This new generation MOSFET features low on-resistance and fast switching, making it ideal for high-efficiency power management applications.

Applications

- Motor Control
- Backlighting
- DC-DC Converters
- Power Management Functions

Features

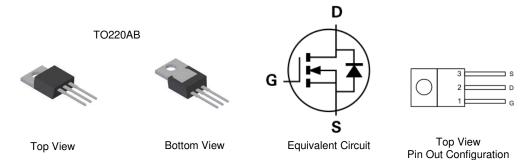
 Rated to +175°C – Ideal for High Ambient Temperature Environments

100V +175°C N-CHANNEL ENHANCEMENT MODE MOSFET

- 100% Unclamped Inductive Switching Ensures More Reliable and Robust End Application
- Low Input Capacitance
- High BV_{DSS} Rating for Power Application
- Low Input/Output Leakage
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

Mechanical Data

- Case: TO220AB
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Terminals: Matte Tin Finish Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Terminal Connections: See Diagram Below
- Weight: 1.85 grams (Approximate)



Ordering Information (Note 4)

Part Number	Case	Packaging				
DMTH10H005SCT	TO220AB	50 Pieces/Tube				

Notes: 1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.

2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



) | | =Manufacturer's Marking TH1H005S = Product Type Marking Code YYWW = Date Code Marking YY or <u>YY</u> = Last Two Digits of Year (ex: 17 = 2017) WW or <u>WW</u> = Week Code (01 to 53)



Maximum Ratings (@ $T_A = +25^{\circ}C$, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage	V _{DSS}	100	V	
Gate-Source Voltage	V _{GSS}	±20	V	
Continuous Drain Current	$T_{C} = +25^{\circ}C$ $T_{C} = +100^{\circ}C$	ID	140 99	A
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	I _{DM}	400	Α	
Maximum Continuous Body Diode Forward Current	Is	100	A	
Pulsed Body Diode Forward Current (10µs Pulse, Duty Cycle = 1%)	I _{SM}	400	A	
Avalanche Current, L = 3mH (Note 7)	I _{AS}	19	Α	
Avalanche Energy, L = 3mH (Note 7)	Eas	542	mJ	
Avalanche Current, L = 0.1mH	I _{AS}	25	Α	
Avalanche Energy, L = 0.1mH	E _{AS}	31.2	mJ	

Thermal Characteristics

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	T _A = +25°C	PD	2.9	W
Thermal Resistance, Junction to Ambient (Note 5)		R _{0JA}	51	°C/W
Total Power Dissipation	T _C = +25°C	PD	187	W
Thermal Resistance, Junction to Case		R _{eJC}	0.8	°C/W
Operating and Storage Temperature Range		T _{J,} T _{STG}	-55 to +175	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

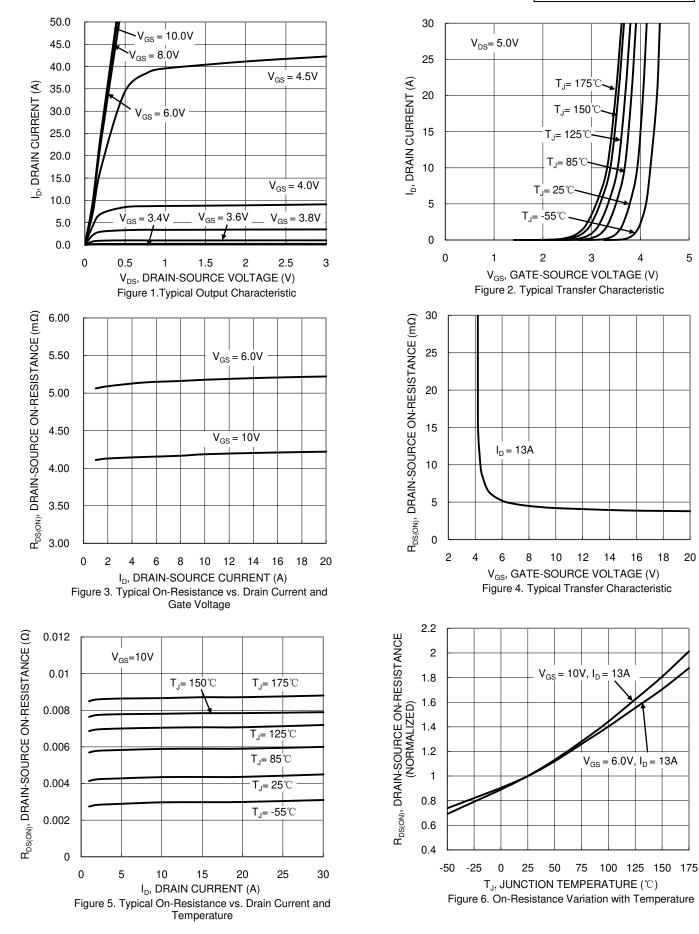
Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 6)	Symbol	IVIIII	Тур	Max	Unit	Test Condition	
Drain-Source Breakdown Voltage	BV _{DSS}	100	_		V	$V_{GS} = 0V, I_D = 1mA$	
Zero Gate Voltage Drain Current	IDSS	_	_	1	μA	$V_{DS} = 80V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 6)						<u> </u>	
Gate Threshold Voltage	V _{GS(TH)}	2	_	4	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	
Static Drain-Source On-Resistance	R _{DS(ON)}	_	3.8	5	mΩ	$V_{GS} = 10V, I_D = 13A$	
Diode Forward Voltage	V _{SD}	_	_	1.3	V	V _{GS} = 0V, I _S = 13A	
DYNAMIC CHARACTERISTICS (Note 7)							
Input Capacitance	CISS		8,474	_			
Output Capacitance	C _{OSS}		1,608		pF	$V_{DS} = 50V, V_{GS} = 0V$ f = 1MHz	
Reverse Transfer Capacitance	CRSS		78	_			
Gate Resistance	Rg	_	0.41	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$	
Total Gate Charge	Q _G	_	111.7	_			
Gate-Source Charge	Q _{GS}	_	28.9	_	nC	$V_{DD} = 50V, I_D = 13A, V_{GS} = 10V$	
Gate-Drain Charge	Q _{GD}	_	21.3	_			
Turn-On Delay Time	t _{D(ON)}	_	29.9	_			
Turn-On Rise Time	t _R	_	30.3	_		$\label{eq:VDD} \begin{split} V_{DD} &= 50V, \ V_{GS} = 10V, \\ I_D &= 13A, \ R_g = 6\Omega \end{split}$	
Turn-Off Delay Time	t _{D(OFF)}		79.7		ns		
Turn-Off Fall Time	tF		41.6		1	-	
Reverse Recovery Time	t _{RR}	_	70	_	ns		
Reverse Recovery Charge	Q _{RR}		181		nC	I _F = 13A, di/dt = 100A/μs	

Notes: 5. Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.

6. Short duration pulse test used to minimize self-heating effect.
7. Guaranteed by design. Not subject to product testing.



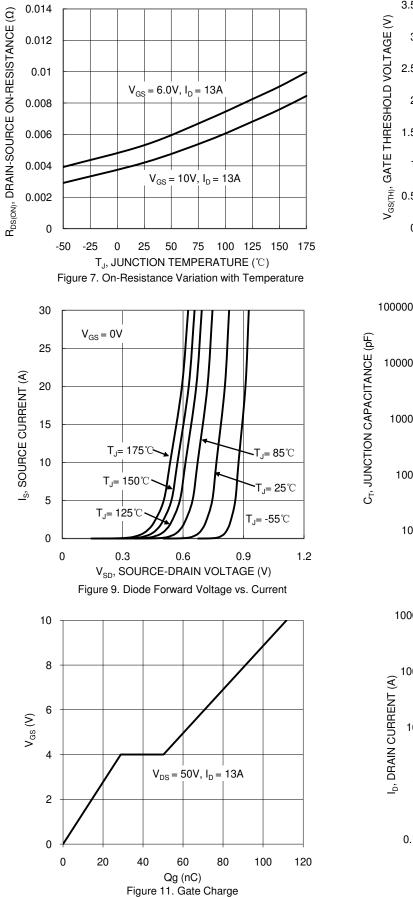
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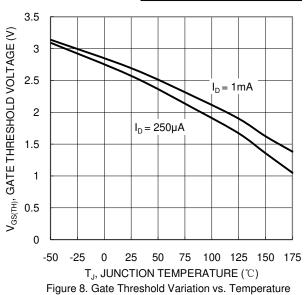


DMTH10H005SCT Document number: DS39468 Rev. 2 - 2

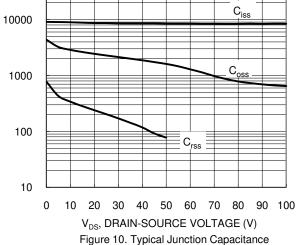


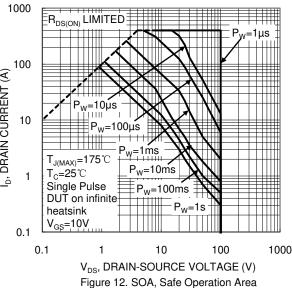
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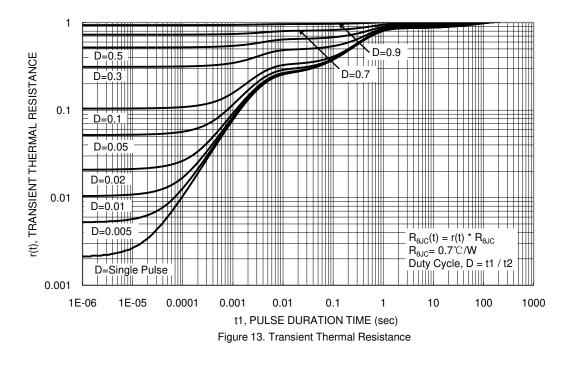






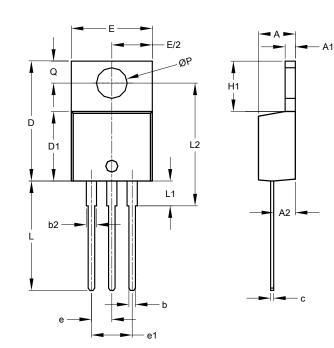






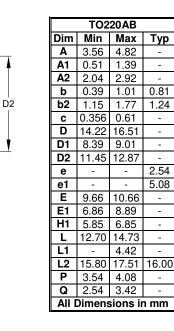
Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.



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