



#### 80V N-CHANNEL ENHANCEMENT MODE MOSFET

### **Product Summary**

BV <sub>DSS</sub>	R <sub>DS(ON)</sub> Max	I <sub>D</sub> Max T <sub>C</sub> = +25°C
	6.9mΩ @ V <sub>GS</sub> = 10V	48A
80V	10.4mΩ @ V <sub>GS</sub> = 4.5V	38A

### Description

This MOSFET is designed to minimize the on-state resistance  $(R_{DS(ON)})$ , yet maintain superior switching performance, making it ideal for high efficiency power management applications.

### **Applications**

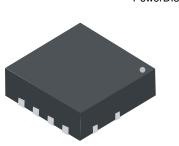
- Backlighting
- Power Management Functions
- DC-DC Converters

## **Features and Benefits**

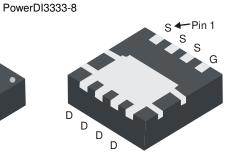
- Low R<sub>DS(ON)</sub> Ensures On-State Losses are Minimized
- Excellent Q<sub>gd</sub> × R<sub>DS(ON)</sub> Product (FOM)
- Advanced Technology for DC-DC Converts
- Small Form Factor Thermally Efficient Package Enables Higher Density End Products
- Occupies Just 33% of the Board Area Occupied by SO-8 Enabling Smaller End Product
- 100% Unclamped Inductive Switching (UIS) Test in Production Ensures More Reliable and Robust End Application
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

#### **Mechanical Data**

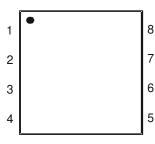
- Case: PowerDI<sup>®</sup>3333-8
- Case Material: Molded Plastic, "Green" Molding Compound.
   UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See Diagram
- Terminal Finish Matte Tin Annealed over Copper Leadframe.
   Solderable per MIL-STD-202, Method 208 ©3
- Weight: 0.072 grams (Approximate)



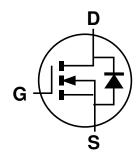




Bottom View



Top View Pin-Out



**Equivalent Circuit** 

### Ordering Information (Note 4)

Part Number	Case	Packaging
DMT8008LFG-7	PowerDI3333-8	2,000/Tape & Reel
DMT8008LFG-13	PowerDI3333-8	3,000/Tape & Reel

Notes:

- 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

## Marking Information



HY8 = Product Type Marking Code YYWW = Date Code Marking YY = Last Two Digits of Year (ex: 19 = 2019) WW = Week Code (01 to 53)



# 

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage	$V_{DSS}$	80	V	
Gate-Source Voltage		$V_{GSS}$	±20	V
Continuous Drain Current (Note 7) V <sub>GS</sub> = 10V	$T_C = +25$ °C $T_C = +70$ °C	I <sub>D</sub>	48 38	Α
Continuous Drain Current (Note 6) V <sub>GS</sub> = 10V	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I <sub>D</sub>	16 13	Α
Maximum Continuous Body Diode Forward Current (Note 6)	I <sub>S</sub>	45	Α	
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	I <sub>DM</sub>	192	Α	
Pulsed Body Diode Forward Current (10µs Pulse, Duty Cycle = 1%	I <sub>SM</sub>	192	Α	
Avalanche Current, L = 1mH (Note 8)	I <sub>AS</sub>	18	А	
Avalanche Energy, L = 1mH (Note 8)	Eas	162	mJ	

### **Thermal Characteristics**

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	$T_A = +25^{\circ}C$	$P_{D}$	1.0	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	$R_{\theta JA}$	126	°C/W
Total Power Dissipation (Note 6)	$T_A = +25^{\circ}C$	P <sub>D</sub>	2.5	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	$R_{\theta JA}$	49	°C/W
Total Power Dissipation (Note 7)	T <sub>C</sub> = +25°C	P <sub>D</sub>	23.5	W
Thermal Resistance, Junction to Case (Note 7)		$R_{\theta JC}$	5.3	°C/W
Operating and Storage Temperature Range		$T_{J_i}T_{STG}$	-55 to +150	°C

### **Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

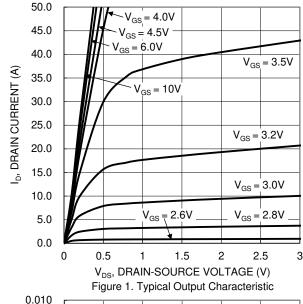
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 9)							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	80	-	_	V	$V_{GS} = 0V$ , $I_D = 1mA$	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>			1	μΑ	$V_{DS} = 64V$ , $V_{GS} = 0V$	
Gate-Source Leakage	I <sub>GSS</sub>	-	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 9)							
Gate Threshold Voltage	V <sub>GS(TH)</sub>	1.2		2.5	٧	$V_{DS} = V_{GS}$ , $I_D = 1mA$	
Static Drain-Source On-Resistance			5.3	6.9	mΩ	$V_{GS} = 10V, I_D = 20A$	
Static Drain-Source On-nesistance	R <sub>DS(ON)</sub>	_	7.9	10.4	mΩ	$V_{GS} = 4.5V, I_D = 10A$	
Diode Forward Voltage	$V_{SD}$	-	0.8	1.2	V	$V_{GS} = 0V, I_S = 20A$	
DYNAMIC CHARACTERISTICS (Note 10)							
Input Capacitance	C <sub>iss</sub>	_	2254	_		V <sub>DS</sub> = 40V, V <sub>GS</sub> = 0V, f = 1MHz	
Output Capacitance	Coss		745	_	рF		
Reverse Transfer Capacitance	C <sub>rss</sub>		31				
Gate Resistance	$R_g$	_	1.98	_	Ω	$V_{DS} = 0V$ , $V_{GS} = 0V$ , $f = 1MHz$	
Total Gate Charge (V <sub>GS</sub> = 4.5V)	Qg	_	18.3	_		V <sub>DS</sub> = 40V, I <sub>D</sub> = 14A	
Total Gate Charge (V <sub>GS</sub> = 10V)	$Q_g$	-	37.7	_	nC		
Gate-Source Charge	$Q_{gs}$	-	5.3	_	IIC		
Gate-Drain Charge	$Q_{gd}$	-	7.8	_			
Turn-On Delay Time	t <sub>D(ON)</sub>	_	6.9	_		$V_{DD} = 40V, V_{GS} = 10V,$ $I_{D} = 14A, R_{G} = 6\Omega$	
Turn-On Rise Time	t <sub>R</sub>	_	12	_			
Turn-Off Delay Time	t <sub>D(OFF)</sub>	_	37	_	ns		
Turn-Off Fall Time	t <sub>F</sub>	-	21	_			
Body Diode Reverse Recovery Time	t <sub>RR</sub>	-	42	_	ns	1 110 11/14 1000///	
Body Diode Reverse Recovery Charge	Q <sub>RR</sub>		53	_	$I_{S} = 14A, di/dt = 100A/\mu s$		

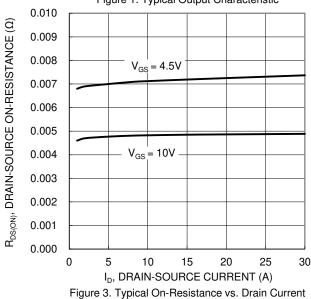
5. Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.

- 6. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.
- 7. Thermal resistance from junction to soldering point (on the exposed drain pad).
- 8. I<sub>AS</sub> and E<sub>AS</sub> ratings are based on low frequency and duty cycles to keep T<sub>J</sub> = +25°C.
  9. Short duration pulse test used to minimize self-heating effect.
  10. Guaranteed by design. Not subject to product testing.









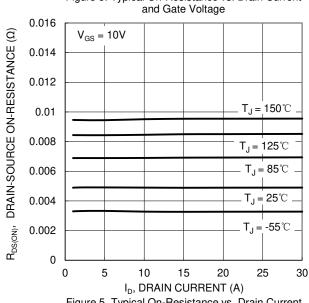
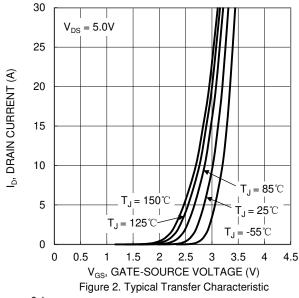
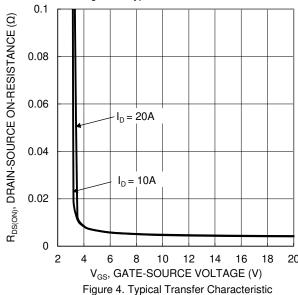


Figure 5. Typical On-Resistance vs. Drain Current and Temperature





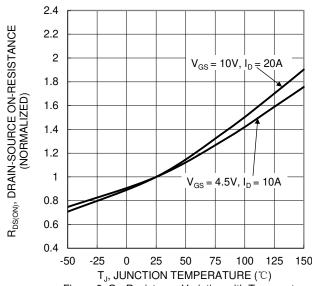


Figure 6. On-Resistance Variation with Temperature





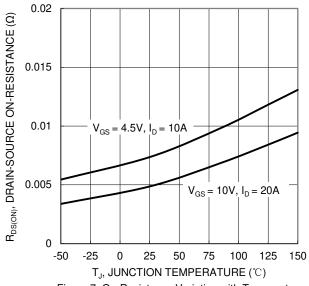
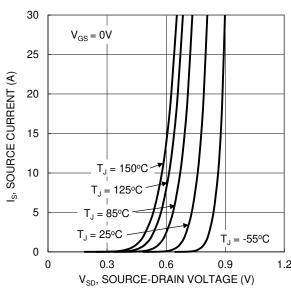


Figure 7. On-Resistance Variation with Temperature



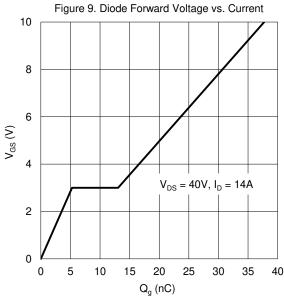


Figure 11. Gate Charge

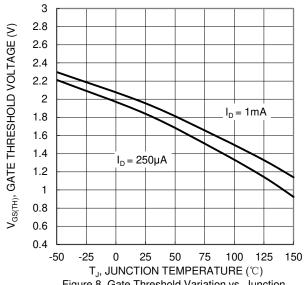
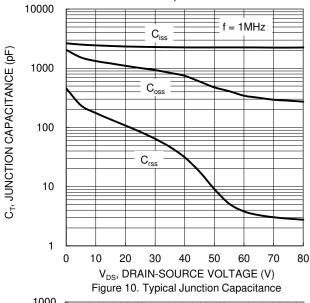


Figure 8. Gate Threshold Variation vs. Junction Temperature



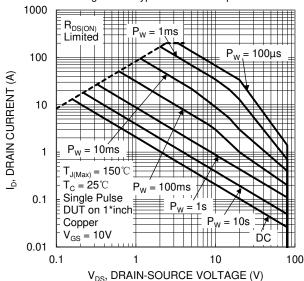


Figure 12. SOA, Safe Operation Area



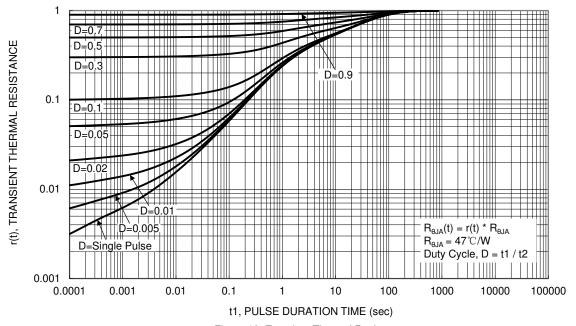


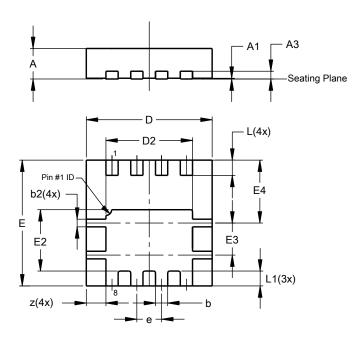
Figure 13. Transient Thermal Resistance



## **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### PowerDI3333-8

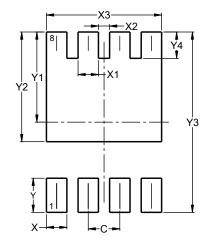


PowerDI3333-8						
Dim	Min	Max	Тур			
Α	0.75	0.85	0.80			
<b>A</b> 1	0.00	0.05	0.02			
<b>A3</b>	-	_	0.203			
b	0.27	0.37	0.32			
b2	0.15	0.25	0.20			
D	3.25	3.35	3.30			
D2	2.22	2.32	2.27			
Е	3.25	3.35	3.30			
E2	1.56	1.66	1.61			
E3	0.79	0.89	0.84			
E4	1.60	1.70	1.65			
е	-	_	0.65			
L	0.35	0.45	0.40			
L1	_	_	0.39			
Z	_	_	0.515			
All Dimensions in mm						

## **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### PowerDI3333-8



Dimensions	Value (in mm)
С	0.650
Х	0.420
X1	0.420
X2	0.230
Х3	2.370
Υ	0.700
Y1	1.850
Y2	2.250
Y3	3.700
Y4	0.540



#### IMPORTANT NOTICE

DIODES INCORPORATED MAKES NO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARDS TO THIS DOCUMENT, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION).

Diodes Incorporated and its subsidiaries reserve the right to make modifications, enhancements, improvements, corrections or other changes without further notice to this document and any product described herein. Diodes Incorporated does not assume any liability arising out of the application or use of this document or any product described herein; neither does Diodes Incorporated convey any license under its patent or trademark rights, nor the rights of others. Any Customer or user of this document or products described herein in such applications shall assume all risks of such use and will agree to hold Diodes Incorporated and all the companies whose products are represented on Diodes Incorporated website, harmless against all damages.

Diodes Incorporated does not warrant or accept any liability whatsoever in respect of any products purchased through unauthorized sales channel. Should Customers purchase or use Diodes Incorporated products for any unintended or unauthorized application, Customers shall indemnify and hold Diodes Incorporated and its representatives harmless against all claims, damages, expenses, and attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized application.

Products described herein may be covered by one or more United States, international or foreign patents pending. Product names and markings noted herein may also be covered by one or more United States, international or foreign trademarks.

This document is written in English but may be translated into multiple languages for reference. Only the English version of this document is the final and determinative format released by Diodes Incorporated.

#### **LIFE SUPPORT**

Diodes Incorporated products are specifically not authorized for use as critical components in life support devices or systems without the express written approval of the Chief Executive Officer of Diodes Incorporated. As used herein:

- A. Life support devices or systems are devices or systems which:
  - 1. are intended to implant into the body, or
  - 2. support or sustain life and whose failure to perform when properly used in accordance with instructions for use provided in the labeling can be reasonably expected to result in significant injury to the user.
- B. A critical component is any component in a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or to affect its safety or effectiveness.

Customers represent that they have all necessary expertise in the safety and regulatory ramifications of their life support devices or systems, and acknowledge and agree that they are solely responsible for all legal, regulatory and safety-related requirements concerning their products and any use of Diodes Incorporated products in such safety-critical, life support devices or systems, notwithstanding any devices- or systems-related information or support that may be provided by Diodes Incorporated. Further, Customers must fully indemnify Diodes Incorporated and its representatives against any damages arising out of the use of Diodes Incorporated products in such safety-critical, life support devices or systems.

Copyright © 2019, Diodes Incorporated

www.diodes.com