

Data Sheet

Description

KGF65A6L and MGF65A6L are 650 V Field Stop IGBTs. Sanken original trench structure decreases gate capacitance, and achieves low saturation voltage and switching losses reduction. Thus, Field Stop IGBTs can improve the efficiency of your circuit.

Features

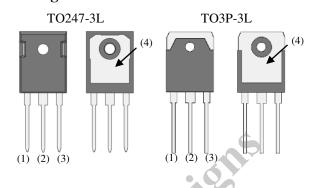
- Low Saturation Voltage
- High Speed Switching
- With Integrated Fast Recovery Diode
- RoHS Compliant

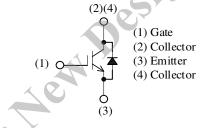
| $\begin{array}{l} \bullet \ \ V_{CE}$ | 60 A 5 μs 1.6 V typ. 150 ns typ. | (1) 0 |
|---|---|-----------------|
| Applications | | * |
| • Uninterruptible Power Supply (UPS) | | 80, |
| Inverter CircuitBridge Circuit | | Selection Guide |
| | 76 | Part Number |
| | | KGF65A6L |
| | 67 | MGF65A6L |
| Aot Recollin | | |

Applications

- Uninterruptible Power Supply (UPS)
- Inverter Circuit
- Bridge Circuit

Package





Not to scale

| Part Number | Package |
|-------------|----------|
| KGF65A6L | TO247-3L |
| MGF65A6L | TO3P-3L |

KGF65A6L, MGF65A6L

Absolute Maximum Ratings

Unless otherwise specified, $T_A = 25$ °C

| Parameter | Symbol | Conditions | Rating | Unit | Remarks |
|--------------------------------------|-----------------------|--|-------------------|------|---------|
| Collector to Emitter Voltage | V_{CE} | | 650 | V | |
| Gate to Emitter Voltage | V_{GE} | | ±30 | V | |
| Continuous Collector Current (1) | I_{C} | T _C = 25 °C | 80 ⁽²⁾ | A | |
| | | $T_C = 100 ^{\circ}C$ | 60 | A | |
| Pulsed Collector Current | $I_{C(PULSE)}$ | $PW \le 1 \text{ ms},$ duty cycle $\le 1\%$ | 180 | A | |
| Diode Continuous Forward Current (1) | I_{F} | $T_C = 25 ^{\circ}C$ | 80 ⁽²⁾ | A | |
| | | T _C = 100 °C | 60 | A | 5 |
| Diode Pulsed Forward Current | I _{F(PULSE)} | $PW \le 1 \text{ ms},$ | 180 | Α | |
| Blode I dised I of ward Carrent | | duty cycle ≤ 1% | 100 | A | |
| Maximum Collector–Emitter dv/dt | dv/dt | $T_J \le 175 ^{\circ}\text{C},$ see Figure 1. | 10 | V/ns | |
| Short Circuit Withstand Time | t _{SC} | $V_{GE} = 15 \text{ V},$ $V_{CE} = 400 \text{ V}$ $T_{J}=175 ^{\circ}\text{C}$ | 5 | μs | |
| Power Dissipation | P_D | T _C = 25 °C | 405 | W | |
| Operating Junction Temperature | T_{J} | | 175 | °C | |
| Storage Temperature Range | T_{STG} | | −55 to 150 | °C | |

Thermal Characteristics

Unless otherwise specified, $T_A = 25$ °C

| Parameter | Symbol | Conditions | Min. | Typ. | Max. | Unit | Remarks |
|--|-----------------------|------------|------|------|------|------|---------|
| Thermal Resistance of IGBT (Junction to Case) | $R_{\theta JC}(IGBT)$ | | _ | _ | 0.37 | °C/W | |
| Thermal Resistance of Diode (Junction to Case) | $R_{\theta JC}(Di)$ | | | _ | 0.93 | °C/W | |
| HotRee | | | | | | | |

 $^{^{(1)}}$ I_C and I_F are determined by the maximum junction temperature for TO3P-3L package. $^{(2)}$ Determined by bonding wires capability.

KGF65A6L, MGF65A6L

Electrical Characteristics

Unless otherwise specified, $T_A = 25$ °C

| Parameter Parameter | Symbol | Conditions | Min. | Тур. | Max. | Unit | |
|---|----------------------|--|------|------|------|------|--|
| Collector to Emitter Breakdown Voltage | V _{(BR)CES} | $I_C = 100 \ \mu A, \ V_{GE} = 0 \ V$ | 650 | | _ | V | |
| Collector to Emitter Leakage Current | I_{CES} | $V_{CE} = 650 \text{ V}, V_{GE} = 0 \text{ V}$ | _ | _ | 100 | μA | |
| Gate to Emitter Leakage Current | I_{GES} | $V_{GE} = \pm 30 \text{ V}$ | _ | | ±500 | nA | |
| Gate Threshold Voltage | V _{GE(TH)} | $V_{CE} = 10 \text{ V}, I_{C} = 1 \text{ mA}$ | 4.0 | 5.5 | 7.0 | V | |
| Collector to Emitter Saturation Voltage | V _{CE(sat)} | $V_{GE} = 15 \text{ V}, I_{C} = 60 \text{ A}$ | — | 1.6 | 1.96 | V | |
| Input Capacitance | C_{ies} | $V_{CE} = 20 \text{ V},$ | _ | 3500 | AP | | |
| Output Capacitance | C_{oes} | $V_{GE} = 0 V$, | _ | 330 | | pF | |
| Reverse Transfer Capacitance | C _{res} | f = 1.0 MHz, | _ | 170 | | | |
| Gate charge | Q_{g} | $V_{CE} = 520 \text{ V}, I_{C} = 60 \text{ A},$ $V_{GE} = 15 \text{ V}$ | -< | 110 | _ | nC | |
| Turn-On Delay Time | $t_{d(on)}$ | | 1 | 50 | _ | | |
| Rise Time | $t_{\rm r}$ | <i>A</i> | | 70 | _ | ns | |
| Turn-Off Delay Time | $t_{d(off)}$ | $T_{\rm J}$ = 25 °C, | | 130 | | | |
| Fall Time | t_{f} | see Figure 1. | _ | 60 | _ | | |
| Turn-on Energy (3) | Eon | 60) | _ | 1.7 | _ | I | |
| Turn-off Energy | E _{off} | | _ | 1.4 | _ | mJ | |
| Turn-On Delay Time | $t_{d(on)}$ | | | 50 | | | |
| Rise Time | $t_{\rm r}$ | 70 | | 70 | | | |
| Turn-Off Delay Time | t _{d(off)} | $T_I = 175 ^{\circ}\text{C},$ | | 160 | _ | ns | |
| Fall Time | $t_{\rm f}$ | see Figure 1. | | 150 | _ | | |
| Turn-on Energy (3) | Eon | | _ | 2.7 | _ | | |
| Turn-off Energy | E _{off} | | | 2.5 | | mJ | |
| Emitter to Collector Diode Forward Voltage | V_F | $I_F = 60 \text{ A}$ | | 1.7 | | V | |
| Emitter to Collector Diode Reverse Recovery Time | t _{rr} | $I_F = 60 \text{ A},$ $di/dt = 500 \text{ A/}\mu\text{s}$ | | 65 | _ | ns | |

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 $^{^{\}left(3\right) }$ Energy losses include the reverse recovery of diode.

Test Circuits and Waveforms

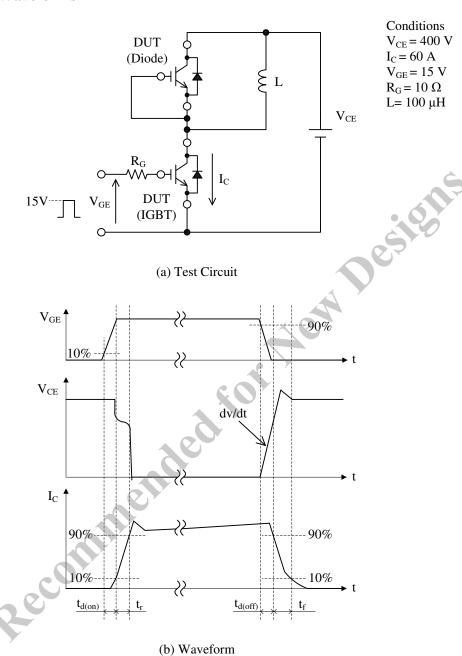


Figure 1. Test Circuits and Waveforms of dv/dt and Switching Time

Rating and Characteristic Curves

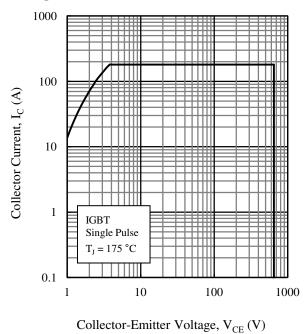


Figure 2. IGBT Reverse Bias Safe Operating Area

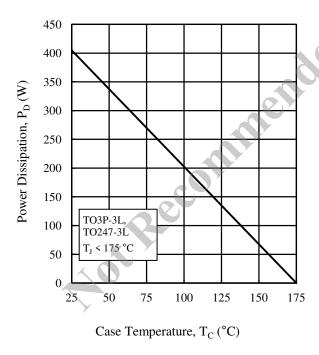


Figure 4. Power Dissipation vs. Case Temperature

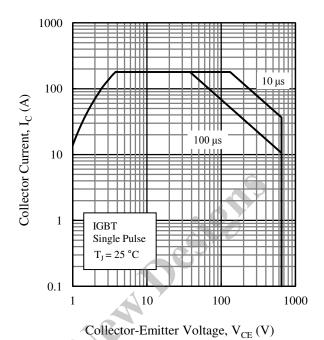


Figure 3. IGBT Safe Operating Area

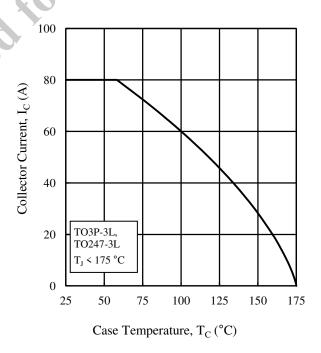


Figure 5. Collector Current vs. Case Temperature

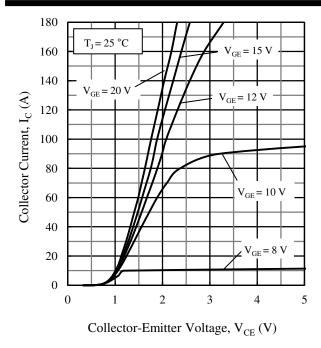


Figure 6. Output Characteristics ($T_J = 25$ °C)

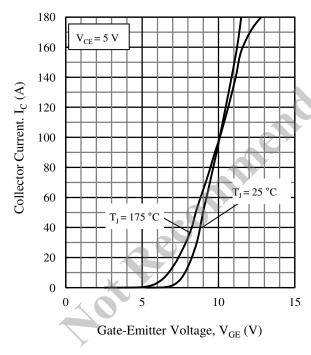


Figure 8. Transfer Characteristics

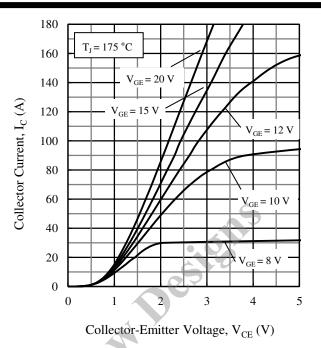


Figure 7. Output Characteristics ($T_J = 175$ °C)

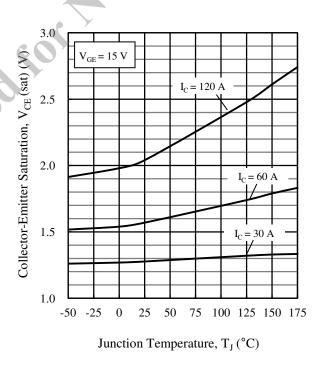


Figure 9. Saturation Voltage vs. Junction Temperature

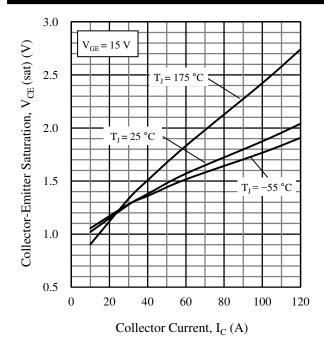


Figure 10. Saturation Voltage vs. Collector Current

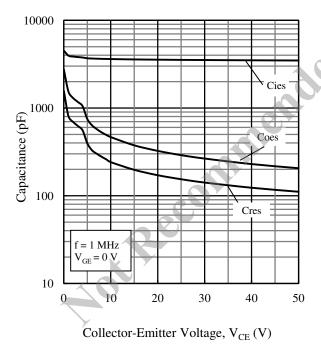
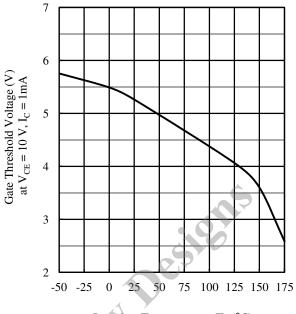


Figure 12. Capacitance Characteristics



Junction Temperature, T_J (°C)

Figure 11. Gate Threshold Voltage vs. Junction Temperature

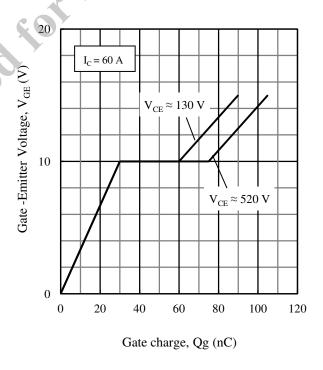


Figure 13. Typical Gate Charge

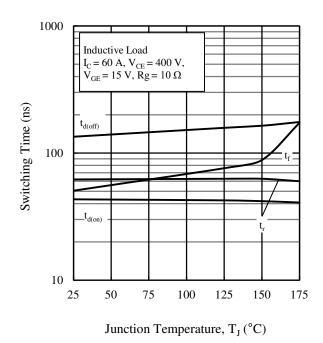


Figure 14. Switching Time vs. Junction Temperature

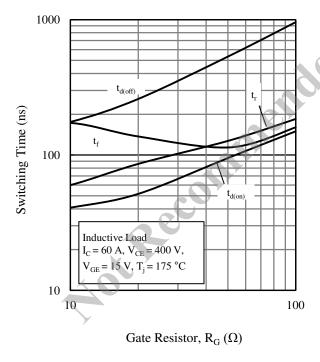


Figure 16. Switching Time vs. Gate Resistor

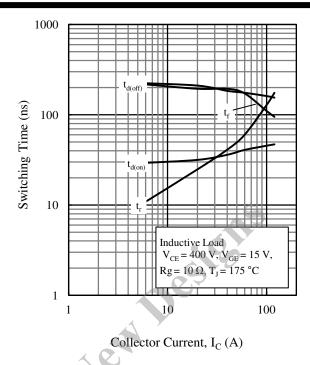


Figure 15. Switching Time vs. Collector Current

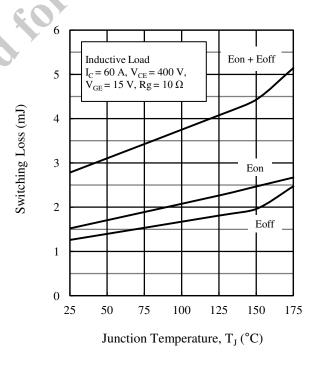


Figure 17. Switching Loss vs. Junction Temperature

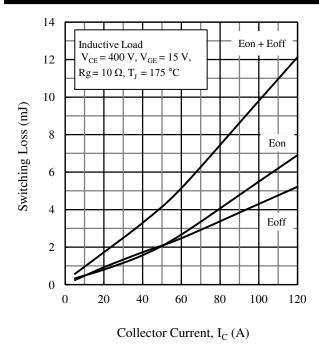


Figure 18. Switching Loss vs. Collector Current

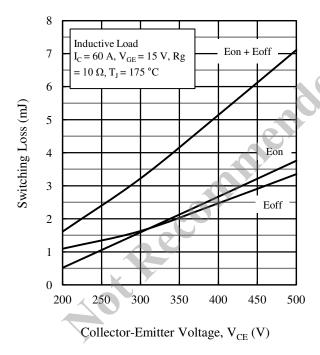


Figure 20. Switching Loss vs. Collector-Emitter Voltage

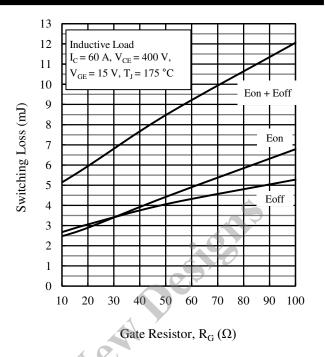


Figure 19. Switching Loss vs. Gate Resistor

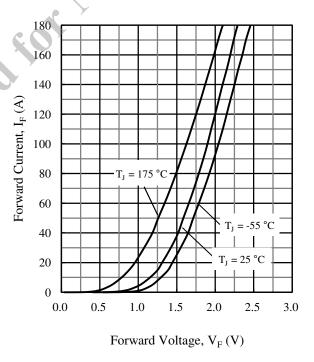


Figure 21. Diode Forward Characteristics

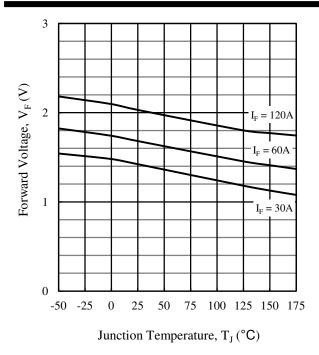


Figure 22. Diode Forward Voltage vs. Junction Temperature

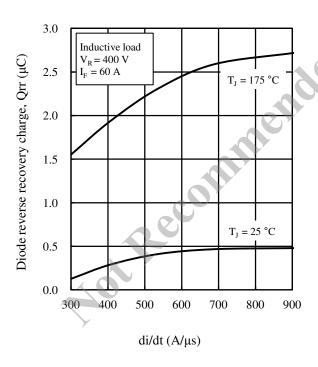


Figure 24. Diode Reverse Recovery Charge vs. di/dt

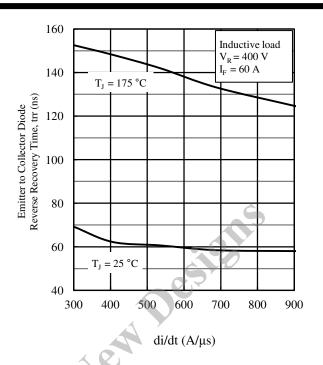


Figure 23. Diode Reverse Recovery Time vs. di/dt

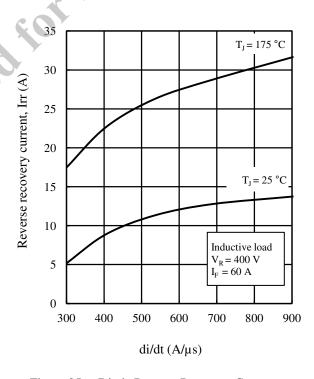
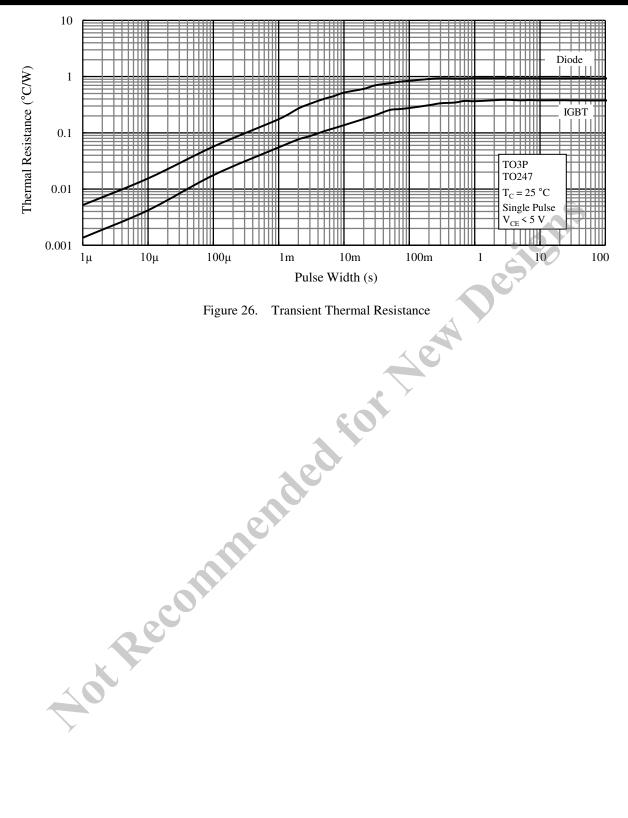
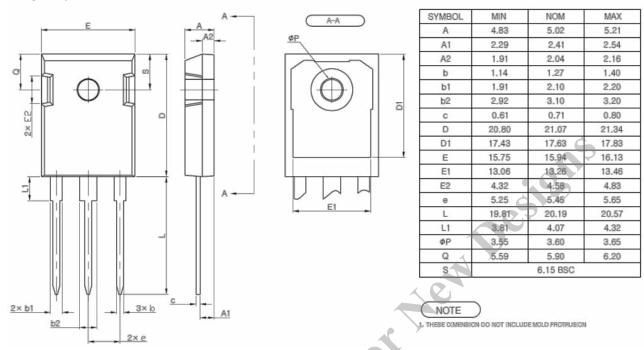


Figure 25. Diode Reverse Recovery Current vs. di/dt

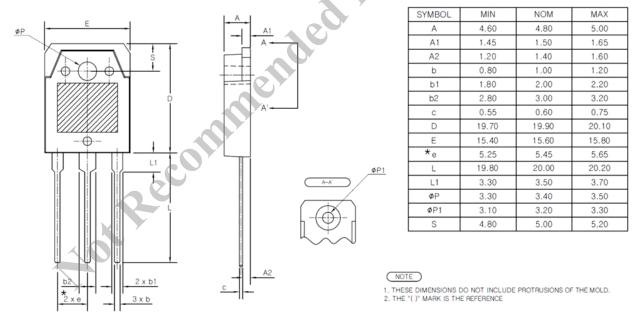


Physical Dimensions

• TO247-3L



• TO3P-3L



NOTES:

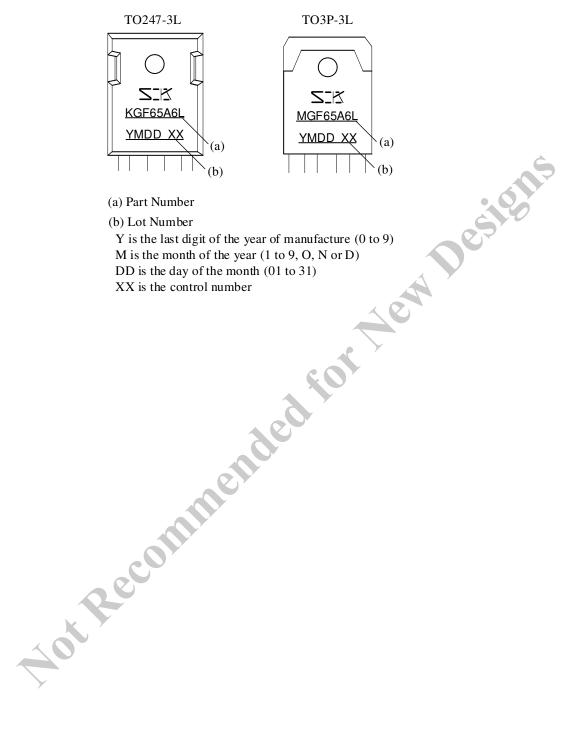
- All dimensions in millimeters
- Pin treatment for TO247 and TO3P: Pb-free (RoHS compliant)
- When soldering the products, make sure to minimize the working time within the following limits:

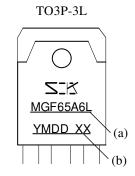
 260 ± 5 °C 10 ± 1 s, 2 times (flow)

 380 ± 10 °C 3.5 ± 0.5 s, 1 time (soldering iron)

- Soldering should be at a distance of at least 1.5 mm from the body of the products.
- The recommended screw torque for TO247, TO3P and TO3PF: 0.686 to 0.882 N·m (7 to 9 kgf·cm)

Marking Diagram





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