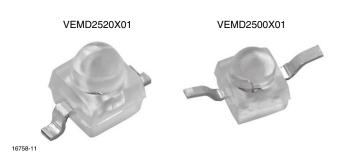


Vishay Semiconductors

Silicon PIN Photodiode



DESCRIPTION

VEMD2500X01 and VEMD2520X01 are high speed and high sensitive PIN photodiodes in a clear epoxy, miniature surface mount package (SMD) with dome lens. The photo sensitive area of the chip is 0.23 mm².

FEATURES

- · Package type: surface mount
- Package form: GW, RGW
- Dimensions (L x W x H in mm): 2.3 x 2.3 x 2.8
- AEC-Q101 qualified
- · High radiant sensitivity
- Suitable for visible and near infrared radiation
- Fast response times
- Angle of half sensitivity: $\varphi = \pm 15^{\circ}$
- Package matched with IR emitter series VSMB2000X01
- Floor life: 4 weeks, MSL 2a, acc. J-STD-020
- Lead (Pb)-free reflow soldering
- Compliant to RoHS Directive 2002/95/EC and in accordance to WEEE 2002/96/EC

Note

** Please see document "Vishay Material Category Policy": www.vishay.com/doc?99902

APPLICATIONS

· High speed photo detector

PRODUCT SUMMARY			
COMPONENT	I _{ra} (μΑ)	φ (deg)	λ _{0.1} (nm)
VEMD2500X01	12	± 15	350 to 1120
VEMD2520X01	12	± 15	350 to 1120

Note

• Test conditions see table "Basic Characteristics"

ORDERING INFORMATION				
ORDERING CODE	PACKAGING	REMARKS	PACKAGE FORM	
VEMD2500X01	Tape and reel	MOQ: 6000 pcs, 6000 pcs/reel	Reverse gullwing	
VEMD2520X01	Tape and reel	MOQ: 6000 pcs, 6000 pcs/reel	Gullwing	

Note

• MOQ: minimum order quantity

ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Reverse voltage		V _R	60	V
Power dissipation	T _{amb} ≤ 25 °C	Pv	215	mW
Junction temperature		Тj	100	°C
Operating temperature range		T _{amb}	- 40 to + 100	°C
Storage temperature range		T _{stg}	- 40 to + 100	°C
Soldering temperature	Acc. reflow solder profile fig. 7	T _{sd}	260	°C
Thermal resistance junction/ambient	Acc. J-STD-051	R _{thJA}	250	K/W



COMPLIANT

<u>GREEN</u> (5-2008)**





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BASIC CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Forward voltage	I _F = 50 mA	V _F		1		V
Breakdown voltage	I _R = 100 μA, E = 0	V _(BR)	32			V
Reverse dark current	V _R = 10 V, E = 0	I _{ro}		1	10	nA
Diode capacitance	$V_{R} = 0 V, f = 1 MHz, E = 0$	CD		4		pF
	$V_{R} = 5 V, f = 1 MHz, E = 0$	CD		1.3		pF
Open circuit voltage	$E_e = 1 \text{ mW/cm}^2$, $\lambda = 950 \text{ nm}$	Vo		350		mV
Temperature coefficient of Vo	$E_e = 1 \text{ mW/cm}^2$, $\lambda = 950 \text{ nm}$	TK _{Vo}		- 2.6		mV/K
Short circuit current	$E_e = 1 \text{ mW/cm}^2$, $\lambda = 950 \text{ nm}$	l _k		11		μA
Temperature coefficient of I_k	$E_e = 1 \text{ mW/cm}^2$, $\lambda = 950 \text{ nm}$	TK _{lk}		0.1		%/K
Reverse light current	$E_e = 1 \text{ mW/cm}^2, \lambda = 950 \text{ nm}, \\ V_R = 5 \text{ V}$	I _{ra}	8.5	12	17	μA
Angle of half sensitivity		φ		± 15		deg
Wavelength of peak sensitivity		λ _p		900		nm
Range of spectral bandwidth		λ _{0.1}		350 to 1120		nm
Rise time	$V_{R} = 10 \text{ V}, \text{ R}_{L} = 1 \text{ k}\Omega,$ $\lambda = 820 \text{ nm}$	t _r		100		ns
Fall time	$V_{R} = 10 \text{ V}, \text{ R}_{L} = 1 \text{ k}\Omega,$ $\lambda = 820 \text{ nm}$	t _f		100		ns

BASIC CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

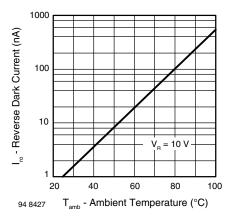


Fig. 1 - Reverse Dark Current vs. Ambient Temperature

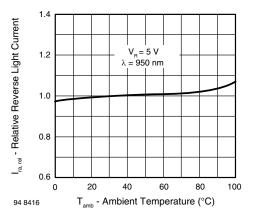


Fig. 2 - Relative Reverse Light Current vs. Ambient Temperature

For technical questions, contact: <u>detectortechsupport@vishay.com</u> THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT <u>www.vishay.com/doc?91000</u>



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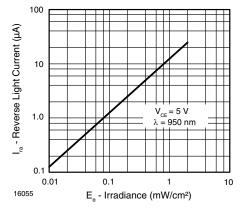


Fig. 3 - Reverse Light Current vs. Irradiance

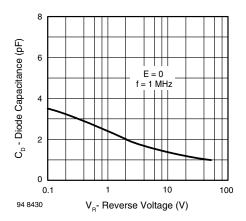


Fig. 4 - Diode Capacitance vs. Reverse Voltage

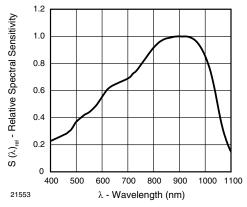


Fig. 5 - Relative Spectral Sensitivity vs. Wavelength

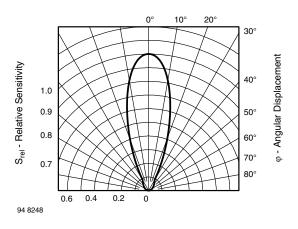


Fig. 6 - Relative Radiant Sensitivity vs. Angular Displacement

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REFLOW SOLDER PROFILE

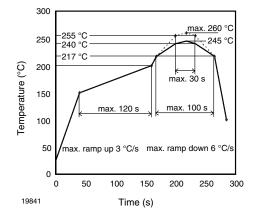


Fig. 7 - Lead (Pb)-free Reflow Solder Profile acc. J-STD-020D

PACKAGE DIMENSIONS in millimeters: VEMD2500X01

VEMD2500X01, VEMD2520X01

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DRYPACK

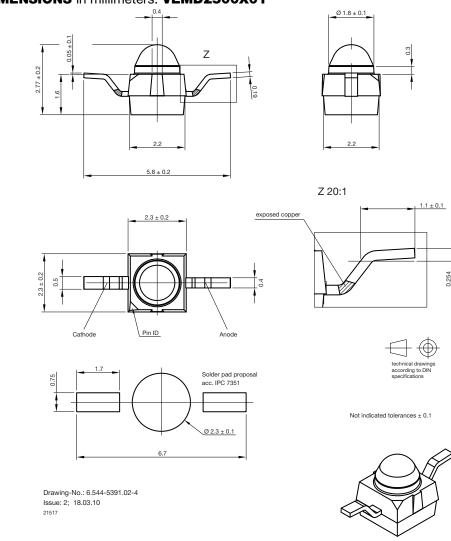
Devices are packed in moisture barrier bags (MBB) to prevent the products from moisture absorption during transportation and storage. Each bag contains a desiccant.

FLOOR LIFE

Floor life (time between soldering and removing from MBB) must not exceed the time indicated on MBB label: Floor life: 4 weeks Conditions: $T_{amb} < 30$ °C, RH < 60 % Moisture sensitivity level 2a, acc. to J-STD-020.

DRYING

In case of moisture absorption devices should be baked before soldering. Conditions see J-STD-020 or label. Devices taped on reel dry using recommended conditions 192 h at 40 $^{\circ}$ C (+ 5 $^{\circ}$ C), RH < 5 %.



Rev. 1.2, 18-Oct-11

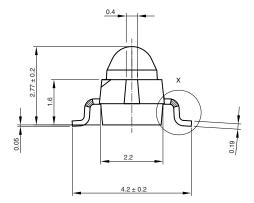
4 For technical questions, contact: <u>detectortechsupport@vishay.com</u> Document Number: 83294

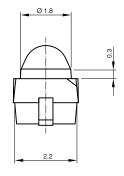
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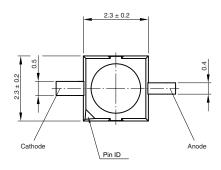
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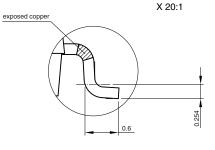
PACKAGE DIMENSIONS in millimeters: VEMD2520X01





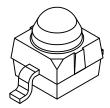


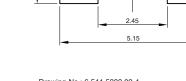






Not indicated tolerances ± 0.1





Solder pad proposal acc. IPC 7351

0.75

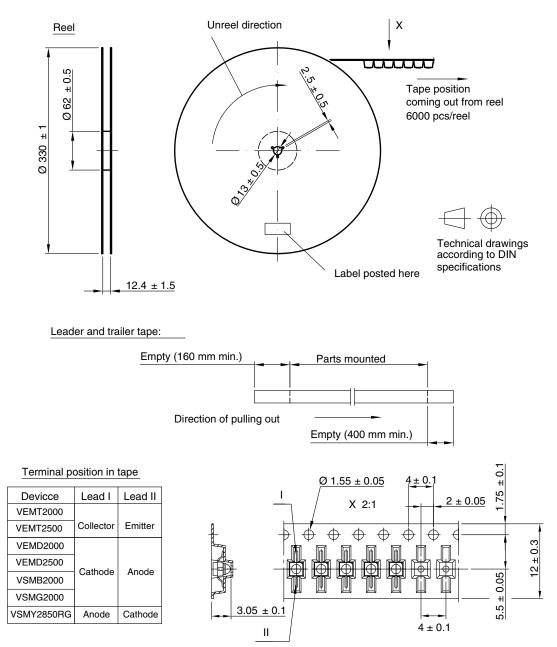
Drawing-No.: 6.544-5383.02-4 Issue: 4; 18.03.10 21488

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TAPING AND REEL DIMENSIONS in millimeters: VEMD2500X01



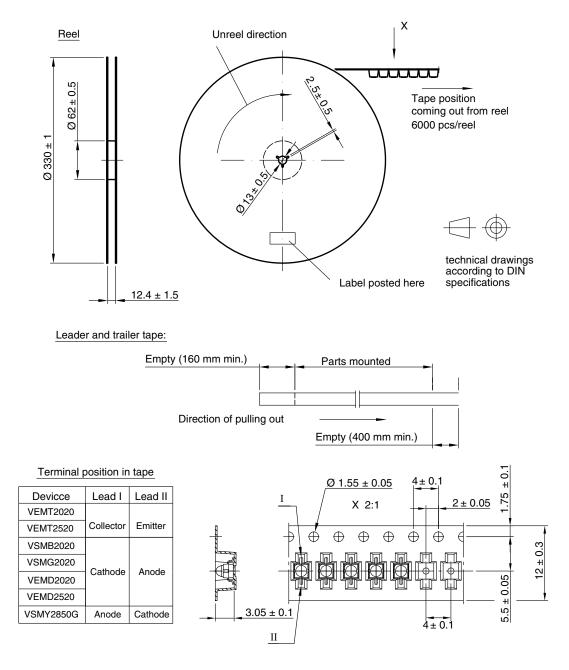
Drawing-No.: 9.800-5100.01-4 Issue: 2; 18.03.10 ²¹⁵⁷²

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TAPING AND REEL DIMENSIONS in millimeters: VEMD2520X01



Drawing-No.: 9.800-5091.01-4 Issue: 3; 18.03.10 21571

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