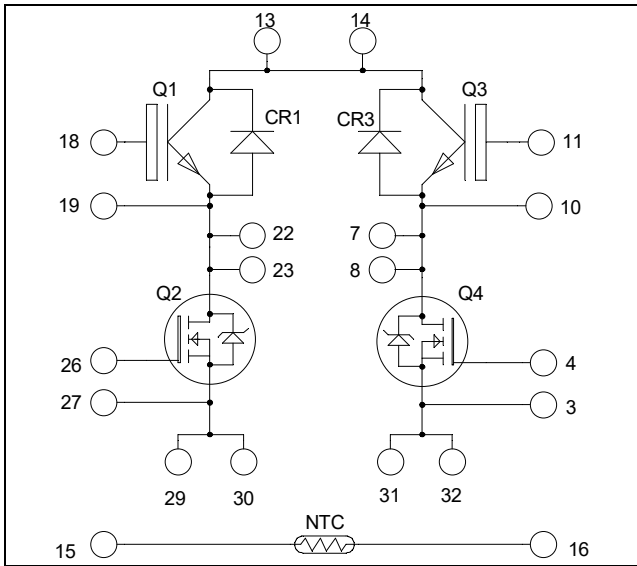


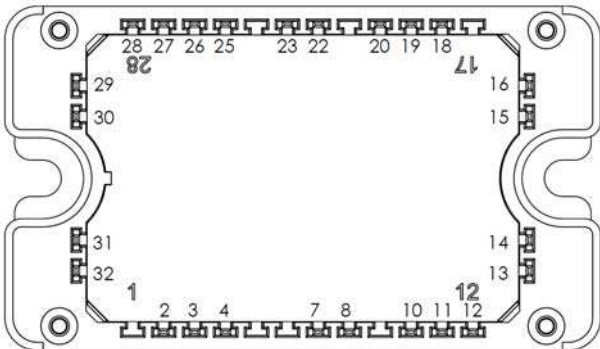
**Full – Bridge Power module**

**Trench & Field Stop IGBT3 Q1, Q3:**  
 $V_{CES} = 600V$  ;  $I_C = 50A$  @  $T_c = 80^\circ C$

**Super Junction MOSFET Q2, Q4:**  
 $V_{DSS} = 600V$  ;  $I_D = 49A$  @  $T_c = 25^\circ C$



Top switches : Trench + Field Stop IGBT3  
 Bottom switches : Super junction MOSFET



All multiple inputs and outputs must be shorted together  
 13/14 ; 22/23 ; 29/30 ; 31/32

**Application**

- Solar converter

**Features**

- **Q2, Q4 Super junction MOSFET**
  - Ultra low  $R_{DSon}$
  - Low Miller capacitance
  - Ultra low gate charge
  - Avalanche energy rated
- **Q1, Q3 Trench & Field Stop IGBT3**
  - Low voltage drop
  - Switching frequency up to 20 kHz
  - RBSOA & SCSOA rated
  - Low tail current

- Kelvin emitter for easy drive
- Very low stray inductance
- High level of integration
- Internal thermistor for temperature monitoring

**Benefits**

- Optimized conduction & switching losses
- Direct mounting to heatsink (isolated package)
- Low junction to case thermal resistance
- Solderable terminals both for power and signal for easy PCB mounting
- Low profile
- RoHS Compliant

**All ratings @  $T_j = 25^\circ C$  unless otherwise specified**

**CAUTION:** These Devices are sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed.

## 1. Top switches

### 1.1 Top Trench + Field Stop IGBT3 characteristics

#### Absolute maximum ratings

Symbol	Parameter	Max ratings	Unit
V <sub>CES</sub>	Collector - Emitter Voltage	600	V
I <sub>C</sub>	Continuous Collector Current	T <sub>C</sub> = 25°C	80
		T <sub>C</sub> = 80°C	50
I <sub>CM</sub>	Pulsed Collector Current	T <sub>C</sub> = 25°C	100
V <sub>GE</sub>	Gate - Emitter Voltage	±20	V
P <sub>D</sub>	Power Dissipation	T <sub>C</sub> = 25°C	176
RBSOA	Reverse Bias Safe Operating Area	T <sub>J</sub> = 150°C	100A @ 550V

#### Electrical Characteristics

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
I <sub>CES</sub>	Zero Gate Voltage Collector Current	V <sub>GE</sub> = 0V, V <sub>CE</sub> = 600V			250	μA
V <sub>CE(sat)</sub>	Collector Emitter Saturation Voltage	V <sub>GE</sub> = 15V	T <sub>J</sub> = 25°C	1.5	1.9	V
		I <sub>C</sub> = 50A		1.7		
V <sub>GE(th)</sub>	Gate Threshold Voltage	V <sub>GE</sub> = V <sub>CE</sub> , I <sub>C</sub> = 600μA	5.0	5.8	6.5	V
I <sub>GES</sub>	Gate - Emitter Leakage Current	V <sub>GE</sub> = 20V, V <sub>CE</sub> = 0V			600	nA

#### Dynamic Characteristics

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
C <sub>ies</sub>	Input Capacitance	V <sub>GE</sub> = 0V V <sub>CE</sub> = 25V f = 1MHz		3150		pF
C <sub>oes</sub>	Output Capacitance			200		
C <sub>res</sub>	Reverse Transfer Capacitance			95		
T <sub>d(on)</sub>	Turn-on Delay Time	Inductive Switching (25°C) V <sub>GE</sub> = ±15V V <sub>Bus</sub> = 300V I <sub>C</sub> = 50A R <sub>G</sub> = 8.2Ω		110		ns
T <sub>r</sub>	Rise Time			45		
T <sub>d(off)</sub>	Turn-off Delay Time			200		
T <sub>f</sub>	Fall Time			40		
T <sub>d(on)</sub>	Turn-on Delay Time	Inductive Switching (150°C) V <sub>GE</sub> = ±15V V <sub>Bus</sub> = 300V I <sub>C</sub> = 50A R <sub>G</sub> = 8.2Ω		120		ns
T <sub>r</sub>	Rise Time			50		
T <sub>d(off)</sub>	Turn-off Delay Time			250		
T <sub>f</sub>	Fall Time			60		
E <sub>on</sub>	Turn-on Switching Energy	V <sub>GE</sub> = ±15V V <sub>Bus</sub> = 300V I <sub>C</sub> = 50A R <sub>G</sub> = 8.2Ω	T <sub>J</sub> = 25°C	0.3		mJ
			T <sub>J</sub> = 150°C	0.43		
E <sub>off</sub>	Turn-off Switching Energy	I <sub>C</sub> = 50A R <sub>G</sub> = 8.2Ω	T <sub>J</sub> = 25°C	1.35		mJ
			T <sub>J</sub> = 150°C	1.75		
R <sub>thJC</sub>	Junction to Case Thermal resistance				0.85	°C/W

## 1.2 Top fast diode characteristics

Symbol	Characteristic	Test Conditions		Min	Typ	Max	Unit
V <sub>RRM</sub>	Peak Repetitive Reverse Voltage					600	V
I <sub>RM</sub>	Reverse Leakage Current	V <sub>R</sub> =600V				25	μA
I <sub>F</sub>	DC Forward Current		T <sub>c</sub> = 80°C		30		A
V <sub>F</sub>	Diode Forward Voltage	I <sub>F</sub> = 30A			1.8	2.3	V
		I <sub>F</sub> = 60A			2.1		
		I <sub>F</sub> = 30A	T <sub>j</sub> = 125°C		1.5		
t <sub>rr</sub>	Reverse Recovery Time	I <sub>F</sub> = 30A V <sub>R</sub> = 400V di/dt = 200A/μs	T <sub>j</sub> = 25°C		25		ns
Q <sub>rr</sub>	Reverse Recovery Charge		T <sub>j</sub> = 125°C		160		
			T <sub>j</sub> = 25°C		35		
			T <sub>j</sub> = 125°C		480		
R <sub>thJC</sub>	Junction to Case Thermal resistance					1.2	°C/W

## 2. Bottom switches

### 2.1 Bottom Super junction MOSFET characteristics

#### Absolute maximum ratings

Symbol	Parameter	Max ratings	Unit
V <sub>DSS</sub>	Drain - Source Voltage	600	V
I <sub>D</sub>	Continuous Drain Current	T <sub>c</sub> = 25°C	49
		T <sub>c</sub> = 80°C	38
I <sub>DM</sub>	Pulsed Drain current	130	A
V <sub>GS</sub>	Gate - Source Voltage	±20	V
R <sub>DS(on)</sub>	Drain - Source ON Resistance	45	mΩ
P <sub>D</sub>	Power Dissipation	T <sub>c</sub> = 25°C	290
I <sub>AR</sub>	Avalanche current (repetitive and non repetitive)	15	A
E <sub>AR</sub>	Repetitive Avalanche Energy	3	mJ
E <sub>AS</sub>	Single Pulse Avalanche Energy	1900	

#### Electrical Characteristics

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>GS</sub> = 0V, V <sub>DS</sub> = 600V			250	μA
R <sub>DS(on)</sub>	Drain - Source on Resistance	V <sub>GS</sub> = 10V, I <sub>D</sub> = 24.5A		40	45	mΩ
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>GS</sub> = V <sub>DS</sub> , I <sub>D</sub> = 3mA	2.1	3	3.9	V
I <sub>GSS</sub>	Gate - Source Leakage Current	V <sub>GS</sub> = ±20 V, V <sub>DS</sub> = 0V			100	nA

**Dynamic Characteristics**

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
C <sub>iss</sub>	Input Capacitance	V <sub>GS</sub> = 0V ; V <sub>DS</sub> = 25V		7.2		nF
C <sub>rss</sub>	Reverse Transfer Capacitance	f = 1MHz		0.29		
Q <sub>g</sub>	Total gate Charge	V <sub>GS</sub> = 10V V <sub>Bus</sub> = 300V I <sub>D</sub> = 49A		150		nC
Q <sub>gs</sub>	Gate – Source Charge			34		
Q <sub>gd</sub>	Gate – Drain Charge			51		
T <sub>d(on)</sub>	Turn-on Delay Time	<b>Inductive Switching (125°C)</b> V <sub>GS</sub> = 10V V <sub>Bus</sub> = 400V I <sub>D</sub> = 49A R <sub>G</sub> = 4.7Ω		21		ns
T <sub>r</sub>	Rise Time			30		
T <sub>d(off)</sub>	Turn-off Delay Time			100		
T <sub>f</sub>	Fall Time			45		
E <sub>on</sub>	Turn-on Switching Energy	<b>Inductive switching @ 25°C</b> V <sub>GS</sub> = 10V ; V <sub>Bus</sub> = 400V I <sub>D</sub> = 49A ; R <sub>G</sub> = 4.7Ω		675		μJ
E <sub>off</sub>	Turn-off Switching Energy			520		
E <sub>on</sub>	Turn-on Switching Energy	<b>Inductive switching @ 125°C</b> V <sub>GS</sub> = 10V ; V <sub>Bus</sub> = 400V I <sub>D</sub> = 49A ; R <sub>G</sub> = 4.7Ω		1100		μJ
E <sub>off</sub>	Turn-off Switching Energy			635		
R <sub>thJC</sub>	Junction to Case Thermal resistance				0.5	°C/W

**3. Temperature sensor** (see application note APT0406 on www.microsemi.com).

Symbol	Characteristic	Min	Typ	Max	Unit
R <sub>25</sub>	Resistance @ 25°C		50		kΩ
ΔR <sub>25</sub> /R <sub>25</sub>			5		%
B <sub>25/85</sub>	T <sub>25</sub> = 298.15 K		3952		K
ΔB/B		T <sub>C</sub> =100°C	4		%

$$R_T = \frac{R_{25}}{\exp\left[B_{25/85}\left(\frac{1}{T_{25}} - \frac{1}{T}\right)\right]}$$

T: Thermistor temperature  
R<sub>T</sub>: Thermistor value at T

**4. Package characteristics**

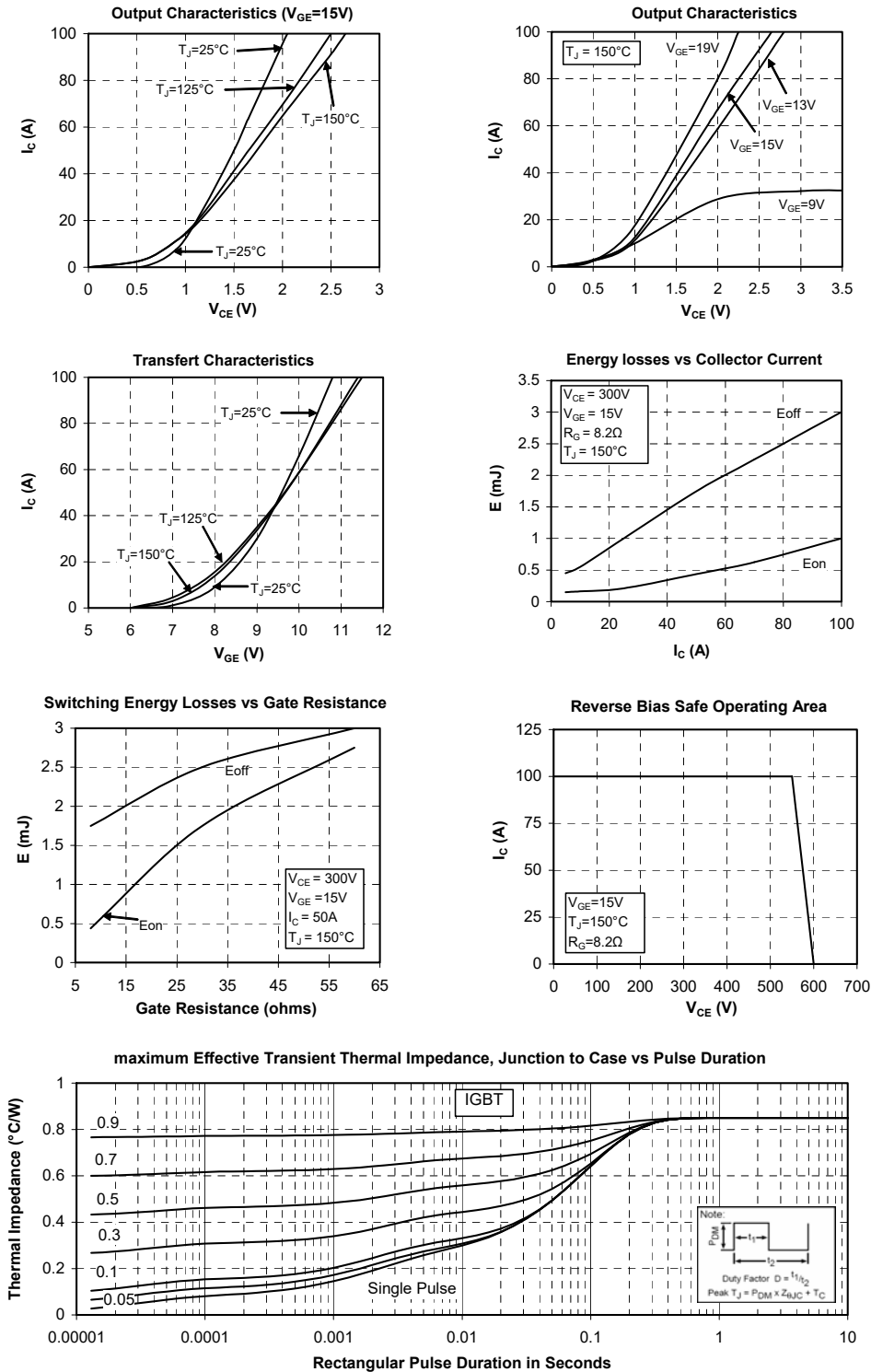
Symbol	Characteristic	Min	Max	Unit		
V <sub>ISOL</sub>	RMS Isolation Voltage, any terminal to case t=1 min, 50/60Hz	4000		V		
T <sub>J</sub>	Operating junction temperature range	-40	150*	°C		
T <sub>JOP</sub>	Recommended junction temperature under switching conditions	-40	T <sub>Jmax</sub> -25			
T <sub>STG</sub>	Storage Temperature Range	-40	125			
T <sub>C</sub>	Operating Case Temperature	-40	125			
Torque	Mounting torque	To heatsink	M4	2	3	N.m
Wt	Package Weight				110	g

T<sub>j</sub>=175°C for IGBT

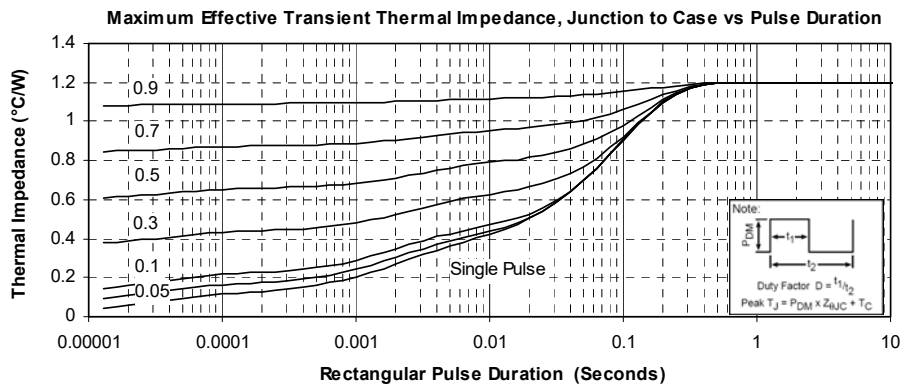
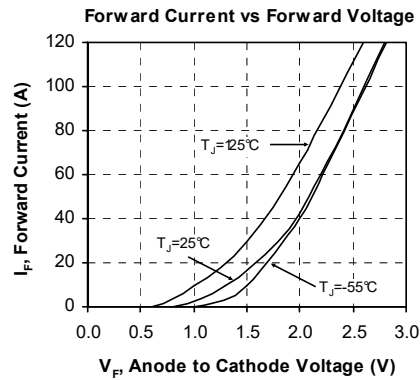


## 6. Top switches curves

### 6.1 Top Trench + Field Stop IGBT3 typical performance curves

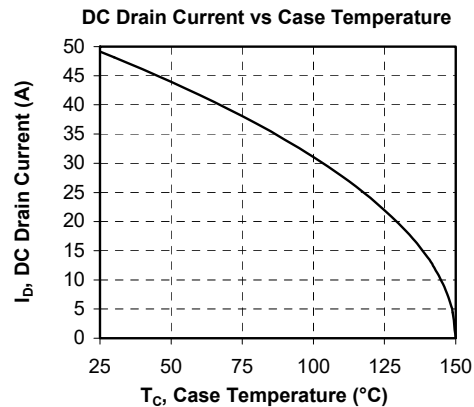
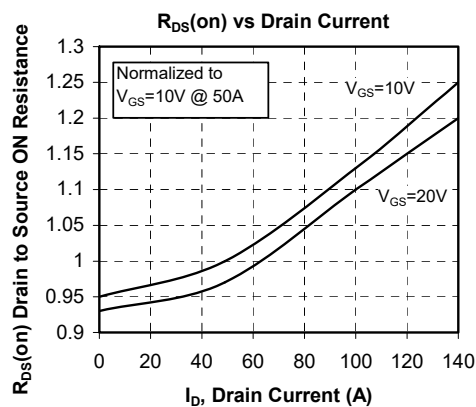
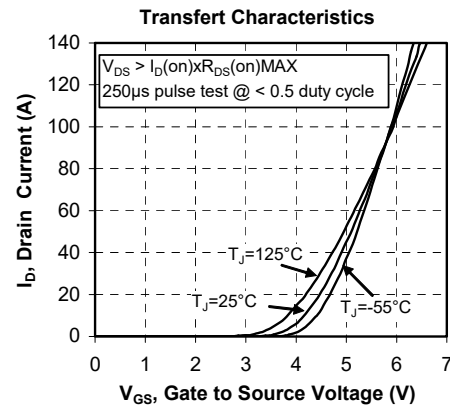
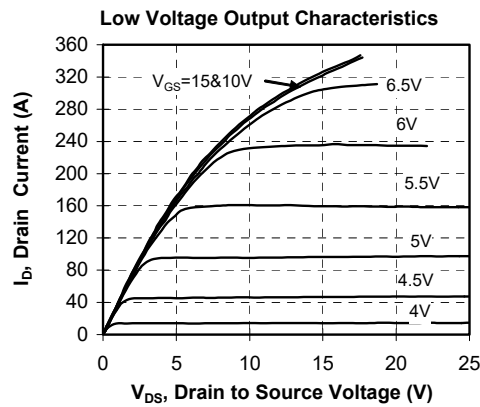
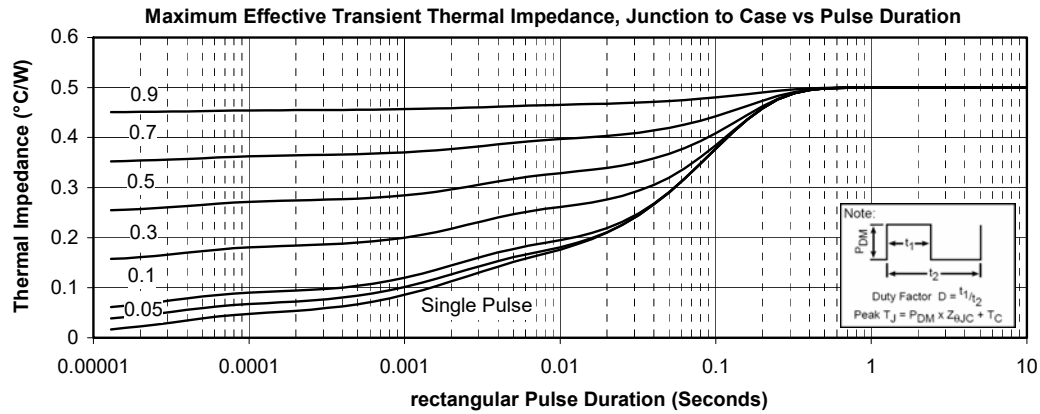


## 6.2 Top Fast diode typical performance curves

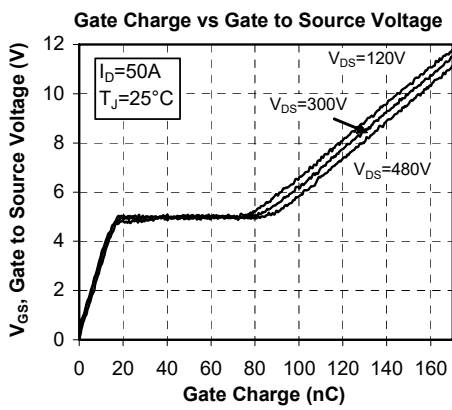
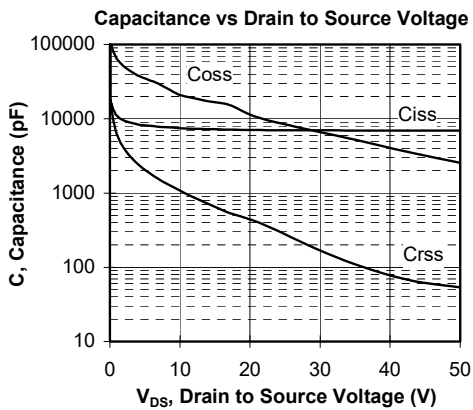
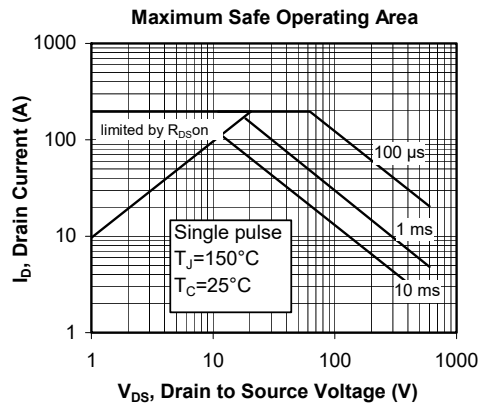
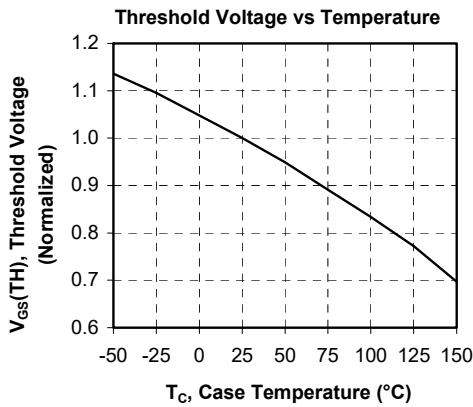
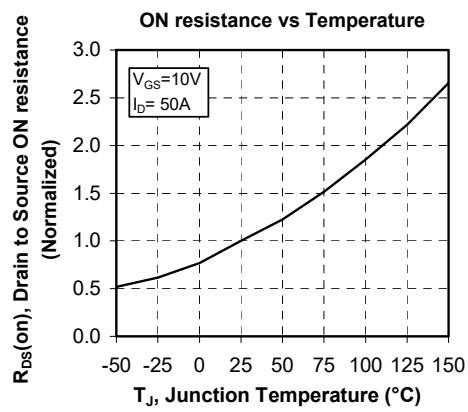
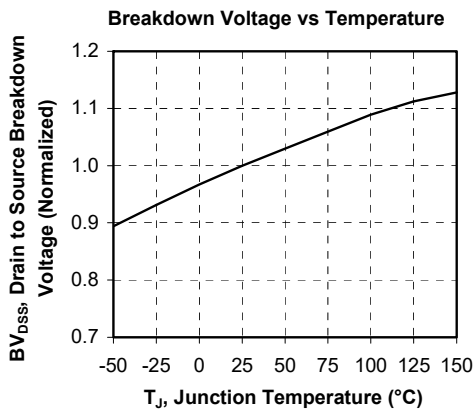


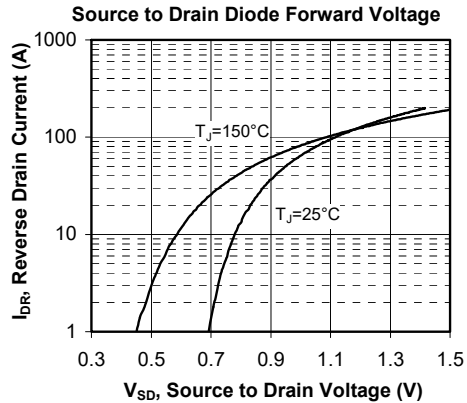
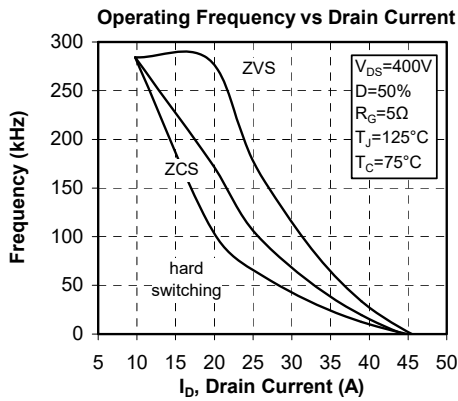
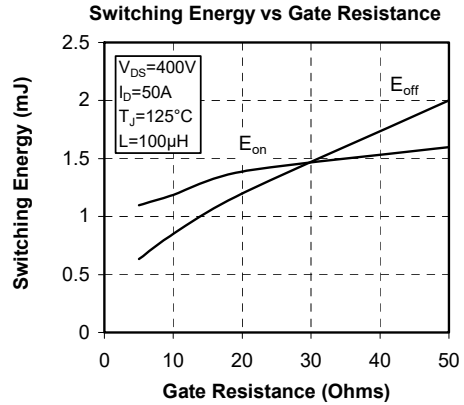
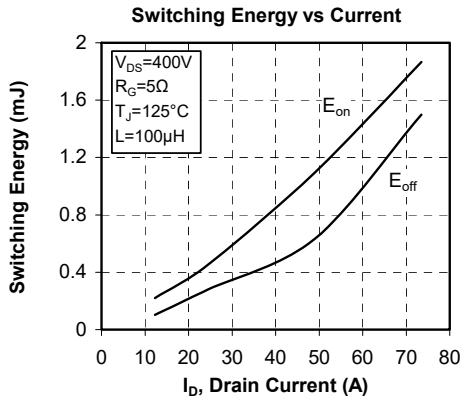
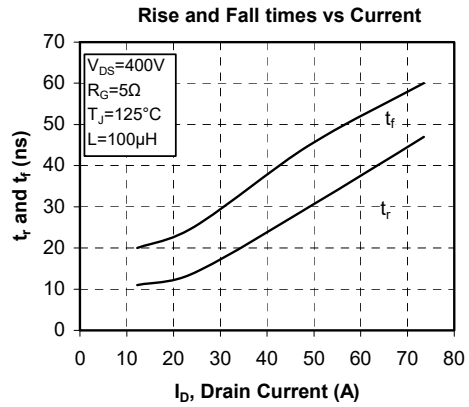
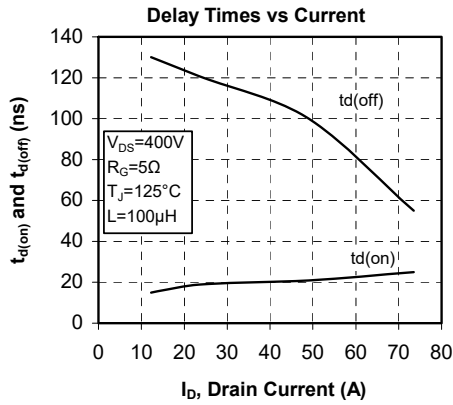
## 7. Bottom switches curves

### 7.1 Bottom Super junction MOSFET typical performance curves









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