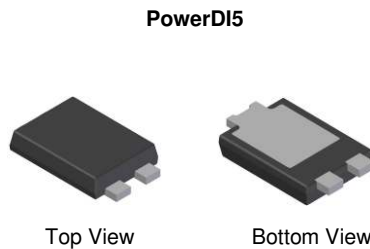


## Features

- Designed as Bypass Diodes for Solar Panels
- Selectively Rated for +200°C Maximum Junction Temperature for High Thermal Reliability
- Patented Super Barrier Rectifier Technology (SBR®)
- High Forward Surge Capability
- Ultra-Low Forward Voltage Drop
- Excellent High Temperature Stability
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. “Green” Device (Note 3)**
- **An automotive-compliant part is available under separate datasheet ([SBR10U45SP5Q](#))**

## Mechanical Data

- Package: PowerDI®5
- Package Material: Molded Plastic, “Green” Molding Compound; UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish – Matte Tin Annealed over Copper Leadframe; Solderable per MIL-STD-202, Method 208 ③
- Weight: 0.093 grams (Approximate)



**Note:** Pins Left & Right must be electrically connected at the printed circuit board.

## Ordering Information (Note 4)

Part Number	Package	Packing	
		Qty.	Carrier
SBR10U45SP5-13	PowerDI5	5,000	Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
  2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
  3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
  4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

## Marking Information



S10U45S = Product Type Marking Code  
 D = Manufacturer's Code Marking  
 K = Factory Designator  
 YYWW = Date Code Marking  
 YY = Last Two Digits of Year (ex: 23 for 2023)  
 WW = Week Code (01 to 53)

**Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load.  
For capacitive load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage	V <sub>RRM</sub>	45	V
Working Peak Reverse Voltage	V <sub>RWM</sub>		
DC Blocking Voltage	V <sub>RM</sub>		
RMS Reverse Voltage	V <sub>R(RMS)</sub>	32	V
Average Rectified Output Current	I <sub>O</sub>	10	A
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I <sub>FSM</sub>	275	A
Repetitive Peak Avalanche Power (1μs, +25°C)	P <sub>ARM</sub>	30,000	W

**Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Thermal Resistance Junction to Ambient (Note 5)	R <sub>θJA</sub>	73	°C/W
Thermal Resistance Junction to Ambient (Note 6)	R <sub>θJA</sub>	31	
Operating Temperature Range	V <sub>R</sub> ≤ 80% V <sub>RRM</sub>	-65 to +150	°C
	V <sub>R</sub> ≤ 50% V <sub>RRM</sub>	≤180	
	DC Forward Mode (Note 7)	≤200	
Storage Temperature Range	T <sub>STG</sub>	-65 to +175	°C

**Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 8)	V <sub>(BR)R</sub>	45	—	—	V	I <sub>R</sub> = 0.3mA
Forward Voltage Drop	V <sub>F</sub>	—	—	0.42	V	I <sub>F</sub> = 8A, T <sub>J</sub> = +25°C
		—	0.42	0.47		I <sub>F</sub> = 10A, T <sub>J</sub> = +25°C
		—	0.38	0.41		I <sub>F</sub> = 10A, T <sub>J</sub> = +125°C
Leakage Current (Note 8)	I <sub>R</sub>	—	0.05	0.3	mA	V <sub>R</sub> = 45V, T <sub>J</sub> = +25°C
		—	—	15		V <sub>R</sub> = 45V, T <sub>J</sub> = +100°C
		—	28.0	75		V <sub>R</sub> = 45V, T <sub>J</sub> = +150°C

- Notes:
5. FR-4 PCB, 2oz. copper. Minimum recommended pad layout per <http://www.diodes.com/package-outlines.html>.
  6. Polyimide PCB, 2oz. copper. Cathode pad dimensions 18.8mm x 14.4mm. Anode pad dimensions 5.6mm x 14.4mm.
  7. Max junction temperature guaranteed for 2 hours.
  8. Short duration pulse test used to minimize self-heating effect.

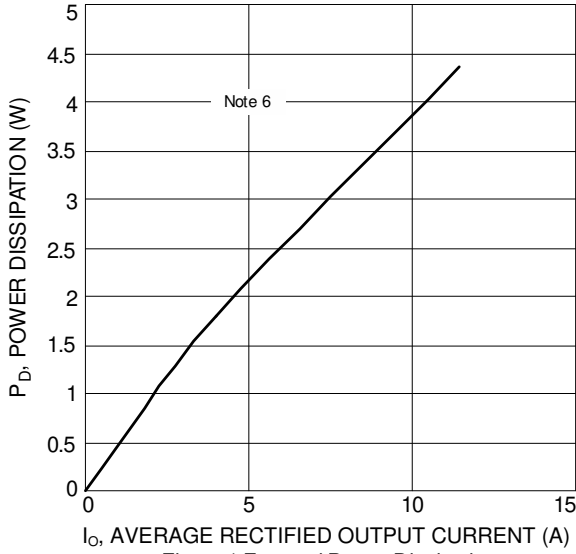


Figure 1 Forward Power Dissipation

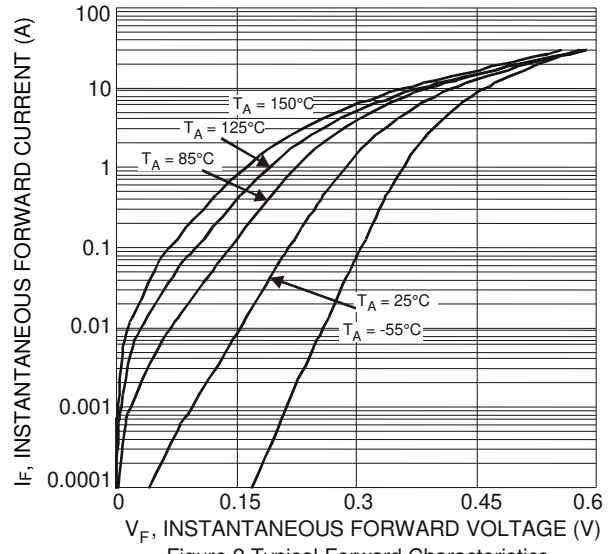


Figure 2 Typical Forward Characteristics

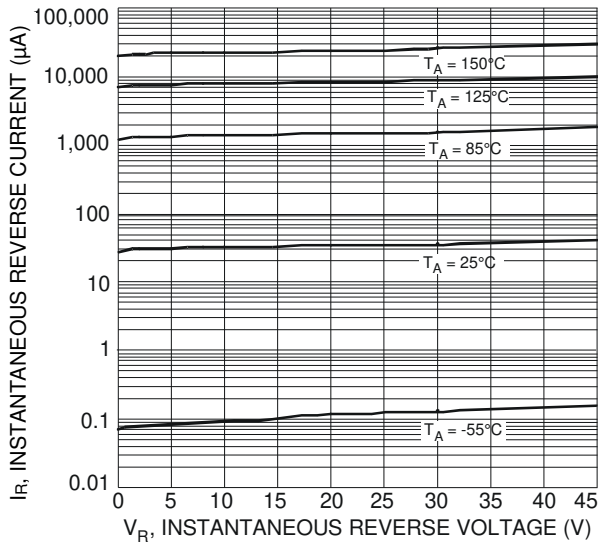


Figure 3 Typical Reverse Characteristics

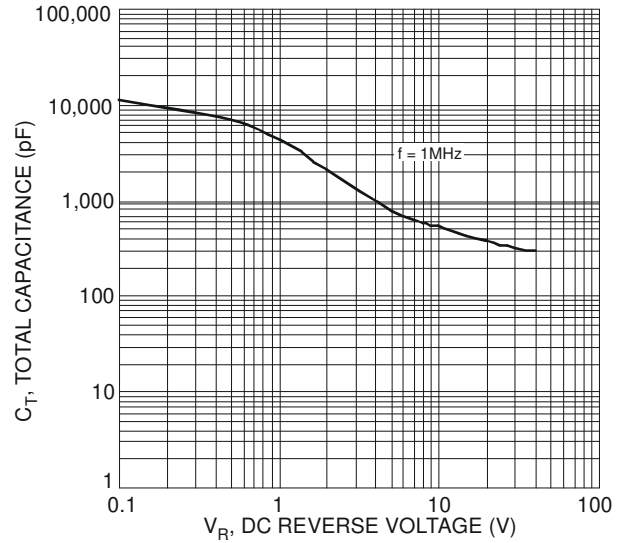


Figure 4 Total Capacitance vs. Reverse Voltage

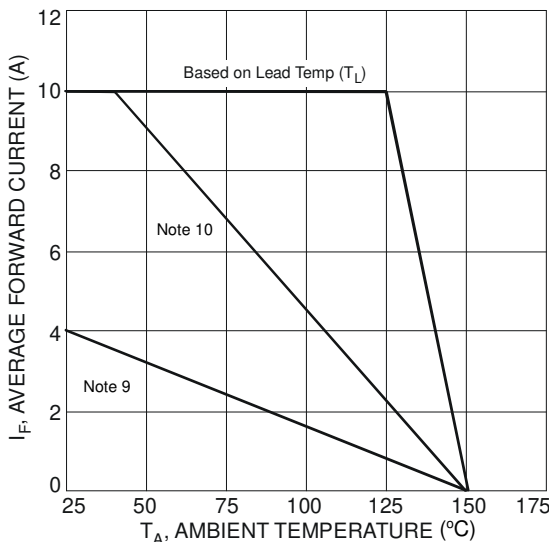


Figure 5 Forward Current Derating Curve

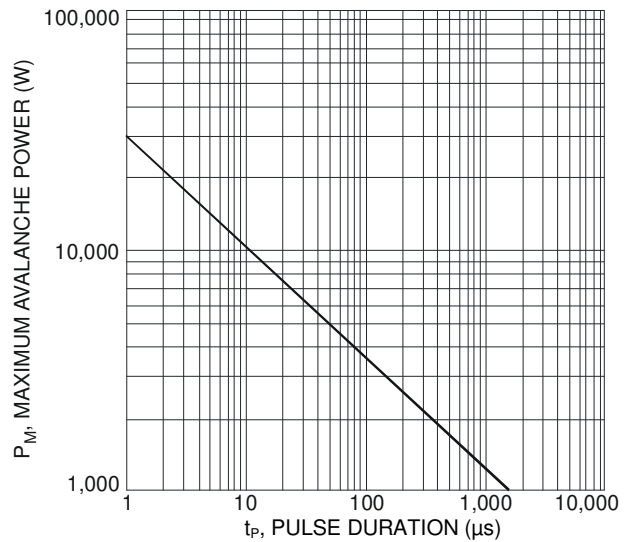


Figure 6 Maximum Avalanche Power

Notes: 9. Device mounted on FR-4 substrate, 2oz copper, with minimum recommended pad layout.  
10. Device mounted on FR-4 substrate, 2oz copper, with 10cm x 10cm pad layout.

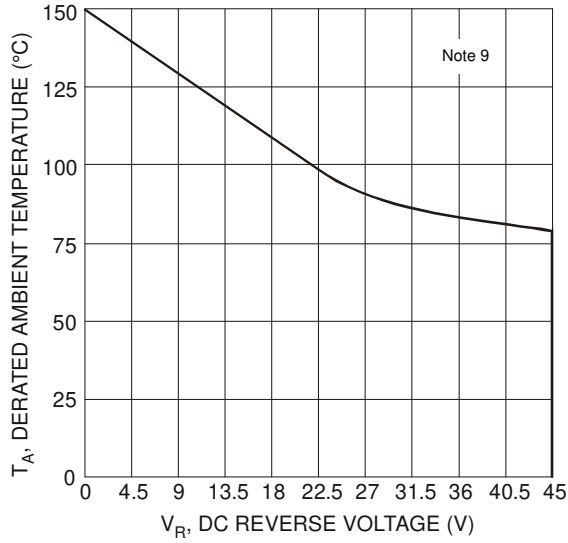
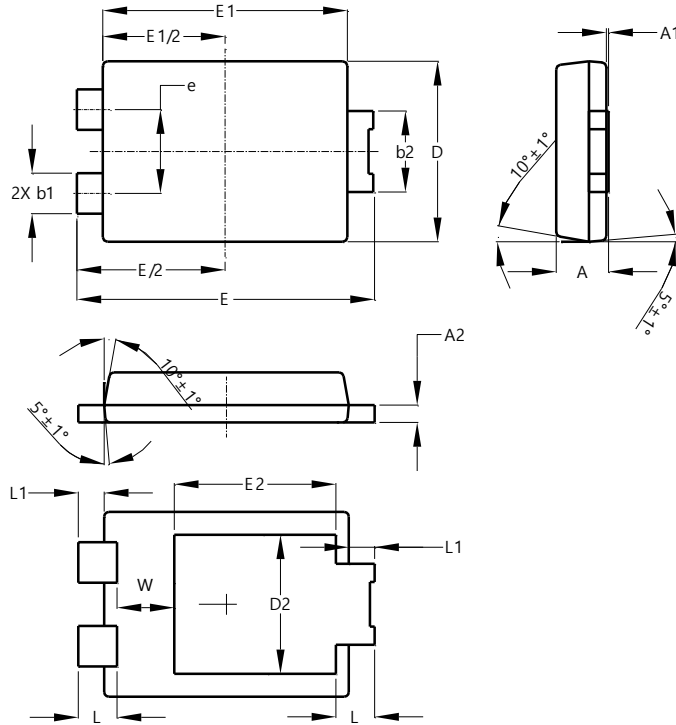


Figure 7 Operating Temperature Derating

**Package Outline Dimensions**

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

**PowerDI5**

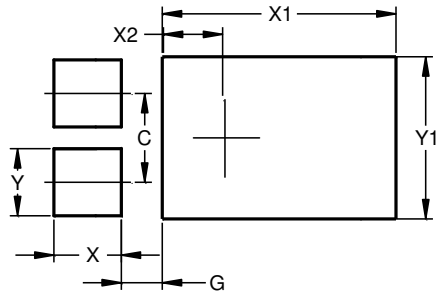


PowerDI5			
Dim	Min	Max	Typ
A	1.05	1.15	1.10
A1	0.00	0.05	--
A2	0.33	0.43	0.381
b1	0.80	0.99	0.89
b2	1.70	1.88	1.78
D	3.90	4.05	3.966
D2	--	--	3.054
E	6.40	6.60	6.51
e	--	--	1.84
E1	5.30	5.45	5.37
E2	--	--	3.549
L	0.75	0.95	0.85
L1	0.50	0.65	0.57
W	1.10	1.41	1.255
All Dimensions in mm			

**Suggested Pad Layout**

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

**PowerDI5**



Dimensions	Value (in mm)
C	1.840
G	0.852
X	1.400
X1	4.860
X2	1.310
Y	1.390
Y1	3.360

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