

## Vishay Semiconductors

# **Small Signal Fast Switching Diode**



#### **FEATURES**

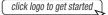
- Silicon epitaxial planar diode
- Fast switching diodes
- AEC-Q101 qualified available
- Base P/N-E3 RoHS-compliant, commercial grade



ROHS

- Base P/N-HE3 RoHS-compliant, AEC-Q101 qualified
- Material categorization: for definitions of compliance please see <a href="https://www.vishav.com/doc?99912">www.vishav.com/doc?99912</a>

### **DESIGN SUPPORT TOOLS**





#### **MECHANICAL DATA**

Case: SOD-123

Weight: approx. 10.3 mg
Packaging codes / options:

18/10K per 13" reel (8 mm tape), 10K/box 08/3K per 7" reel (8 mm tape), 15K/box

PARTS TABLE					
PART	ORDERING CODE	TYPE MARKING	CIRCUIT CONFIGURATION	REMARKS	
1N4148W	1N4148W-E3-08 or 1N4148W-E3-18	A2	Single	Tape and reel	
	1N4148W-HE3-08 or 1N4148W-HE3-18	A2	Single		

<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Reverse voltage		V <sub>R</sub>	75	V	
Repetitive peak reverse voltage		$V_{RRM}$	100	V	
Average rectified current half wave rectification with resistive load <sup>(1)</sup>	f ≥ 50 Hz	I <sub>F(AV)</sub>	150	mA	
Surge forward current	t <sub>p</sub> < 1 s	I <sub>FSM</sub>	500	mA	
Surge forward current	t <sub>p</sub> = 1 μs	I <sub>FSM</sub>	2	Α	
Power dissipation (1)		P <sub>tot</sub>	350	mW	

THERMAL CHARACTERISTICS (T <sub>amb</sub> = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Thermal resistance junction to ambient air (1)		R <sub>thJA</sub>	357	K/W	
Junction temperature		Tj	150	°C	
Storage temperature range		T <sub>stg</sub>	-65 to +150	°C	
Operating temperature range		T <sub>op</sub>	-55 to +150	°C	

#### Note

<sup>(1)</sup> Valid provided that electrodes are kept at ambient temperature.



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<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Forward voltage	$I_F = 10 \text{ mA}$	V <sub>F</sub>			1	V
Torward voitage	I <sub>F</sub> = 100 mA	$V_{F}$			1.2	V
	$V_R = 20 \text{ V}$	I <sub>R</sub>			25	nA
Leakage current	V <sub>R</sub> = 75 V	I <sub>R</sub>			5	μΑ
Leakage current	V <sub>R</sub> = 100 V	I <sub>R</sub>			100	μΑ
	$V_R = 20 \text{ V}, T_J = 150 ^{\circ}\text{C}$	I <sub>R</sub>			50	μΑ
Diode capacitance	$V_F = V_R = 0 V$	C <sub>D</sub>			4	pF
Voltage rise when switching ON	Tested with 50 mA pulses, $t_p = 0.1 \mu s$ , rise time < 30 ns, $f_p = (5 \text{ to } 100) \text{ kHz}$	V <sub>fr</sub>			2.5	V
Reverse recovery time	$I_F$ = 10 mA, $i_R$ = 1 mA, $V_R$ = 6 V, $R_L$ = 100 $\Omega$	t <sub>rr</sub>			4	ns

### TYPICAL CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)

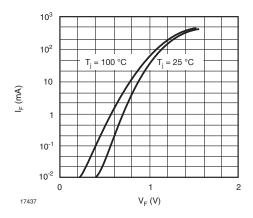


Fig. 1 - Forward Characteristics

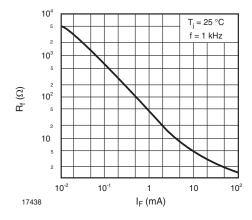


Fig. 2 - Dynamic Forward Resistance vs. Forward Current

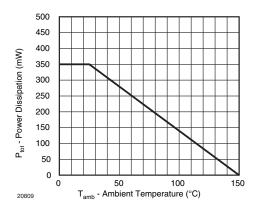


Fig. 3 - Admissible Power Dissipation vs. Ambient Temperature

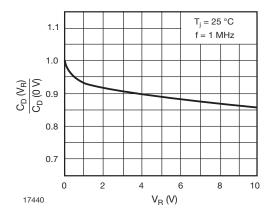


Fig. 4 - Relative Capacitance vs. Reverse Voltage



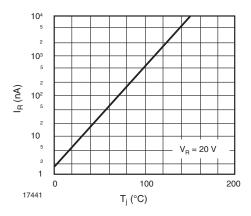


Fig. 5 - Leakage Current vs. Junction Temperature

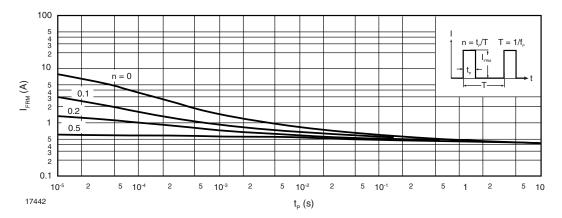
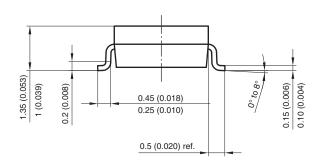


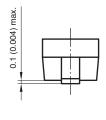
Fig. 6 - Admissible Repetitive Peak Forward Current vs. Pulse Duration



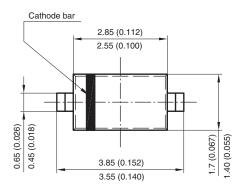
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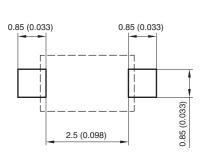
### PACKAGE DIMENSIONS in millimeters (inches): SOD-123





Mounting Pad Layout





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