Product data sheet

1. Product profile

1.1 General description

NPN high-voltage transistor in a medium power and flat lead SOT89 (SC-62) Surface-Mounted Device (SMD) plastic package.

PNP complement: PXTA92.

1.2 Features and benefits

- High breakdown voltage
- AEC-Q101 qualified
- Medium power and flat lead SMD plastic package

1.3 Applications

- Electronic ballast for fluorescent lighting
- LED driver for LED chain module
- High Intensity Discharge (HID) front lighting
- Automotive motor management
- Hook switch for wired telecom
- Switch Mode Power Supply (SMPS)

1.4 Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V_{CEO}	collector-emitter voltage	open base	-	-	300	V
I _C	collector current		-	-	100	mA
I _{CM}	peak collector current		-	-	200	mA
h _{FE}	DC current gain	$V_{CE} = 10 \text{ V};$ $I_{C} = 30 \text{ mA}$	40	-	-	



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2. Pinning information

Table 2. Pinning

	9		
Pin	Description	Simplified outline	Graphic symbol
1	emitter		
2	collector		. J
3	base	3 2 1	3 — 1 sym042

3. Ordering information

Table 3. Ordering information

Type number	Package			
	Name	Description	Version	
PXTA42	SC-62	plastic surface-mounted package; exposed die pad for good heat transfer; 3 leads	SOT89	

4. Marking

Table 4. Marking codes

Type number	Marking code ^[1]
PXTA42	*1D

^{[1] * =} placeholder for manufacturing site code

5. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V_{CBO}	collector-base voltage	open emitter	-	300	V
V_{CEO}	collector-emitter voltage	open base	-	300	V
V_{EBO}	emitter-base voltage	open collector	-	6	V
I _C	collector current		-	100	mA
I _{CM}	peak collector current		-	200	mA
I _{BM}	peak base current		-	100	mA
P _{tot}	total power dissipation	$T_{amb} \leq 25 ^{\circ}C$	<u>[1]</u> _	1300	mW
Tj	junction temperature		-	150	°C
T _{amb}	ambient temperature		-65	+150	°C
T _{stg}	storage temperature		-65	+150	°C

^[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for collector 6 cm².

6. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1] -	-	96	K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point		-	-	16	K/W

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7. Characteristics

Table 7. Characteristics

T_{amb} = 25 °C unless otherwise specified.

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Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I _{CBO}	collector-base cut-off current	$V_{CB} = 200 \text{ V}; I_E = 0 \text{ A}$	-	-	100	nA
I _{EBO}	emitter-base cut-off current	$V_{EB} = 6 \text{ V}; I_{C} = 0 \text{ A}$	-	-	100	nA
h _{FE}	DC current gain	$V_{CE} = 10 \text{ V}; I_{C} = 1 \text{ mA}$	25	-	-	
		$V_{CE} = 10 \text{ V}; I_{C} = 10 \text{ mA}$	40	-	-	
		$V_{CE} = 10 \text{ V}; I_{C} = 30 \text{ mA}$	40	-	-	
V _{CEsat}	collector-emitter saturation voltage	$I_C = 20 \text{ mA}; I_B = 2 \text{ mA}$	-	-	500	mV
V _{BEsat}	base-emitter saturation voltage	$I_C = 20 \text{ mA}; I_B = 2 \text{ mA}$	-	-	900	mV
f _T	transition frequency	$V_{CE} = 20 \text{ V}; I_{C} = 10 \text{ mA};$ f = 100 MHz	50	-	-	MHz
C _{re}	feedback capacitance	$V_{CB} = 20 \text{ V}; I_{C} = I_{c} = 0 \text{ A};$ f = 1 MHz	-	-	3	pF

8. Test information

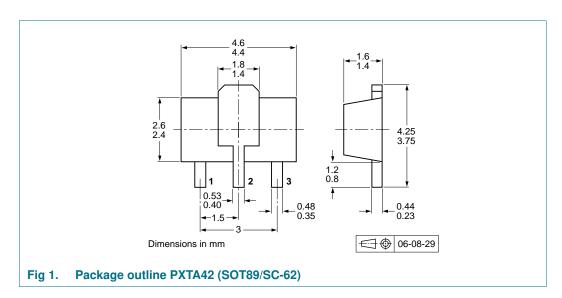
8.1 Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 - Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

^[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm².

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9. Package outline



10. Packing information

Table 8. Packing methods

The indicated -xxx are the last three digits of the 12NC ordering code.[1]

Type number	Package	Description I		Packing quanti	
				1000	4000
PXTA42	SOT89	8 mm pitch, 12 mm tape and reel; T1	2]	-115	-135
		8 mm pitch, 12 mm tape and reel; T3	3]	-120	-

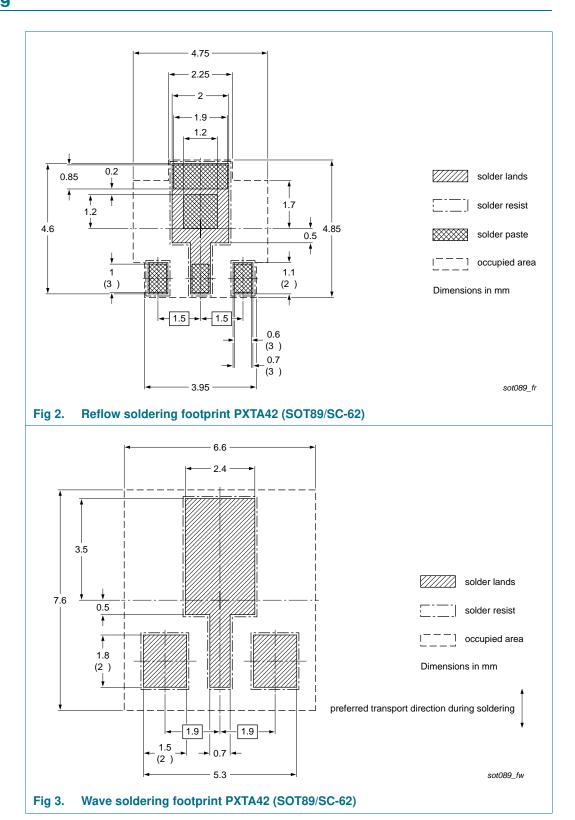
^[1] For further information and the availability of packing methods, see $\underline{\text{Section 14}}$.

^[2] T1: normal taping

^[3] T3: 90° taping

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11. Soldering



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12. Revision history

Table 9. Revision history

	-						
Document ID	Release date	Data sheet status	Change notice	Supersedes			
PXTA42 v.5	20110711	Product data sheet	-	PXTA42 v.4			
Modifications:		f this document has been r NXP Semiconductors.	edesigned to comply v	vith the new identity			
	 Legal texts h 	ave been adapted to the ne	ew company name wh	ere appropriate.			
	 Section 1.1 " 	General description": amer	nded				
	 Section 1.2 " 	Features and benefits": am	ended				
	 <u>Section 1.3 "Applications"</u>: amended 						
	Section 1.4 "Quick reference data": added						
	Figure 1: superseded by minimized package outline drawing						
	Section 8 "Test information": added						
	Section 10 "Packing information": added						
	Section 11 "S	Soldering": added					
	 Section 13 "L 	<u>egal information": updated</u>					
PXTA42 v.4	20041209	Product data sheet	-	PXTA42 v.3			
PXTA42 v.3	19990426	Product specification	-	PXTA42_43_CNV v.2			
PXTA42_43_CNV v.2	19970618	Product specification	-	-			

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13. Legal information

13.1 Data sheet status

Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions"
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL http://www.nexperia.com.

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PXTA42

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