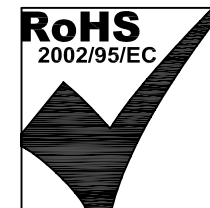


NOTE:

1. UNUSED PIN P7 IS OMITTED.

ELECTRICAL SPECIFICATIONS:

- |                               |                              |  |
|-------------------------------|------------------------------|--|
| 1.0 TURNS RATIO:              | (P4-P5-P6) : (J3-J6)         | : 1CT : 1CT ± 3%                         |
|                               | (P3-P2-P1) : (J1-J2)         | : 1CT : 1.41CT ± 3%                      |
| 2.0 INDUCTANCE:               | (P4-P6)                      | : 350uH MIN. @ 0.1V, 100KHz, 8mA DC Bias |
|                               | (P3-P1)                      | : 350uH MIN. @ 0.1V, 100KHz, 8mA DC Bias |
| 3.0 LEAKAGE INDUCTANCE:       | P6-P4 (WITH J6 AND J3 SHORT) | : 0.3 MAX. @ 1MHZ                        |
|                               | P3-P1 (WITH J2 AND J1 SHORT) | : 0.3 MAX. @ 1MHZ                        |
| 4.0 INTERWINDING CAPACITANCE: | (P6,P5,P4) TO (J6,J3)        | : 30pf MAX @ 1MHZ                        |
|                               | (P3,P2,P1) TO (J2,J1)        | : 30pf MAX. @ 1MHZ                       |
| 5.0 DC RESISTANCE:            | (J6-J3)=(J2-J1)              | : 1.2 ohms Max.                          |



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X0

|   | TRANSMIT                     | RECEIVE                  |
|---|------------------------------|--------------------------|
| 6.0 RETURN LOSS: (P6-P4)=100 OHMS AND (P1-P3)=50 OHM REF.<br>1MHz TO 30MHz<br>60MHz TO 80MHz    | : -12dB MIN.                 | -18dB MIN.<br>-12dB MIN. |
| NOTE: 100 OHMS CONNECTED TO (J2-J1) OR (J6-J3).   |                              |                          |
| 7.0 DIELECTRIC WITHSTAND: (J1, J2) TO (P1, P3)<br>(J3, J6) TO (P4, P6)                          | : 1500 VAC<br>: 1500 VAC     | 1500 VAC<br>1500 VAC     |
| 8.0 INSERTION LOSS: RS=RL=100 OHMS<br>100KHz TO 100MHz  | : -1.1 dB TYP                | -1.1 dB TYP              |
| 9.0 RISE TIME: RS=100 OHMS AND RL = 100 OHMS<br>OUTPUT VOLTAGE = 1 V peak<br>PULSE WIDTH= 112nS | : 3.0 nS MAX<br>: 3.0 nS MAX | 3.0 nS MAX<br>3.0 nS MAX |
| 10.0 CROSS TALK: 1-100 MHz  | : -30 dB TYP                 | -30 dB TYP               |
| 11.0 COMMON TO COMMON MODE ATTENUATION: 1MHz TO 100MHz  | : -35 dB TYP                 | -35 dB TYP               |

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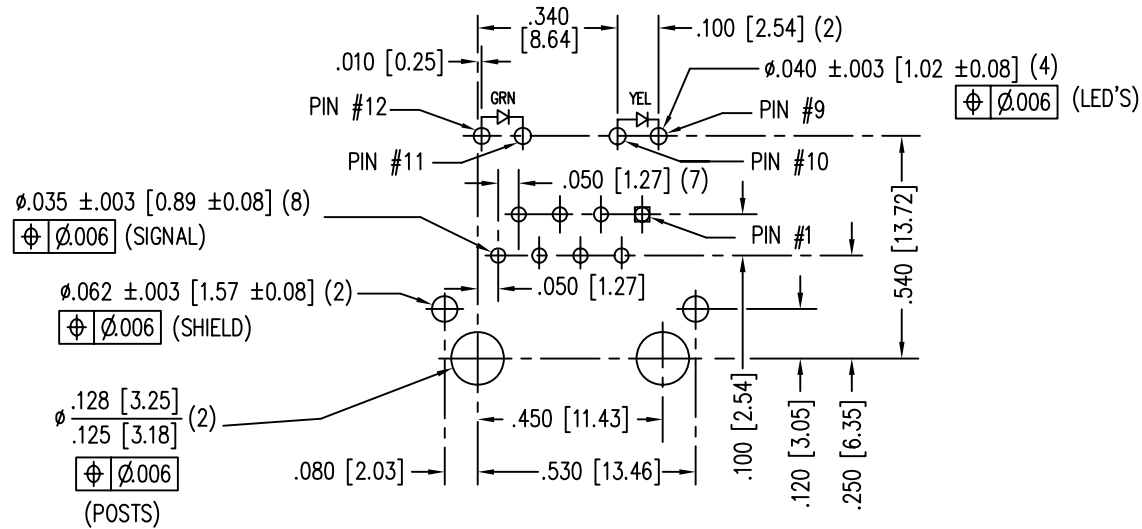
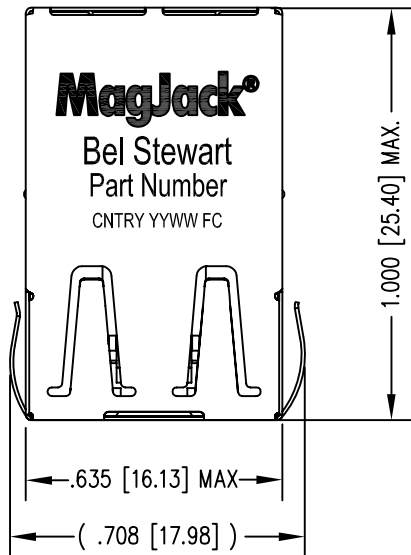
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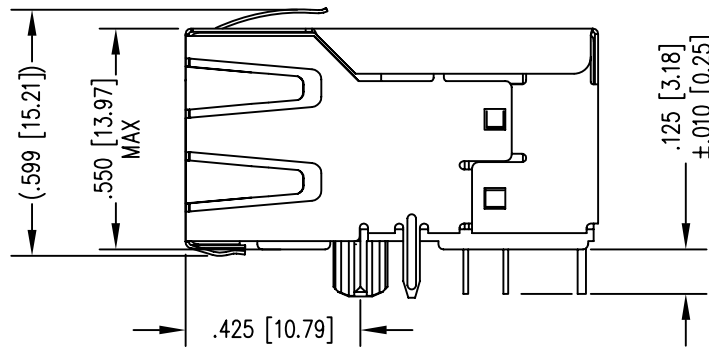
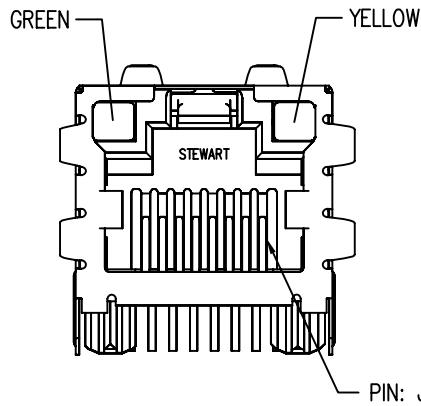
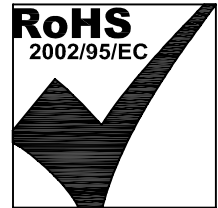
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P.C.B. RECOMMENDED HOLE LAYOUT  
 SEEN FROM COMPONENT SIDE  
 ALL CENTERLINE DIMENSIONS ARE BASIC.



NOTES:

- CONNECTOR MATERIALS:  
 HOUSING: THERMOPLASTIC UL94 V-0  
 CONTACT/SHIELD: COPPER ALLOY  
 SHIELD PLATING: NICKEL OR TIN  
 CONTACT PLATING: SELECTIVE GOLD,  
 50 MICRO-INCHES MIN. IN CONTACT AREA.
- PIN NOT ELECTRICALLY CONNECTED MAYBE OMITTED.  
 SEE ELECTRICAL DRAWING FOR OMITTED PINS.
- TOLERANCES COMPLY WITH F.C.C. DIMENSION REQUIREMENTS.
- ALL TOLERANCES NOT OTHERWISE SPECIFIED TO BE ±.005 [0.13]
- REFLOW AND WAVE SOLDER COMPATIBLE-260°C FOR 10 SECONDS MAX.

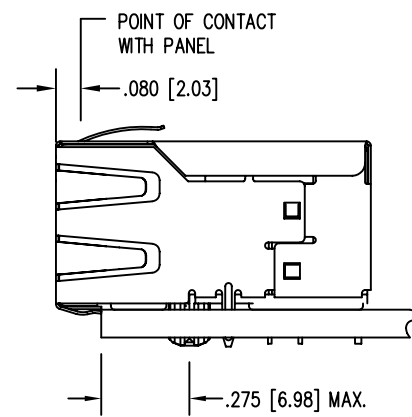
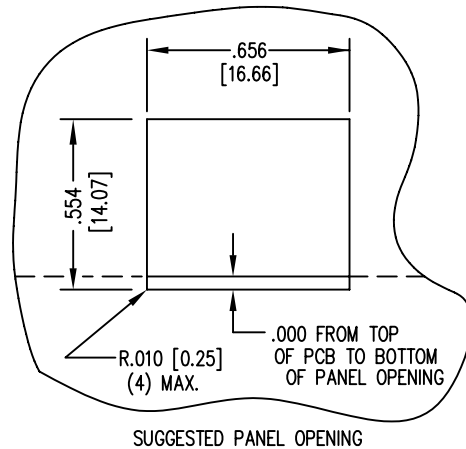
| LED SPECIFICATION |            |                 |        |
|-------------------|------------|-----------------|--------|
| STANDARD LED      | WAVELENGTH | FORWARD V (MAX) | *(TYP) |
| GREEN             | 565 nm     | 2.5 V           | 2.2 V  |
| YELLOW            | 590 nm     | 2.5 V           | 2.1 V  |

\*WITH A FORWARD CURRENT OF 20 mA (TYP)

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|                 |                           |            |
|-----------------|---------------------------|------------|
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|-----------------|---------------------------|------------|



1. THE SUGGESTED PANEL OPENING IS INTENDED TO GIVE THE USER THE ABILITY TO HAVE REASONABLE JACK / PANEL CLEARANCES YET MAINTAIN RELIABLE GROUNDING CAPABILITY.
2. ALL TOLERANCES NOT OTHERWISE SPECIFIED TO BE  $\pm 0.005$  [0.13]

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SHEET 4 OF 4

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REV. 09