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November 2013

FDPF12N50UT N-Channel UniFETTM Ultra FRFETTM MOSFET 500 V, 10 A, 800 m Ω

Features

- + $R_{DS(on)}$ = 650 m Ω (Typ.) @ V_{GS} = 10 V, I_D = 5 A
- Low Gate Charge (Typ. 21 nC)
- Low C_{rss} (Typ. 11 pF)
- 100% Avalanche Tested
- Improved dv/dt Capability
- RoHS Compliant

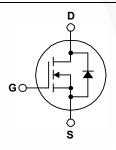
Applications

- LCD/LED/PDP TV
- Lighting
- Uninterruptible Power Supply

Description

UniFETTM MOSFET is Fairchild Semiconductor's high voltage MOSFET family based on planar stripe and DMOS technology. This MOSFET is tailored to reduce on-state resistance, and to provide better switching performance and higher avalanche energy strength. UniFET Ultra FRFETTM MOSFET has much superior body diode reverse recovery performance. Its trr is less than 50nsec and the reverse dv/dt immunity is 20V/nsec while normal planar MOSFETs have over 200nsec and 4.5V/nsec respectively. Therefore UniFET Ultra FRFET MOSFET can remove additional component and improve system reliability in certain applications that require performance improvement of the MOSFET's body diode. This device family is suitable for switching power converter applications such as power factor correction (PFC), flat panel display (FPD) TV power, ATX and electronic lamp ballasts.





MOSFET Maximum Ratings T_C = 25°C unless otherwise noted.

Symbol	Parameter			FDPF12N50UT	Unit	
V _{DSS}	Drain to Source Voltage			500	V	
V _{GSS}	Gate to Source Voltage			±30	V	
	Drain Current	- Continuous (T _C = 25 ^o C)		10*	10*	
D		- Continuous (T _C = 100 ^o C)		6*	A	
I _{DM}	Drain Current	- Pulsed	- Pulsed (Note 1)		А	
E _{AS}	Single Pulsed Avalanche Energy (Note 2)		(Note 2)	456	mJ	
I _{AR}	Avalanche Current		(Note 1)	10	А	
E _{AR}	Repetitive Avalanche Energy		(Note 1)	16.5	mJ	
dv/dt	Peak Diode Recovery dv/dt		(Note 3)	20	V/ns	
P _D	Dewer Dissinction	(T _C = 25 ^o C)		42	W	
	Power Dissipation	- Derate Above 25°C		0.3	W/ºC	
T _J , T _{STG}	Operating and Storage Temperature Range			-55 to +150	°C	
TL	Maximum Lead Tempera	ture for Soldering, 1/8" from Case fo	r 5 Seconds	300	°C	

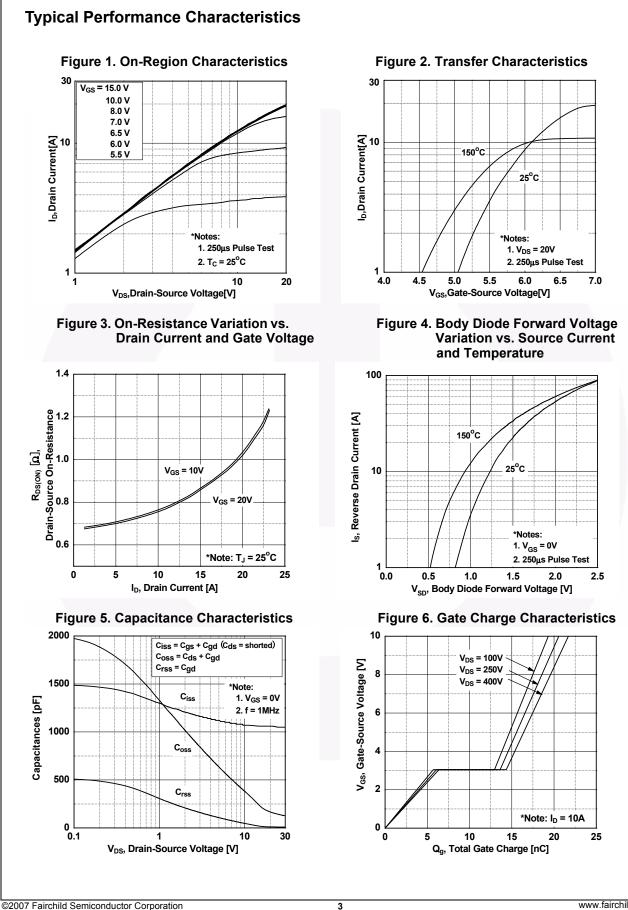
*Drain current limited by maximum junction temperature.

Thermal Characteristics

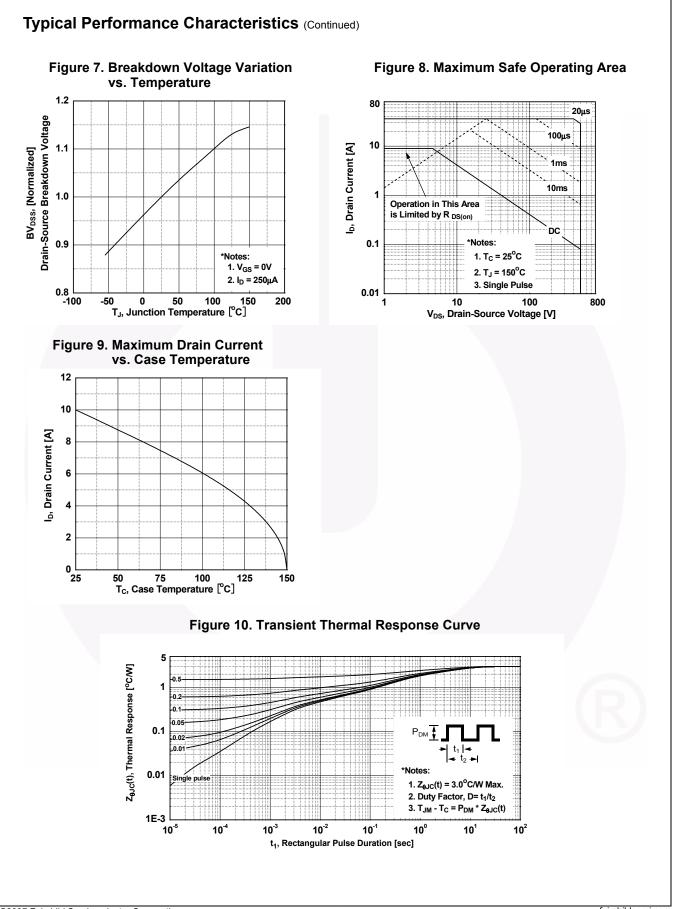
Symbol	Parameter	FDPF12N50UT	Unit
$R_{ extsf{ heta}JC}$	Thermal Resistance, Junction to Case, Max.	3.0	°C/W
$R_{ extsf{ heta}JA}$	Thermal Resistance, Junction to Ambient, Max.	62.5	0/00

Part Number Top Mark		Package	Packing Method	Reel Size	e Ta	ape Width	Qu	antity	
FDPF12	N50UT	FDPF12N50UT	TO-220F	Tube	N/A		N/A	50	units
Electrica	I Chara	cteristics T _C = 25°C	C unless oth	erwise noted.					
Symbol		Parameter		Test Conditions	s	Min.	Тур.	Max.	Unit
Off Charac	teristics								
BV _{DSS}	Drain to S	ource Breakdown Voltage	ln i	= 250 μA, V _{GS} = 0 V, T	₁ = 25 ^o C	500	-	-	V
ΔBV _{DSS} /ΔTJ		n Voltage Temperature		$I_D = 250 \ \mu\text{A}, \text{ Referenced to } 25^{\circ}\text{C}$			0.7	-	V/ºC
I _{DSS}	Zero Gate Voltage Drain Current			$V_{DS} = 500 V, V_{GS} = 0 V$ $V_{DS} = 400 V, T_{C} = 125^{\circ}C$			-	25 250	μA
I _{GSS}	Gate to B	ody Leakage Current		_S = ±30 V, V _{DS} = 0 V		-	-	±100	nA
On Charac	toristics								
		eshold Voltage	V	_S = V _{DS} , I _D = 250 μA		3.0	-	5.0	V
V _{GS(th)} R _{DS(on)}		in to Source On Resistance		$r_{S} = V_{DS}, r_{D} = 250 \mu A$ $r_{S} = 10 \text{V}, r_{D} = 5 \text{A}$		-	- 0.65	0.8	Ω
9 _{FS}		Fransconductance	-	$S = 40 \text{ V}, I_D = 5 \text{ A}$		-	11	-	S S
			[*D	5					
Dynamic C	-1								
C _{iss}	Input Cap		V_	V _{DS} = 25 V, V _{GS} = 0 V, f = 1 MHz		-	1050	1395	pF
C _{oss}	-	apacitance				-	140	190	pF
C _{rss}		Transfer Capacitance		V _{DS} = 400 V, I _D = 10 A, V _{GS} = 10 V (Note 4)		-	11	17	pF
Q _{g(tot)}		e Charge at 10V				-	21	30	nC
Q _{gs} Q _{gd}		ource Gate Charge rain "Miller" Charge	VG			-	6 9	-	nC nC
Switching d(on) tr d(off)	Turn-On E Turn-On F Turn-Off E	Delay Time Rise Time Delay Time		_D = 250 V, I _D = 10 A, _S = 10 V, R _G = 25 Ω	-	- - -	35 45 60	80 100 130	ns ns ns
t _f	Turn-Off F	all lime			(Note 4)		35	80	ns
Drain-Sou	rce Diode	Characteristics							
I _S		Continuous Drain to Source					-	10	Α
I _{SM}		Aaximum Pulsed Drain to Source Diode					-	40	Α
V _{SD}		ource Diode Forward Volta		_S = 0 V, I _{SD} = 10 A		-	-	1.6	V
t _{rr} Q _{rr}		Recovery Time Recovery Charge		_S = 0 V, I _{SD} = 10 A, /dt = 100 A/μs		-	65 0.1	-	ns μC
2. L = 9 mH, I _{AS} = 3. I _{SD} ≤ 10 A, di/dt	10 A, V _{DD} = 50 ≤ 200 A/μs, V _{DD}	hited by maximum junction tempera V, $R_G = 25 \Omega$, starting $T_J = 25^{\circ}C$. $_3 \leq BV_{DSS}$, starting $T_J = 25^{\circ}C$. ating temperature typical characteri							



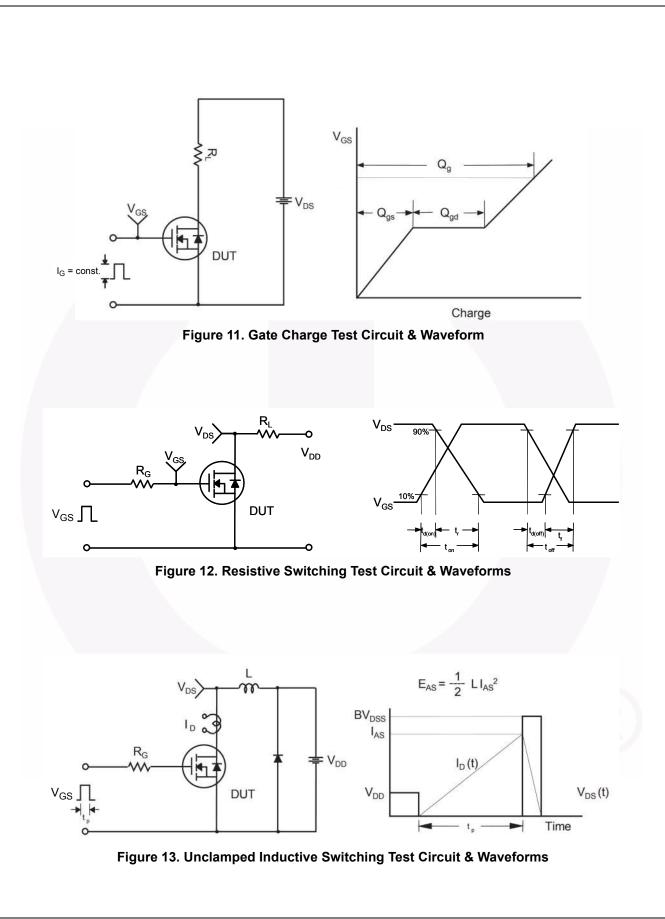


FDPF12N50UT Rev. C2

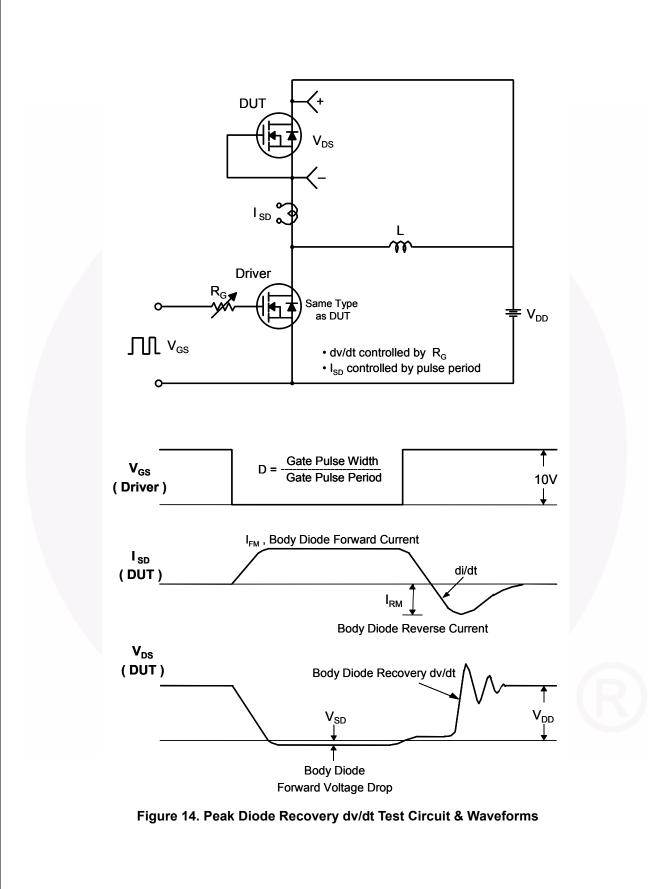


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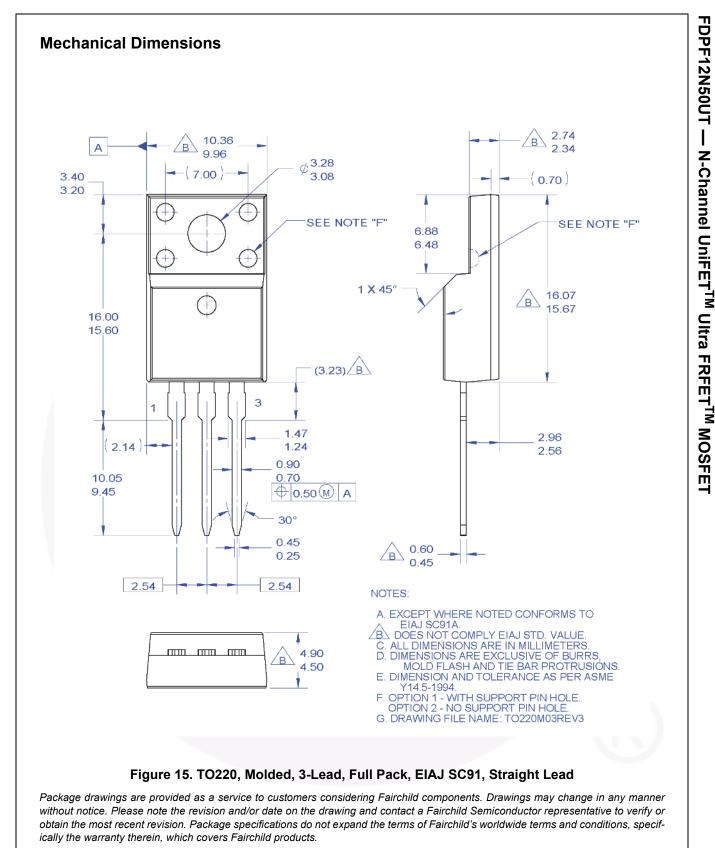
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FDPF12N50UT — N-Channel UniFETTM Ultra FRFETTM MOSFET



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