Switching (-30V, -2.0A)

SP8J4

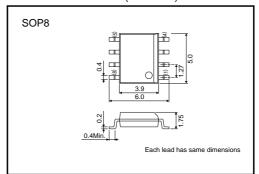
Features

- 1) Low On-resistance. (270m Ω at 4.5V)
- 2) High Power Package.
- 3) High speed switching.
- 4) Low voltage drive. (4.5V)

Applications

Power switching, DC-DC converter

●External dimensions (Unit : mm)



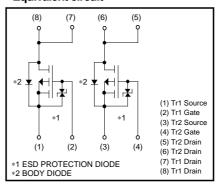
Structure

Silicon P-channel MOS FET

Packaging specifications

	Package	Taping
Туре	Code	ТВ
	Basic ordering unit (pieces)	2500
SP8J4		0

●Equivalent circuit



● Absolute maximum ratings (Ta=25°C)

Parameter		Symbol	Limits	Unit	
Drain-source voltage		VDSS	-30	V	
Gate-source voltage		Vgss	±20	V	
Drain augrant	Continuous	lσ	±2.0	Α	
Drain current	Pulsed	IDP	±8.0	A *1	
Source current	Continuous	Is	-1.6	А	
(Body diode)	Pulsed	Isp	-8.0	A *1	
Total power dissipation		Po	2.0	W *2	
Channel temperature		Tch	150	°C	
Range of Storage temperature		Tstg	-55 to +150	°C	

^{*1} Pw≤10≪s, Duty cycle≤1% *2 Mounted on a ceramic board

●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions	
Gate-source leakage	Igss	_	-	±10	∞A	V _{GS} =±20V, V _{DS} =0V	
Drain-source breakdown voltage	V _{(BR) DSS}	-30	-	_	V	I _D = -1mA, V _G S=0V	
Zero gate voltage drain current	IDSS	-	_	-1	∞A	V _{DS} = -30V, V _{GS} =0V	
Gate threshold voltage	VGS (th)	-1.0	_	-2.5	V	V _{DS} = -10V, I _D = -1mA	
Static drain-source on-state resistance	R _{DS} (on)	_	170	235	mΩ	I _D = -2.0A, V _G S= -10V	*
		_	270	375	mΩ	I _D = -1.0A, V _G S= -4.5V	*
		_	320	440	mΩ	I _D = -1.0A, V _G S= -4.0V	*
Forward transfer admittance	Yfs	1.0	_	_	S	$V_{DS} = -10V$, $I_{D} = -1.0A$	*
Input capacitance	Ciss	_	190	_	pF	V _{DS} = -10V	
Output capacitance	Coss	_	45	_	pF	V _{GS} =0V	
Reverse transfer capacitance	Crss	_	30	_	pF	f=1MHz	
Turn-on delay time	td (on)	_	7	_	ns	I _D = -1.0A	*
Rise time	tr	_	10	_	ns	V _{DD} ≒ -15V	*
Turn-off delay time	t _{d (off)}	_	25	_	ns	V _{GS} = -10V R _L =15Ω	*
Fall time	tf	_	4.5	_	ns	Rgs=10Ω	*
Total gate charge	Qg	-	2.4	_	nC	V _{DD} ≒−15V	
Gate-source charge	Qgs	-	1.0	_	nC	V _{GS} = -5V	
Gate-drain charge	Q _{gd}	_	0.8	_	nC	I _D =-2.0A	

Body diode characteristics (source-drain characteristics)

200) diode criaracteriones (course diam criaracteriones)						
Forward voltage	VSD	_	_	-1.2	V	I _S = -1.6A, V _{GS} =0V



Electrical characteristic curves

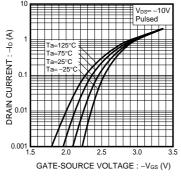


Fig.1 Typical Transfer Characteristics

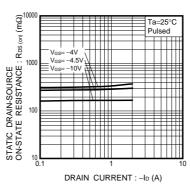


Fig.2 Static Drain-Source On-State Resistance vs. Drain Current

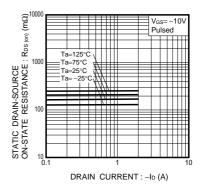


Fig.3 Static Drain-Source On-State Resistance vs. Drain Current

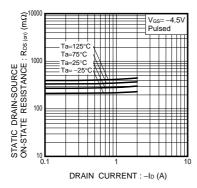


Fig.4 Static Drain-Source On-State vs. Drain Current

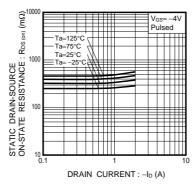


Fig.5 Static Drain-Source On-State vs. Drain Current

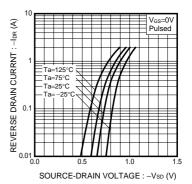


Fig.6 Reverse Drain Current Source-Drain Current

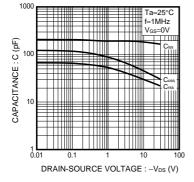


Fig.7 Typical Capacitance vs. Drain-Source Voltage

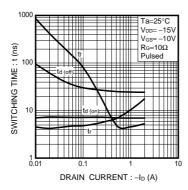


Fig.8 Switching Characteristics

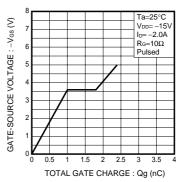


Fig.9 Dynamic Input Characteristics

Measurement circuits

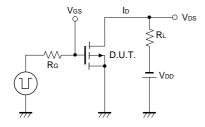


Fig.10 Switching Time Test Circuit

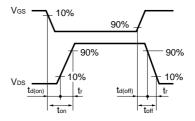


Fig.11 Switching Time Waveforms

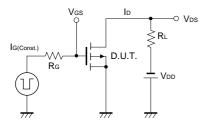


Fig.12 Gate Charge Test Circuit

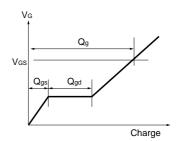


Fig.13 Gate Charge Waveform

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