

Low voltage NPN power transistor

Features

- High switching speed
- Good performances in terms of h_{FE} linearity

Application

■ Linear and switching industrial applications

Description

The device is manufactured in planar technology with "base island" layout. The resulting transistor shows high gain performance coupled with low saturation voltage.

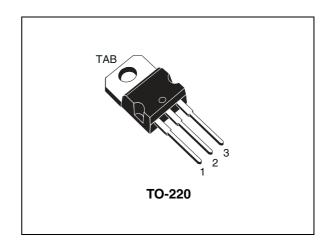


Figure 1. Internal schematic diagram

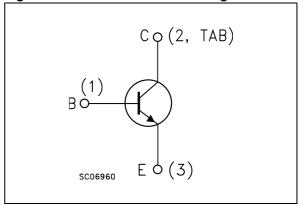


Table 1. Device summary

Order code	Marking	Package	Packaging
2ST31A	2ST31A	TO-220	Tube

1 Absolute maximum ratings

Table 2. Absolute maximum ratings

Symbol	Parameter	Value	Unit
V _{CBO}	Collector-base voltage (I _E = 0)	60	V
V _{CEO}	Collector-emitter voltage (I _B = 0) 60		V
V _{EBO}	Emitter-base voltage ($I_C = 0$)	5	V
I _C	Collector current	3	Α
I _{CM}	Collector peak current	5	Α
I _B	Base current	1	Α
P _{TOT}	Total dissipation at T _{case} = 25°C	40	W
T _{STG}	Storage temperature	-65 to 150	°C
TJ	Max. operating junction temperature	150	°C

Table 3. Thermal data

Symbol	Parameter	Value	Unit
R _{thJC}	Thermal resistance junction-case max.	3.1	°C/W

2 Electrical characteristics

 T_{case} = 25 °C unless otherwise specified.

Table 4. Electrical characteristics

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
I _{CEO}	Collector cut-off current (I _B = 0)	V _{CE} = 30 V			0.3	mA
I _{EBO}	Emitter cut-off current (I _C = 0)	V _{EB} = 5 V			1	mA
I _{CES}	Collector cut-off current (V _{BE} = 0)	V _{CE} = 60 V			0.2	mA
V _{CEO(sus)} ⁽¹⁾	Collector-emitter sustaining voltage (I _B = 0)	I _C = 30 mA	60			V
V _{CE(sat)} ⁽¹⁾	Collector-emitter saturation voltage	$I_C = 3 \text{ A}$ $I_B = 375 \text{ mA}$			1.2	V
V _{BE(sat)} ⁽¹⁾	Base-emitter saturation voltage	$I_C = 3 \text{ A}$ $I_B = 375 \text{ mA}$			1.45	V
h _{FE} ⁽¹⁾	DC current gain	$\begin{split} I_{\text{C}} &= 20 \text{ mA} & V_{\text{CE}} = 4 \text{ V} \\ I_{\text{C}} &= 1 \text{ A} & V_{\text{CE}} = 4 \text{V} \end{split}$	100 25	150		

^{1.} Pulse test: pulse duration $\leq 300~\mu s,$ duty cycle $\leq 2~\%$

Electrical characteristics 2ST31A

2.1 Electrical characteristics (curve)

Figure 2. Safe operating area

Ic (A)

Ic MAX PULSED PULSE OPERATION*

Ic MAX PULSED PULSE OPERATION*

D.C. OPERATION

* FOR SINGLE NON

REPETITIVE PULSE

10⁻¹

10⁰

2 4 6 8 10¹

4 7 6 8 (V)

Figure 3. Derating curves

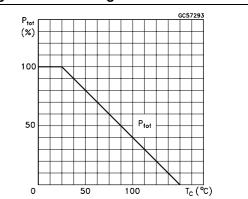
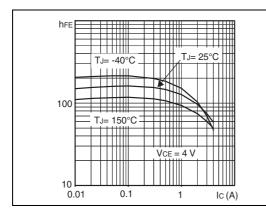


Figure 4. DC-current gain

Figure 5. Base-emitter saturation voltage



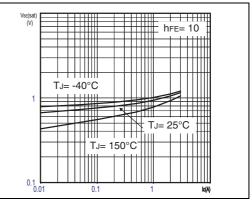
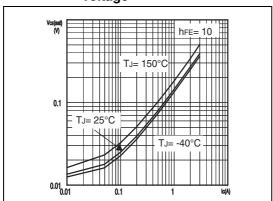


Figure 6. Collector-emitter saturation voltage

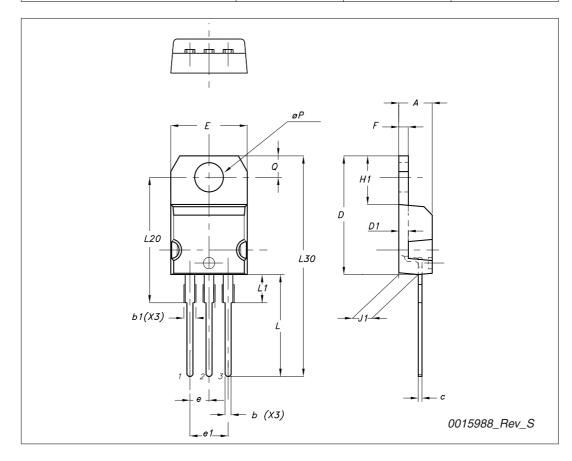


3 Package mechanical data

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TO-220 type A mechanical data

Dim	mm			
	Min	Тур	Max	
A	4.40		4.60	
b	0.61		0.88	
b1	1.14		1.70	
С	0.48		0.70	
D	15.25		15.75	
D1		1.27		
E	10		10.40	
е	2.40		2.70	
e1	4.95		5.15	
F	1.23		1.32	
H1	6.20		6.60	
J1	2.40		2.72	
L	13		14	
L1	3.50		3.93	
L20		16.40		
L30		28.90		
ØP	3.75		3.85	
Q	2.65		2.95	



2ST31A Revision history

4 Revision history

Table 5. Document revision history

Date	Revision	Changes	
24-Aug-2010	1	Initial release.	
14-Dec-2010	2	Document status promoted from preliminary data to datasheet.	

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