

In-vehicle network ESD protection diode

Product data sheet

## 1. General description

ElectroStatic Discharge (ESD) protection diode in a very small SOT323 (SC-70) Surface-Mounted Device (SMD) plastic package designed to protect two automotive in-vehicle network lines from the damage caused by ESD and other transients.

## 2. Features and benefits

- One very small SOT323 package to protect two in-vehicle network lines
- Low clamping voltage: V<sub>CL</sub> = 38 V at I<sub>PP</sub> = 1 A
- Typical diode capacitance matching  $\Delta C_d/C_d = 0.1 \%$
- ESD protection up to 18 kV; IEC 61000-4-2, level 4
- IEC 61000-4-5 (surge); I<sub>PP</sub> = 3 A at t<sub>p</sub> = 8/20 μs
- AEC-Q101 qualified

## 3. Applications

- In-vehicle network ESD protection for CAN, LIN, FlexRay and Single Edge Nibble Transmission (SENT) interfaces
- Generic automotive applications

## 4. Quick reference data

Table 1. Qui	ck reference data					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>RWM</sub>	reverse standoff voltage	T <sub>amb</sub> = 25 °C	-	-	26.5	V
C <sub>d</sub>	diode capacitance	f = 1 MHz; V <sub>R</sub> = 0 V; T <sub>amb</sub> = 25 °C	-	8.5	11	pF

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## 5. Pinning information

Table 2.	Pinning	information		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	к	cathode	3	
2	к	cathode		
3	СС	common cathode		2 <del>  [J]</del> ]
			1 2 SC-70 (SOT323)	006aaa155

## 6. Ordering information

Table 3. Ordering information					
Type number	Package				
	Name	Description	Version		
PESD2IVN-U	SC-70	plastic surface-mounted package; 3 leads	SOT323		

## 7. Marking

Table 4. Marking codes	
Type number	Marking code
	[1]
PESD2IVN-U	3Y%

[1] % = placeholder for manufacturing site code

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## 8. Limiting values

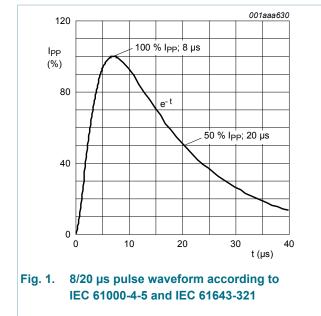
#### Table 5.Limiting values

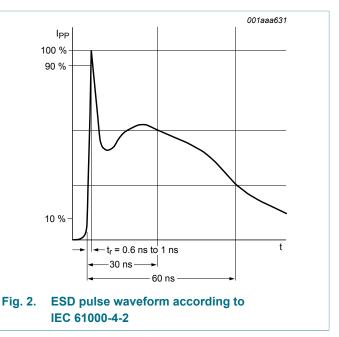
In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Мах	Unit	
P <sub>PPM</sub>	rated peak pulse power	t <sub>p</sub> = 8/20 μs	[1][2]	-	150	W	
I <sub>PPM</sub>	rated peak pulse current		[1][2]	-	3	А	
Tj	junction temperature			-	150	°C	
T <sub>amb</sub>	ambient temperature			-55	150	°C	
T <sub>stg</sub>	storage temperature			-65	150	°C	
ESD maximum ratings							
V <sub>ESD</sub>	electrostatic discharge voltage	IEC 61000-4-2 (contact discharge)	[2][3]	-	18	kV	
		MIL-STD-883 (human body model)	[2][3]	-	10	kV	

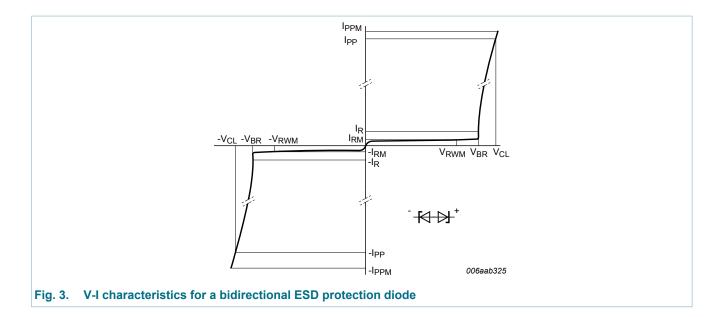
Non-repetitive current pulse 8/20 µs exponential decay waveform according to IEC 61000-4-5 and IEC 61643-321.

[3] Device stressed with ten non-repetitive ESD pulses.





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## 9. Characteristics

Table 6. Characteristics							
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V <sub>RWM</sub>	reverse standoff voltage	T <sub>amb</sub> = 25 °C		-	-	26.5	V
I <sub>RM</sub>	reverse leakage current	V <sub>RWM</sub> = 26.5 V; T <sub>amb</sub> = 25 °C		-	1	50	nA
V <sub>BR</sub>	breakdown voltage	I <sub>R</sub> = 5 mA; T <sub>amb</sub> = 25 °C		28	30	32	V
C <sub>d</sub>	diode capacitance	f = 1 MHz; V <sub>R</sub> = 0 V; T <sub>amb</sub> = 25 °C		-	8.5	11	pF
		f = 1 MHz; $V_R$ = 2.5 V; $T_{amb}$ = 25 °C		-	6.6	-	pF
$\Delta C_d/C_d$ diode capacita	diode capacitance	f = 1 MHz; V <sub>R</sub> = 0 V; T <sub>amb</sub> = 25 °C		-	0.1	-	%
	matching	f = 1 MHz; $V_R$ = 2.5 V; $T_{amb}$ = 25 °C		-	0.1	-	%
V <sub>CL</sub>	clamping voltage	I <sub>PP</sub> = 1 A; T <sub>amb</sub> = 25 °C	[1][2]	-	-	38	V
		I <sub>PPM</sub> = 3 A; T <sub>amb</sub> = 25 °C	[1][2]	-	-	53	V
R <sub>dyn</sub>	dynamic resistance	I <sub>R</sub> = 20 A; T <sub>amb</sub> = 25 °C	[3]	-	2	-	Ω

Non-repetitive current pulse 8/20 µs exponential decay waveform according to IEC 61000-4-5 and IEC 61643-321.

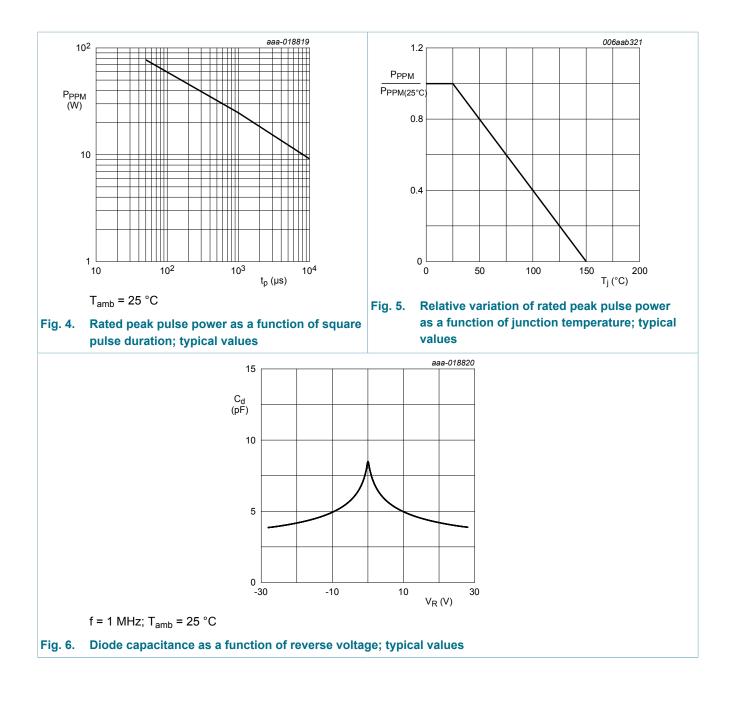
[2] Measured from pin 1 or 2 to 3.

[3] Non-repetitive current pulse, Transmission line Pulse (TLP), square pulse, ANSI/ESD STM5.5.1-2008.

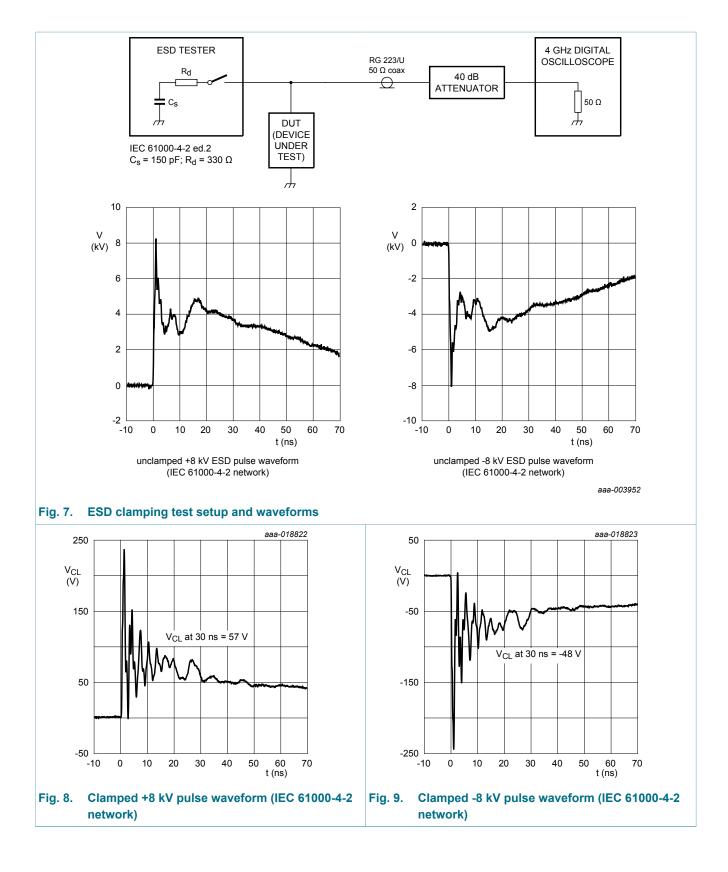
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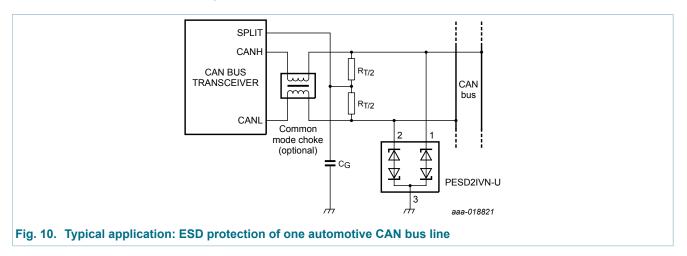
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## **10.** Application information

The device is designed for the protection of two automotive in-vehicle network bus lines from surge pulses and ESD damage. The device provides a surge capability of up to 3 A for an 8/20  $\mu$ s waveform.



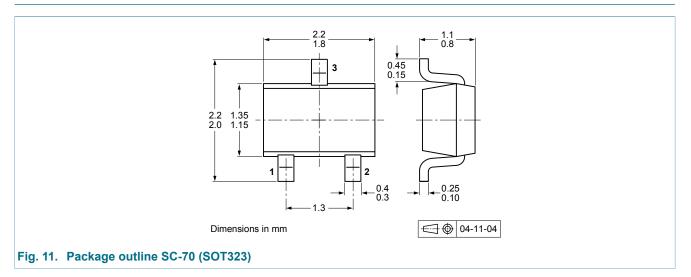
#### Circuit board layout and protection device placement

Circuit board layout is critical for the suppression of ESD, Electrical Fast Transient (EFT) and surge transients. The following guidelines are recommended:

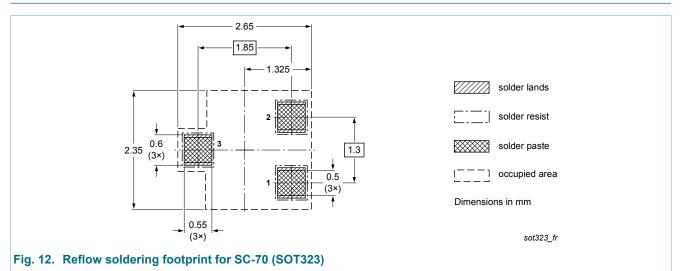
- 1. Place the device as close to the input terminal or connector as possible.
- 2. Minimize the path length between the device and the protected line.
- 3. Keep parallel signal paths to a minimum.
- 4. Avoid running protected conductors in parallel with unprotected conductors.
- 5. Minimize all Printed-Circuit Board (PCB) conductive loops including power and ground loops.
- 6. Minimize the length of the transient return path to ground.
- 7. Avoid using shared transient return paths to a common ground point.
- 8. Use ground planes whenever possible. For multilayer PCBs, use ground vias.

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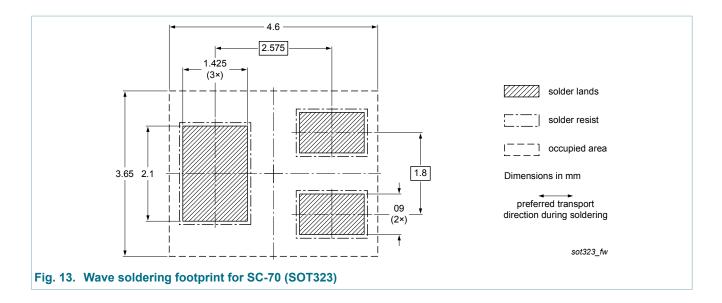
## 11. Package outline



## 12. Soldering



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## **13. Revision history**

Table 7. Revision history					
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes	
PESD2IVN-U v.1	20150715	Product data sheet	-	-	

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## 14. Legal information

#### 14.1 Data sheet status

Document status [1][2]	Product status [ <u>3]</u>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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[2] The term 'short data sheet' is explained in section "Definitions".

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