TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π -MOSIV)

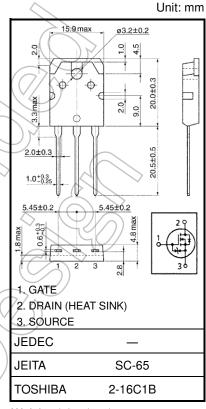
2SK3700

Switching Regulator Applications

- Low drain-source ON-resistance: RDS (ON) = 2.0Ω (typ.)
- High forward transfer admittance: |Yfs| = 4.5 S (typ.)
- Low leakage current: IDSS = $100 \mu A \text{ (max) (VDS} = 720 \text{ V)}$
- Enhancement model: Vth = 2.0 to 4.0 V (VDS = 10 V, ID = 1 mA)

Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Drain-source voltage		V_{DSS}	900	$(\nearrow \land)$	
Drain-gate voltage (RG	$s = 20 \text{ k}\Omega$)	VDGR	900	\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\	
Gate-source voltage		V _{GSS}	±30	V	
Drain current	DC (Note 1)	ΙD	5	A	
	Pulse (Note 1)	I _{DP}	15	V A	
Drain power dissipation	n (Tc=25°C)	PD	150	W	
Single pulse avalanche	e energy (Note 2)	EAS	351	mJ	
Avalanche current		IAR	5	A	
Repetitive avalanche e	nergy (Note 3)	EAR)) 15	mJ	
Channel temperature		Tch	150	∕ °C	
Storage temperature ra	ange	T _{stg}	−55 to150	∕},¢	



Weight: 4.6 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	Rth (ch-c)	0.833	°C/W
Thermal resistance, channel to ambient	Rth (ch-a)	50	°C/W

Note 1: Ensure that the temperature does not exceed 150°C.

Note 2: VDD = 90 V, $T_{ch} = 25^{\circ}\text{C}$ (initial), L = 25.7mH, $R_G = 25 \Omega$, $I_{AR} = 5 \text{ A}$

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Please handle with caution.

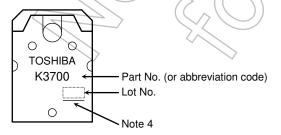
Electrical Characteristics (Ta = 25°C)

Char	acteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	rrent	IGSS	V _G S = ±25 V, V _D S = 0 V	_	_	±10	μΑ
Gate-source brea	akdown voltage	V (BR) GSS	$I_G = \pm 10 \mu A$, $V_{DS} = 0 V$	±30	_	_	V
Drain cut-OFF cu	urrent	IDSS	V _{DS} = 720 V, V _{GS} = 0 V	7	_	100	μΑ
Drain-source bre	akdown voltage	V (BR) DSS	I _G = 10mA, V _G S = 0 V	900	_	_	V
Gate threshold v	oltage	Vth	V _{DS} = 10 V, I _D = 1 mA	2.0) /_	4.0	V
Drain-source ON	I resistance	RDS (ON)	V _G S = 10 V, I _D = 3 A	77	2.0	2.5	Ω
Forward transfer	admittance	Y _{fs}	V _{DS} = 20 V, I _D = 3 A	2.0	4.5	_	S
Input capacitanc	е	Ciss		_	1150	_	
Reverse transfer capacitance		Crss	V _{DS} = 25 V, V _{GS} = 0 V, f = 1 MHz	_	20	_	pF
Output capacitance		Coss		_	100	_	
Switching time	Rise time	t _r	10 V ID = 3 A VOUT	- /	30	<u>></u>	
	Turn-ON time	t _{on}	0 V	((70) –	
	Fall time	tf	S RL = 133 Ω	7	>60	_	ns
	Turn-OFF time	t _{off}	Duty ≤ 1%, t _w = 10 μs		170	_	
Total gate charge (gate-source plus gate-drain) Qg		Qg) _	28	_	
Gate-source charge		Qgs	VDD≈400 V, VGS = 10 V, ID = 5 Å	_	17	_	nC
Gate-drain ("miller") charge		Qgd		_	11	_	

Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	IDR	(7/\ -	_	_	5	Α
Pulse drain reverse current (Note 1)	IDRP	_	_	_	15	Α
Forward voltage (diode)	VDSF	I _{DR} = 5 A, V _{GS} = 0 V	_	_	-1.7	٧
Reverse recovery time	trr	I _{DR} = 5 A, V _{GS} = 0 V,	_	900	_	ns
Reverse recovery charge	Q _{rr}	dl _{DR} /dt = 100 A/μs	_	5.4	_	μС

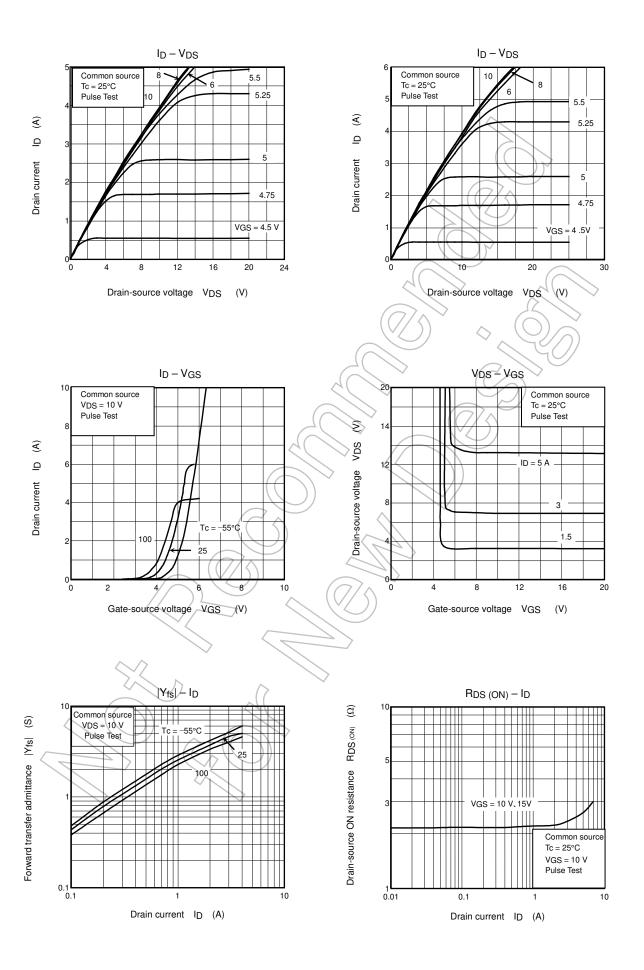
Marking

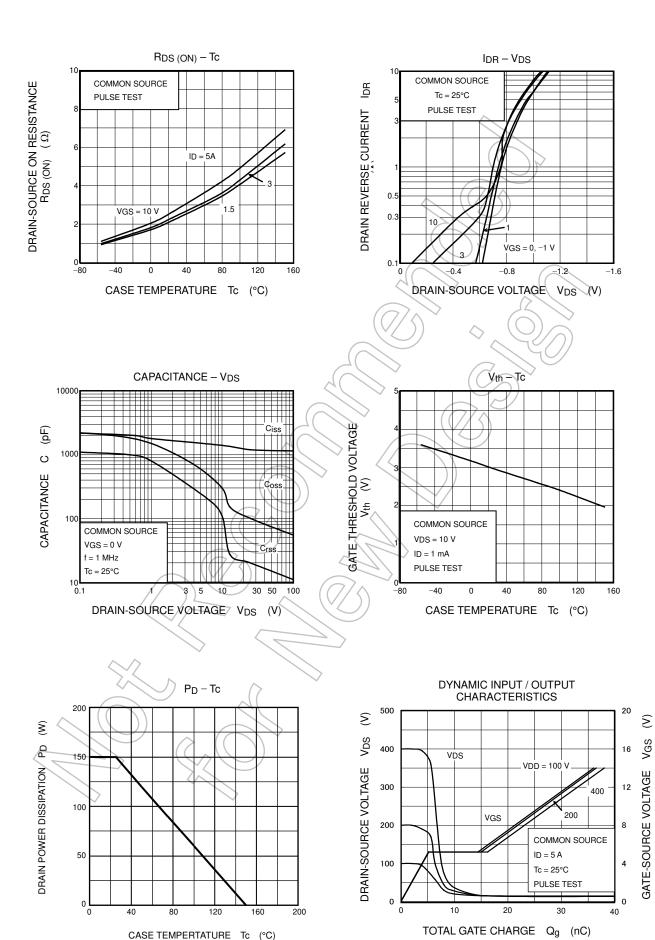


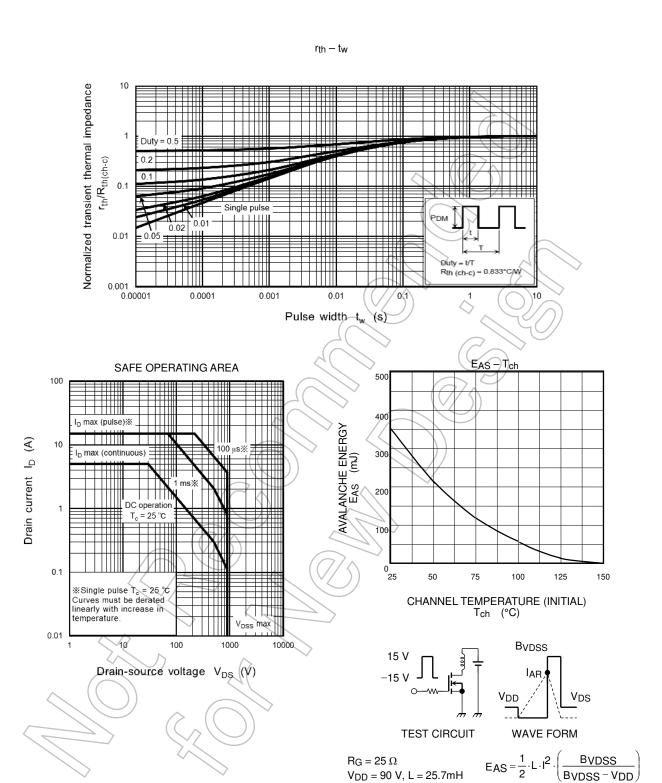
Note 4: A line under a Lot No. identifies the indication of product Labels.

Not underlined: [[Pb]]/INCLUDES > MCV Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

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