**Vishay Semiconductors** 

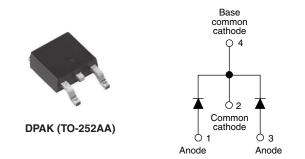
**RoHS** 

COMPLIANT HALOGEN

FREE

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High Performance Schottky Rectifier, 2 x 3.5 A



PRIMARY CHARACTERISTICS								
I <sub>F(AV)</sub>	2 x 3.5 A							
V <sub>R</sub>	60 V							
V <sub>F</sub> at I <sub>F</sub>	See Electrical table							
I <sub>RM</sub>	30 mA at 125 °C							
T <sub>J</sub> max.	150 °C							
E <sub>AS</sub>	6 mJ							
Package	DPAK (TO-252AA)							
Circuit configuration	Common cathode							

### **FEATURES**

- Low forward voltage drop
- · Guard ring for enhanced ruggedness and long term reliability
- Popular DPAK outline
- · Center tap configuration
- Small foot print, surface mountable
- High frequency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

### DESCRIPTION

The VS-6CWQ06FN-M3 surface mount, center tap, Schottky rectifier series has been designed for applications requiring low forward drop and small foot prints on PC board. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS								
SYMBOL	CHARACTERISTICS VALUES							
I <sub>F(AV)</sub>	Rectangular waveform	7	А					
V <sub>RRM</sub>		60	V					
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	490	А					
V <sub>F</sub>	3 A <sub>pk</sub> , T <sub>J</sub> = 25 °C (per leg)	0.61	V					
TJ	Range	-40 to +150	°C					

VOLTAGE RATINGS								
PARAMETER	SYMBOL	VS-6CWQ06FN-M3	UNITS					
Maximum DC reverse voltage	V <sub>R</sub>	60	V					
Maximum working peak reverse voltage	V <sub>RWM</sub>	60	v					

ABSOLUTE MAXIMUM RATINGS										
PARAMETER	SYMBOL	TEST CON	VALUES	UNITS						
Maximum average forward per leg	ag 1 50 % dutu such at T 100 % restance lange of T		50 % duty cycle at $T_{C}$ = 133 °C, rectangular waveform							
current, see fig. 5 per device	I <sub>F(AV)</sub>	$30\%$ duty cycle at $T_{\rm C} = 135\%$	7							
Maximum peak one cycle non-repetitive		5 $\mu s$ sine or 3 $\mu s$ rect. pulse	Following any rated load	490	A					
surge current see fig. 7	IFSM	10 ms sine or 6 ms rect. pulse	condition and with rated V <sub>RRM</sub> applied	70						
Non-repetitive avalanche energy per leg	E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 1 A, L = 12 mH		6	mJ					
Repetitive avalanche current per leg	I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>B</sub> typical		1	А					

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# Vishay Semiconductors

ELECTRICAL SPECIFICATIONS										
PARAMETER	SYMBOL	TEST CO	TEST CONDITIONS							
		3 A	T <sub>.1</sub> = 25 °C	0.61						
Maximum forward voltage drop per leg, see fig. 1	V <sub>FM</sub> <sup>(1)</sup>	6 A	$-1_{\rm J} = 25$ C	0.76	v					
	VFM ()	3 A	− T <sub>.1</sub> = 125 °C	0.53						
		6 A	- 1j = 125 C	0.65						
Maximum reverse leakage current	I <sub>RM</sub> <sup>(1)</sup>	$T_J = 25 ^{\circ}C$		2	mA					
per leg, see fig. 2		T <sub>J</sub> = 125 °C	<ul> <li>V<sub>R</sub> = Rated V<sub>R</sub></li> </ul>	30	ША					
Threshold voltage	V <sub>F(TO)</sub>			0.38	V					
Forward slope resistance	r <sub>t</sub>	$T_{J} = T_{J} maximum$		34.31	mΩ					
Typical junction capacitance per leg	CT	$V_{R} = 5 V_{DC}$ , (test signal rar	145	pF						
Typical series inductance per leg	L <sub>S</sub>	Measured lead to lead 5 r	mm from package body	5.0	nH					
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>		10 000	V/µs					

#### Note

 $^{(1)}\,$  Pulse width < 300  $\mu s,$  duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS										
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS					
Maximum junction and storage temperature range		T <sub>J</sub> <sup>(1)</sup> , T <sub>Stg</sub>		-40 to +150	°C					
Maximum thermal resistance, per leg		Р	DC operation	4.70	°C/W					
junction to case	per device	R <sub>thJC</sub>	See fig. 4	2.35	C/W					
Approximate weight				0.3	g					
				0.01	oz.					
Marking device			Case style DPAK (TO-252AA)	6CWQ	06FN					

#### Note

 $^{(1)} \quad \frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}} \quad \text{thermal runaway condition for a diode on its own heatsink}$ 



# VS-6CWQ06FN-M3

## **Vishay Semiconductors**

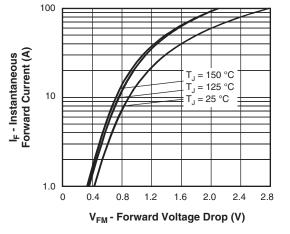


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

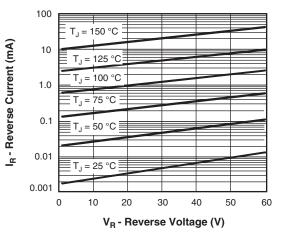


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

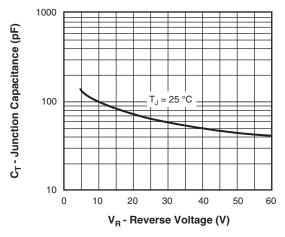


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

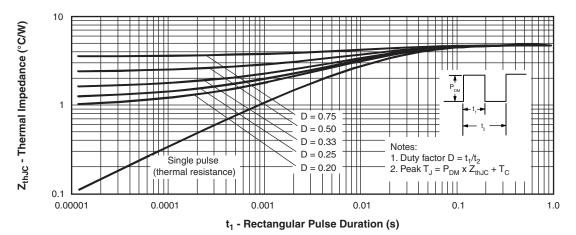


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics (Per Leg)

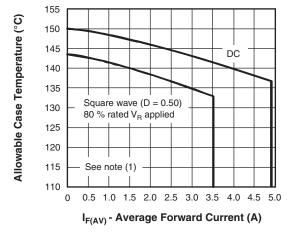
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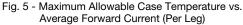
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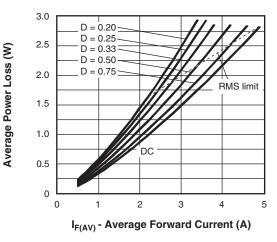


# VS-6CWQ06FN-M3

### **Vishay Semiconductors**









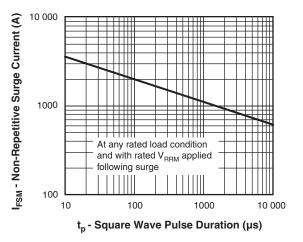


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

#### Note

- <sup>(1)</sup> Formula used:  $T_C = T_J (Pd + Pd_{REV}) \times R_{thJC}$ ;
- Pd = forward power loss =  $I_{F(AV)} \times V_{FM}$  at  $(I_{F(AV)}/D)$  (see fig. 6); Pd<sub>REV</sub> = inverse power loss =  $V_{R1} \times I_R$  (1 - D);  $I_R$  at  $V_{R1}$  = 80 % rated  $V_R$

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Device code	VS-	6	С	w	Q	06	FN	TRL	-M3
	1	2	3	4	5	6	7	8	9
	1	- Visł	nay Sen	niconduc	ctors pro	oduct			
	2			ng (7 A)					
	3		-	configur	ation-				
	-	<ul> <li>Package identifier:</li> <li>W = DPAK</li> </ul>							
	4								
				ing (06 =					
			= TO-2		-00 v)				
			one = tu						
				e and ree	el				
			•	e and re		oriented	1)		
				be and r					
	9.		-	ntal digit			-		
		-M3	= halog	gen-free	, RoHS-	complia	ant and	termina	tions lea

ORDERING INFORMATION (Example)										
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION							
VS-6CWQ06FN-M3	75	3000	Antistatic plastic tube							
VS-6CWQ06FNTR-M3	2000	2000	13" diameter reel							
VS-6CWQ06FNTRL-M3	3000	3000	13" diameter reel							
VS-6CWQ06FNTRR-M3	3000	3000	13" diameter reel							

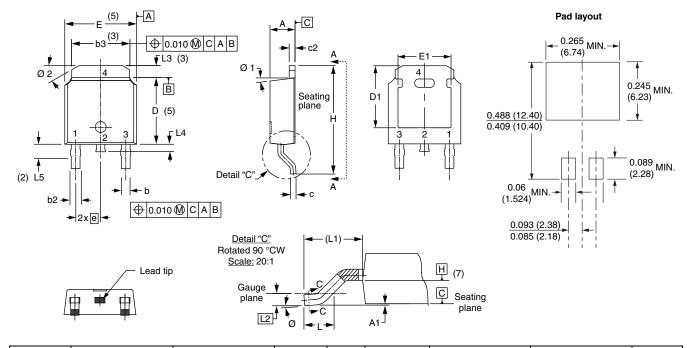
LINKS TO RELATED DOCUMENTS								
Dimensions <u>www.vishay.com/doc?95627</u>								
Part marking information	www.vishay.com/doc?95176							
Packaging information	www.vishay.com/doc?95033							
SPICE model	www.vishay.com/doc?96651							





D-PAK (TO-252AA) "M"

#### **DIMENSIONS** in millimeters and inches



SYMBOL	MILLIMETERS		INC	HES	NOTES	NOTES		MILLIN	IETERS	INC	HES	NOTES
STNIDUL	MIN.	MAX.	MIN.	MAX.	NOTES		SYMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
А	2.18	2.39	0.086	0.094			е	2.29	BSC	0.090	) BSC	
A1	-	0.13	-	0.005			Н	9.40	10.41	0.370	0.410	
b	0.64	0.89	0.025	0.035			L	1.40	1.78	0.055	0.070	
b2	0.76	1.14	0.030	0.045			L1	2.74	BSC	0.108	BREF.	
b3	4.95	5.46	0.195	0.215	3		L2	0.51	BSC	0.020	BSC	
С	0.46	0.61	0.018	0.024			L3	0.89	1.27	0.035	0.050	3
c2	0.46	0.89	0.018	0.035			L4	-	1.02	-	0.040	
D	5.97	6.22	0.235	0.245	5		L5	1.14	1.52	0.045	0.060	2
D1	5.21	-	0.205	-	3		Ø	0°	10°	0°	10°	
E	6.35	6.73	0.250	0.265	5		Ø1	0°	15°	0°	15°	
E1	4.32	-	0.170	-	3		Ø2	25°	35°	25°	35°	

#### Notes

<sup>(1)</sup> Dimensioning and tolerancing as per ASME Y14.5M-1994

<sup>(2)</sup> Lead dimension uncontrolled in L5

<sup>(3)</sup> Dimension D1, E1, L3 and b3 establish a minimum mounting surface for thermal pad

(4) Section C - C dimension apply to the flat section of the lead between 0.13 and 0.25 mm (0.005 and 0.10") from the lead tip

(5) Dimension D, and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body

<sup>(6)</sup> Dimension b1 and c1 applied to base metal only

<sup>(7)</sup> Datum A and B to be determined at datum plane H

<sup>(8)</sup> Outline conforms to JEDEC<sup>®</sup> outline TO-252AA



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