

YNC05S20 DC-DC Converter

4.5 – 5.5 VDC Input; 0.7525 – 3.63 VDC
Programmable @ 20 A

Key Features

- RoHS lead free solder and lead solder exempted products are available
- Delivers up to 20 A (66 W)
- Surface-mount package
- Industry-standard footprint and pinout
- Small size and low profile: 1.30" x 0.53" x 0.314" (33.02 x 13.46 x 7.98 mm)
- Weight: 0.22 oz [6.12 g]
- Coplanarity less than 0.003", maximum
- Synchronous Buck Converter topology
- Source and sink capable
- Start-up into pre-biased output
- No minimum load required
- Programmable output voltage via external resistor
- Operating ambient temperature: -40 °C to 85 °C
- Remote output sense
- Remote ON/OFF (positive or negative)
- Fixed-frequency operation
- Auto-reset output overcurrent protection
- Auto-reset overtemperature protection
- High reliability, MTBF = TBD Million Hours
- All materials meet UL94, V-0 flammability rating
- Approved to the latest edition and amendment of ITE Safety standards, UL/CSA 60950-1 and IEC60950-1

Bel Power Solutions point-of-load converters are recommended for use with regulated bus converters in an Intermediate Bus Architecture (IBA). The YNC05S20 non-isolated DC-DC converter delivers up to 20 A of output current in an industry-standard surface-mount package. Operating from a 4.5 – 5.5V input, this converter is an ideal choice for Intermediate Bus Architectures where point-of-load power delivery is preferred. It provides an extremely-tight, regulated programmable output voltage of 0.7525 V to 3.63 V.

The YNC05S20 converter provides exceptional thermal performance, even in high temperature environments with minimal airflow. This is accomplished through the use of patent pending circuits, packaging, and processing techniques to achieve ultra-high efficiency and excellent thermal management.

The preclusion of heat sinks minimizes impedance to system airflow, thus enhancing cooling for both upstream and downstream devices. The use of 100% automation for assembly, coupled with advanced power electronics and thermal design, results in a product with extremely high reliability.

Applications

- Intermediate Bus Architectures
- Telecommunications
- Data communications
- Distributed Power Architectures
- Servers, Workstations

Benefits

- High efficiency – no heat sink required
- Reduces Total Solution Board Area
- Tape and Reel packing
- Compatible with Pick & Place Equipment
- Minimizes Part Numbers in Inventory

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1. ELECTRICAL SPECIFICATIONS

Conditions: $T_A = 25\text{ }^\circ\text{C}$, Airflow = 200 LFM (1 m/s), $V_{in} = 5\text{ VDC}$, $V_{out} = 0.7525 - 3.63\text{ V}$, unless otherwise specified.

| PARAMETER | NOTES | MIN | TYP | MAX | UNITS |
|---|---|-------------------------------------|------|------|------------------|
| Absolute Maximum Ratings | | | | | |
| Input Voltage | Continuous | -0.3 | | 6 | VDC |
| Operating Ambient Temperature | | -40 | | 85 | $^\circ\text{C}$ |
| Storage Temperature | | -55 | | 125 | $^\circ\text{C}$ |
| Feature Characteristics | | | | | |
| Switching Frequency | | | 300 | | kHz |
| Output Voltage Programming Range ¹ | By external resistor, See Trim Table 1 | 0.7525 | | 3.63 | VDC |
| Remote Sense Compensation ¹ | | | | 0.5 | VDC |
| Turn-On Delay Time ² | Full resistive load | | | | |
| With $V_{in} = (\text{Module Enabled, then } V_{in} \text{ applied})$ | From $V_{in} = V_{in}(\text{min})$ to $V_o = 0.1 * V_o(\text{nom})$ | | 3.5 | | ms |
| With Enable ($V_{in} = V_{in}(\text{nom})$ applied, then enabled) | From enable to $V_o = 0.1 * V_o(\text{nom})$ | | 3.5 | | ms |
| Rise time ² | From 10% to 90%, full resistive load | | 3.5 | | ms |
| ON/OFF Control (Positive Logic) ³ | Module Off | -5 | | 0.8 | VDC |
| | Module On | 2.4 | | 5.5 | VDC |
| ON/OFF Control (Negative Logic) ³ | Module Off | 2.4 | | 5.5 | VDC |
| | Module On | -5 | | 0.8 | VDC |
| Input Characteristics | | | | | |
| Operating Input Voltage Range | | 4.5 | 5.0 | 5.5 | VDC |
| Input Undervoltage Lockout | Turn-on Threshold | | 2.05 | 2.15 | VDC |
| | Turn-off Threshold | 1.75 | 1.9 | | VDC |
| Maximum Input Current | | | | | |
| $V_{in} = 4.5\text{ V}$, $I_{out} = 20\text{ A}$ | $V_{OUT} = 3.3\text{ VDC}$ | | | 15.5 | ADC |
| $V_{in} = 4.5\text{ V}$, $I_{out} = 20\text{ A}$ | $V_{OUT} = 2.5\text{ VDC}$ | | | 12.1 | ADC |
| $V_{in} = 4.5\text{ V}$, $I_{out} = 20\text{ A}$ | $V_{OUT} = 2.0\text{ VDC}$ | | | 9.8 | ADC |
| $V_{in} = 4.5\text{ V}$, $I_{out} = 20\text{ A}$ | $V_{OUT} = 1.8\text{ VDC}$ | | | 9.0 | ADC |
| $V_{in} = 4.5\text{ V}$, $I_{out} = 20\text{ A}$ | $V_{OUT} = 1.5\text{ VDC}$ | | | 7.6 | ADC |
| $V_{in} = 4.5\text{ V}$, $I_{out} = 20\text{ A}$ | $V_{OUT} = 1.2\text{ VDC}$ | | | 6.3 | ADC |
| $V_{in} = 4.5\text{ V}$, $I_{out} = 20\text{ A}$ | $V_{OUT} = 1.0\text{ VDC}$ | | | 5.3 | ADC |
| $V_{in} = 4.5\text{ V}$, $I_{out} = 20\text{ A}$ | $V_{OUT} = 0.7525\text{ VDC}$ | | | 4.4 | ADC |
| Input Stand-by Current (Module disabled) | $V_{in} = 5.0\text{ VDC}$ | | 10 | | mA |
| Input No Load Current (Module enabled) | $V_{in} = 5.5\text{ VDC}$ | | | | |
| | $V_{OUT} = 3.3\text{ VDC}$ | | 80 | | mA |
| | $V_{OUT} = 2.5\text{ VDC}$ | | 80 | | mA |
| | $V_{OUT} = 2.0\text{ VDC}$ | | 75 | | mA |
| | $V_{OUT} = 1.8\text{ VDC}$ | | 72 | | mA |
| | $V_{OUT} = 1.5\text{ VDC}$ | | 65 | | mA |
| | $V_{OUT} = 1.2\text{ VDC}$ | | 60 | | mA |
| | $V_{OUT} = 1.0\text{ VDC}$ | | 50 | | mA |
| | $V_{OUT} = 0.7525\text{ VDC}$ | | 48 | | mA |
| | Input Reflected-Ripple Current - i_s | See Fig. E for setup. (BW = 20 MHz) | | TBD | |
| Input Voltage Ripple Rejection | 120 Hz | | TBD | | dB |

Notes:

- ¹ The output voltage should not exceed 3.63 V (taking into account both the programming and remote sense compensation).
- ² Note that start-up time is the sum of turn-on delay time and rise time.
- ³ Module is on if ON/OFF pin is left open.

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| PARAMETER | NOTES | MIN | TYP | MAX | UNITS |
|---|--|------|------------------|------|-------------------|
| Output Characteristics | | | | | |
| Output Voltage Set Point (no load) | | -1.5 | V _{out} | +1.5 | %V _{out} |
| Output Regulation | | | | | |
| Over Line | Full resistive load | | 2 | | mV |
| Over Load | From no load to full load | | 0.3 | | %V _{out} |
| Output Voltage Range | Overall operating input voltage, resistive load and temperature conditions until end of life | -2.5 | | +2.5 | %V _{out} |
| Output Ripple and Noise - 20 MHz bandwidth (Fig. E) | Over line, load and temperature | | | | |
| | Peak-to-Peak (3.3 V output) | | 60 | 80 | mV _{P-P} |
| | Peak-to-Peak (0.7525 V output) | | 30 | 40 | mV _{P-P} |
| External Load Capacitance | Plus full load (resistive) | | | | |
| | Min ESR > 1 mΩ | | | 1000 | μF |
| | Min ESR > 10 mΩ | | | 5000 | μF |
| Output Current Range | | 0 | | 20 | A |
| Output Current Limit Inception (I _{out}) | | | 27 | | A |
| Output Short- Circuit Current | Short = 10 mΩ, continuous | | 6 | | Arms |
| Dynamic Response | | | | | |
| Load current change from 10 A – 20 A, di/dt = 5 A/μs | Co = 100 μF ceramic + 470 μF POS | | 140 | | mV |
| Settling Time (V _{OUT} < 10% peak deviation) | | | 80 | | μs |
| Unloading current change 20 A – 10 A, di/dt = -5 A/μs | Co = 100 μF ceramic + 470 μF POS | | 140 | | mV |
| Settling Time (V _{OUT} < 10% peak deviation) | | | 80 | | μs |
| Efficiency | | | | | |
| | <i>Full Load (20 A)</i> | | | | |
| | V _{OUT} = 3.3 VDC | | 94.0 | | % |
| | V _{OUT} = 2.5 VDC | | 92.0 | | % |
| | V _{OUT} = 2.0 VDC | | 90.5 | | % |
| | V _{OUT} = 1.8 VDC | | 89.5 | | % |
| | V _{OUT} = 1.5 VDC | | 88.0 | | % |
| | V _{OUT} = 1.2 VDC | | 85.5 | | % |
| | V _{OUT} = 1.0 VDC | | 83.6 | | % |
| | V _{OUT} = 0.7525 VDC | | 76.8 | | % |

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2. OPERATIONS

2.1. INPUT AND OUTPUT IMPEDANCE

The YNC05S20 converter should be connected via a low impedance to the DC power source. In many applications, the inductance associated with the distribution from the power source to the input of the converter can affect the stability of the converter. It is recommended to use decoupling capacitors in order to ensure stability of the converter and reduce input ripple voltage. The converter has an internal input capacitance of 52 μF with very low ESR ceramic capacitors.

In a typical application, low - ESR tantalum or POS capacitors will be sufficient to provide adequate ripple voltage filtering at the input of the converter. However, very low ESR ceramic capacitors of 100-200 μF are recommended at the input of the converter in order to minimize the input ripple voltage. They should be placed as close as possible to the input pins of the converter.

YNC05S20 has been designed for stable operation with or without external capacitance. Low ESR ceramic capacitors (minimum 100 μF) placed as close as possible to the load are recommended for improved transient performance and lower output voltage ripple.

It is important to keep low resistance and low inductance PCB traces when connecting the load to the output pins of the converter in order to maintain good load regulation.

2.2. ON/OFF (PIN 1)

The ON/OFF pin is used to turn the power the converter on or off remotely via a system signal that is referenced to GND. Typical connections are shown in Fig. A.

To turn the converter on the ON/OFF pin should be at a logic low or left open, and to turn the converter off the ON/OFF pin should be at a logic high or connected to V_{in} .

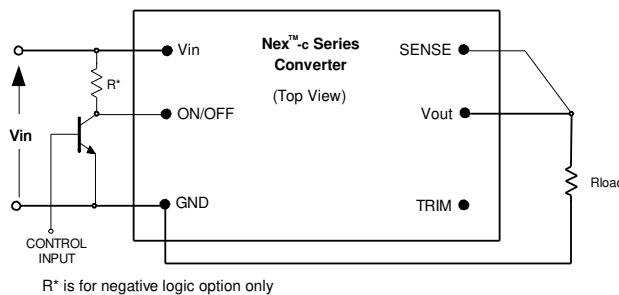


Fig. A: Circuit configuration for ON/OFF function.

The positive logic version turns the converter on when the ON/OFF pin is at a logic high or left open, and turns the converter off when at a logic low or shorted to GND.

The negative logic version turns the converter on when the ON/OFF pin is at a logic low or left open, and turns the converter off when the ON/OFF pin is at a logic high or connected to V_{in} .

ON/OFF pin is internally pulled-up to V_{in} for a positive logic version, and pulled-down for a negative logic version. A TTL or CMOS logic gate, open collector (open drain) transistor can be used to drive ON/OFF pin. When using open collector (open drain) transistor with a negative logic option, add a pull-up resistor (R^*) of 10 k Ω to V_{in} as shown in Fig. C. External pull-up resistor (R^*) can be increased to 20 k Ω if minimum input voltage is more than 4.5V. This device must be capable of:

- sinking up to 0.6 mA at a low level voltage of ≤ 0.8 V
- sourcing up to 0.25 mA at a high logic level of 2.3V – 5.5V

2.3. REMOTE SENSE (PIN 2)

The remote sense feature of the converter compensates for voltage drops occurring only between V_{out} pin (Pin 4) of the converter and the load. The SENSE (Pin 2) pin should be connected at the load or at the point where regulation is required (see Fig. B). There is no sense feature on the output GND return pin, where a solid ground plane is recommended to provide a low voltage drop.

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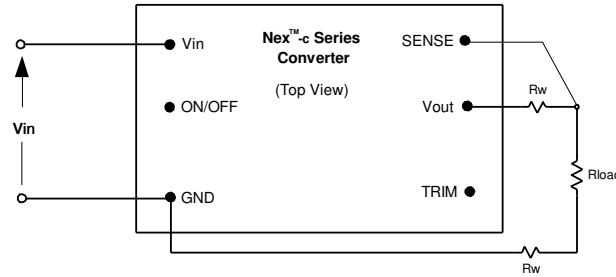


Fig. B: Remote sense circuit configuration.

If remote sensing is not required, the SENSE pin must be connected to the Vout pin (Pin 4) to ensure the converter will regulate at the specified output voltage. If these connections are not made, the converter will deliver an output voltage that is slightly higher than the specified value.

Because the sense lead carries minimal current, large trace on the end-user board are not required. However, sense trace should be located close to a ground plane to minimize system noise and insure optimum performance.

When utilizing the remote sense feature, care must be taken not to exceed the maximum allowable output power capability of the converter, equal to the product of the nominal output voltage and the allowable output current for the given conditions.

When using remote sense, the output voltage at the converter can be increased up to 0.5 V above the nominal rating in order to maintain the required voltage across the load. Therefore, the designer must, if necessary, decrease the maximum current (originally obtained from the derating curves) by the same percentage to ensure the converter's actual output power remains at or below the maximum allowable output power.

2.4. OUTPUT VOLTAGE PROGRAMMING (PIN 3)

The output voltage can be programmed from 0.7525 V to 3.63 V by connecting an external resistor between TRIM pin (Pin 3) and GND pin (Pin 5); see Fig. C. Note that when a trim resistor is not connected, the output voltage of the converter is 0.7525 V.

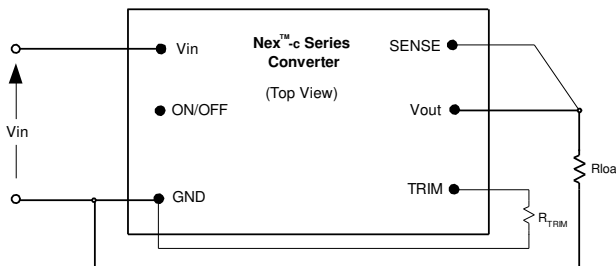


Fig. C: Configuration for programming output voltage.

A trim resistor, R_{TRIM} , for a desired output voltage can be calculated using the following equation:

$$R_{TRIM} = \frac{21.07}{(V_{O-REQ} - 0.7525)} - 5.11 \quad [k\Omega]$$

where,

R_{TRIM} = Required value of trim resistor [kΩ]

V_{O-REQ} = Desired (trimmed) output voltage [V]

Note that the tolerance of a trim resistor directly affects the output voltage tolerance. It is recommended to use standard 1% or 0.5% resistors; for tighter tolerance, two resistors in parallel are recommended rather than one standard value from Table 1.

Ground pin of the trim resistor should be connected directly to the converter GND pin (Pin 5) with no voltage drop in between. Table 1 provides the trim resistor values for popular output voltages.

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| V_{0-REG} [V] | R_{TRIM} [k Ω] | The Closest Standard Value [k Ω] |
|-----------------|--------------------------|--|
| 0.7525 | open | |
| 1.0 | 80.02 | 80.6 |
| 1.2 | 41.97 | 42.2 |
| 1.5 | 23.08 | 23.2 |
| 1.8 | 15.00 | 15.0 |
| 2.0 | 11.78 | 11.8 |
| 2.5 | 6.95 | 6.98 |
| 3.3 | 3.16 | 3.16 |
| 3.63 | 2.21 | 2.21 |

Table 1: Trim Resistor Value

The output voltage can be also programmed by external voltage source. To make trimming less sensitive, a series external resistor R_{EXT} is recommended between TRIM pin and programming voltage source. Control Voltage can be calculated by the formula:

$$V_{CTRL} = 0.7 - \frac{(5.11 + R_{EXT})(V_{O-REQ} - 0.7525)}{30.1} \quad [V]$$

where,

V_{CTRL} = Control voltage [V]

R_{EXT} = External resistor between TRIM pin and voltage source; the value can be chosen depending on the required output voltage range [k Ω]

Control voltages with $R_{EXT} = 0$ and $R_{EXT} = 15K$ are shown in Table 2.

| V_{O-REG} [V] | V_{CTRL} ($R_{EXT} = 0$) | V_{CTRL} ($R_{EXT} = 15 K$) |
|-----------------|------------------------------|---------------------------------|
| 0.7525 | 0.700 | 0.700 |
| 1.0 | 0.658 | 0.535 |
| 1.2 | 0.624 | 0.401 |
| 1.5 | 0.573 | 0.201 |
| 1.8 | 0.522 | 0.000 |
| 2.0 | 0.488 | -0.133 |
| 2.5 | 0.403 | -0.468 |
| 3.3 | 0.268 | -1.002 |
| 3.63 | 0.212 | -1.223 |

Table 2: Control Voltage [VDC]

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3. PROTECTION FEATURES

3.1. INPUT UNDERVOLTAGE LOCKOUT

Input undervoltage lockout is standard with this converter. The converter will shut down when the input voltage drops below a pre-determined voltage; it will start automatically when V_{in} returns to a specified range.

The input voltage must be at least 2.15 V for the converter to turn on. Once the converter has been turned on, it will shut off when the input voltage drops below typically 1.9 V.

3.2. OUTPUT OVERCURRENT PROTECTION (OCP)

The converter is protected against over-current and short circuit conditions. Upon sensing an over-current condition, the converter will enter hiccup mode. Once the overload or short-circuit condition is removed, V_{out} will return to nominal value.

3.3. OVERTEMPERATURE PROTECTION (OTP)

The converter will shut down under an over-temperature condition to protect itself from overheating caused by operation outside the thermal derating curves, or operation in abnormal conditions such as system fan failure. After the converter has cooled to a safe operating temperature, it will automatically restart.

3.4. SAFETY REQUIREMENTS

The converter meets North American and international safety regulatory requirements per UL60950 and EN60950. The maximum DC voltage between any two pins is V_{in} under all operating conditions. Therefore, the unit has ELV (extra low voltage) output; it meets SELV requirements under the condition that all input voltages are ELV.

The converter is not internally fused. To comply with safety agencies requirements, a recognized fuse with a maximum rating of 25 Amps must be used in series with the input line.

4. CHARACTERIZATION

4.1. GENERAL INFORMATION

The converter has been characterized for many operational aspects, to include thermal derating (maximum load current as a function of ambient temperature and airflow) for vertical and horizontal mounting, efficiency, start-up and shutdown parameters, output ripple and noise, transient response to load step-change, overload, and short circuit.

The figures are numbered as Fig. x.y, where x indicates the different output voltages, and y associates with specific plots ($y = 1$ for the vertical thermal derating, ...). For example, Fig. x.1 will refer to the vertical thermal derating for all the output voltages in general.

The following pages contain specific plots or waveforms associated with the converter. Additional comments for specific data are provided below.

4.2. TEST CONDITIONS

All thermal and efficiency data presented were taken with the converter soldered to a test board, specifically a 0.060" thick printed wiring board (PWB) with four layers. The top and bottom layers were not metalized. The two inner layers, comprising two-ounce copper, were used to provide traces for connectivity to the converter.

The lack of metalization on the outer layers as well as the limited thermal connection ensured that heat transfer from the converter to the PWB was minimized. This provides a worst-case but consistent scenario for thermal derating purposes.

All measurements requiring airflow were made in vertical and horizontal wind tunnel facilities using Infrared (IR) thermography and thermocouples for thermometry.

Ensuring components on the converter do not exceed their ratings is important to maintaining high reliability. If one anticipates operating the converter at or close to the maximum loads specified in the derating curves, it is prudent to check actual operating temperatures in the application. Thermographic imaging is preferable; if this capability is not available, then thermocouples may be used. Power-One recommends the use of AWG #40 gauge thermocouples to ensure measurement accuracy. Careful routing of the thermocouple leads will further minimize measurement error. Refer to Fig. D for optimum measuring thermocouple location.

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4.3. THERMAL DERATING

Load current vs. ambient temperature and airflow rates are given in Figs. x.1 to x.2 for maximum temperature of 120°C. Ambient temperature was varied between 25 °C and 85 °C, with airflow rates from 30 to 500 LFM (0.15m/s to 2.5 m/s), and vertical and horizontal converter mounting. The airflow during the testing is parallel to the short axis of the converter, going from pin 1 and pin 6 to pins 2 – 5.

For each set of conditions, the maximum load current was defined as the lowest of:

- (i) The output current at which any MOSFET temperature does not exceed a maximum specified temperature (120 °C) as indicated by the thermographic image, or
- (ii) The maximum current rating of the converter (20 A)

During normal operation, derating curves with maximum FET temperature less than or equal to 120 °C should not be exceeded. Temperature on the PCB at the thermocouple location shown in Fig. D should not exceed 120 °C in order to operate inside the derating curves.

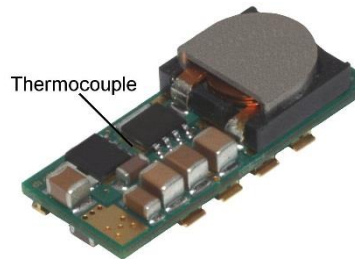


Fig. D: Location of the thermocouple for thermal testing.

4.4. EFFICIENCY

Fig. x.3 show the efficiency vs. load current plot for ambient temperature of 25 °C, airflow rate of 200 LFM (1 m/s) and input voltages of 4.5 V, 5.0 V, and 5.5 V.

4.5. POWER DISSIPATION

Fig. x.4 show the power dissipation vs. load current plot for $T_a = 25$ °C, airflow rate of 200 LFM (1 m/s) with vertical mounting and input voltages of 4.5 V, 5.0 V, and 5.5 V.

4.6. RIPPLE AND NOISE

The output voltage ripple waveform is measured at full rated load current. Note that all output voltage waveforms are measured across a 1 μ F ceramic capacitor.

The output voltage ripple and input reflected ripple current waveforms are obtained using the test setup. See Figure E.

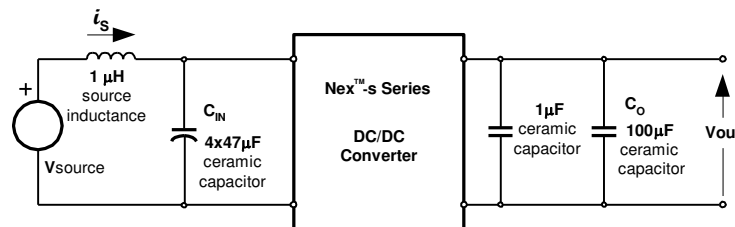


Fig. E: Test setup for measuring input reflected ripple currents, i_s and output voltage ripple.

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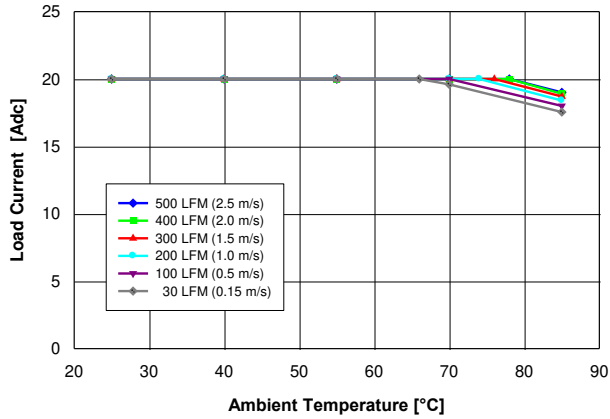


Fig. 3.3V.1: Available load current vs. ambient temperature and airflow rates for $V_{out} = 3.3\text{ V}$ converter mounted vertically with $V_{in} = 5\text{ V}$, air flowing from pin 1 and pin 6 to pins 2 - 5, and maximum MOSFET temperature $\leq 120^\circ\text{C}$.

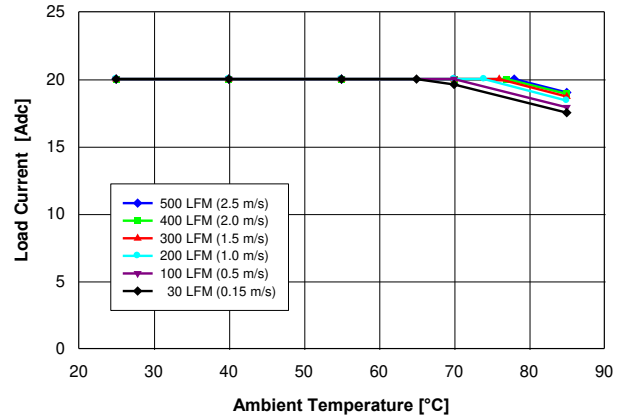


Fig. 3.3V.2: Available load current vs. ambient temperature and airflow rates for $V_{out} = 3.3\text{ V}$ converter mounted horizontally with $V_{in} = 5\text{ V}$, air flowing from pin 1 and pin 6 to pins 2 - 5, and maximum MOSFET temperature $\leq 120^\circ\text{C}$.

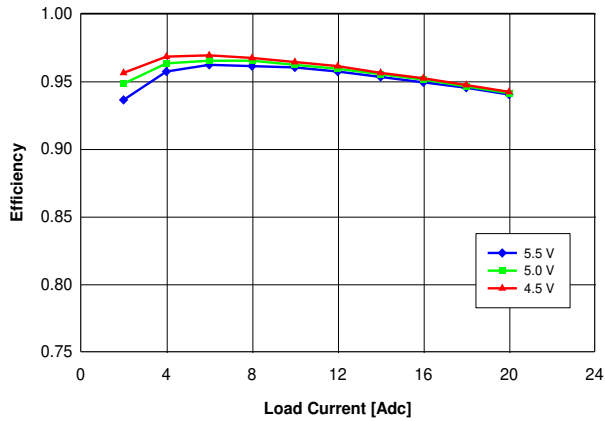


Fig. 3.3V.3: Efficiency vs. load current and input voltage for $V_{out} = 3.3\text{ V}$ converter mounted vertically with air flowing from pin 1 and pin 6 to pins 2 - 5 at a rate of 200 LFM (1 m/s) and $T_a = 25^\circ\text{C}$.

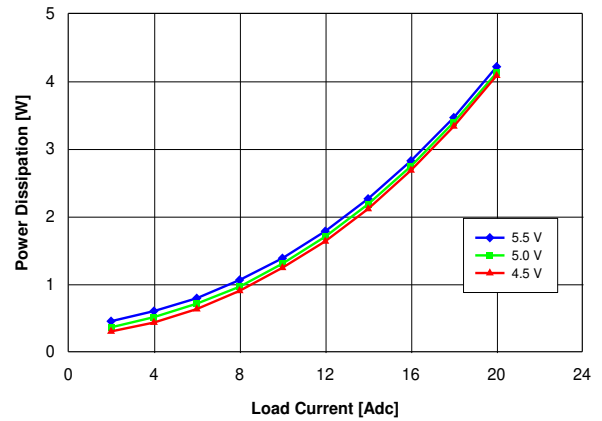


Fig. 3.3V.4: Power loss vs. load current and input voltage for $V_{out} = 3.3\text{ V}$ converter mounted vertically with air flowing from pin 1 and pin 6 to pins 2 - 5 at a rate of 200 LFM (1 m/s) and $T_a = 25^\circ\text{C}$.

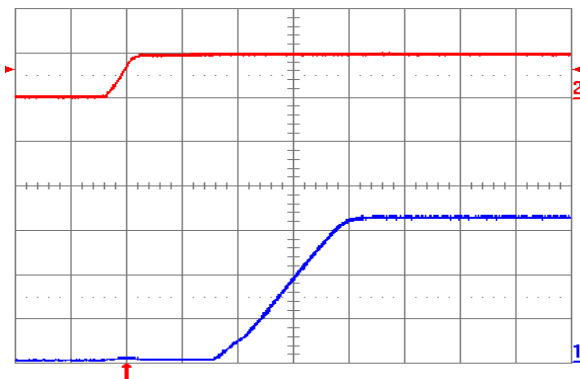


Fig. 3.3V.5: Turn-on transient for $V_{out} = 3.3\text{ V}$ with application of V_{in} at full rated load current (resistive) and $100\ \mu\text{F}$ external capacitance at $V_{in} = 5\text{ V}$. Top trace: V_{in} (5 V/div.); Bottom trace: output voltage (1 V/div.); Time scale: 2 ms/div.

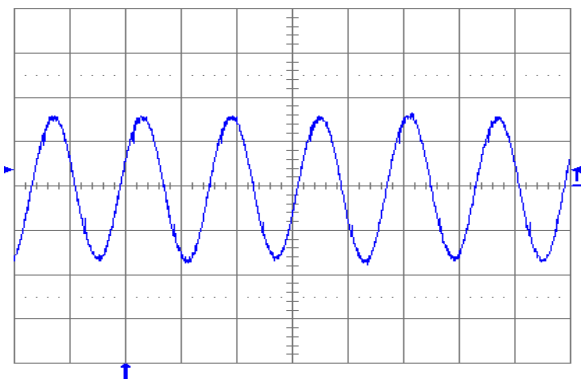


Fig. 3.3V.6: Output voltage ripple (20 mV/div.) at full rated load current into a resistive load with external capacitance $100\ \mu\text{F}$ ceramic + $1\ \mu\text{F}$ ceramic and $V_{in} = 5\text{ V}$ for $V_{out} = 3.3\text{ V}$. Time scale: 2 μs/div.

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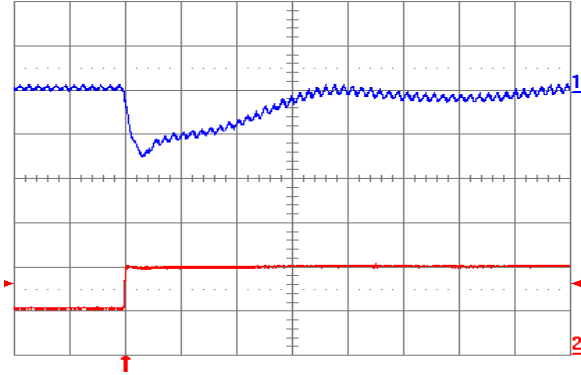


Fig. 3.3V.7: Output voltage response for $V_{out} = 3.3\text{ V}$ to positive load current step change from 10 A to 20 A with slew rate of $5\text{ A}/\mu\text{s}$ at $V_{in} = 5\text{ V}$. Top trace: output voltage (100 mV/div.); Bottom trace: load current (10 A/div.). $C_o = 100\text{ }\mu\text{F}$ ceramic. Time scale: $20\text{ }\mu\text{s}/\text{div}$.

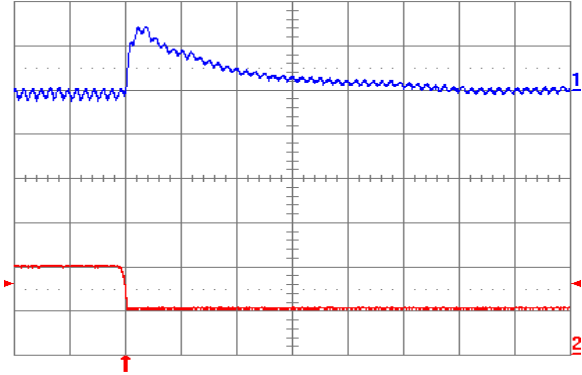


Fig. 3.3V.8: Output voltage response for $V_{out} = 3.3\text{ V}$ to negative load current step change from 20 A to 10 A with slew rate of $-5\text{ A}/\mu\text{s}$ at $V_{in} = 5\text{ V}$. Top trace: output voltage (100 mV/div.); Bottom trace: load current (10 A/div.). $C_o = 100\text{ }\mu\text{F}$ ceramic. Time scale: $20\text{ }\mu\text{s}/\text{div}$.

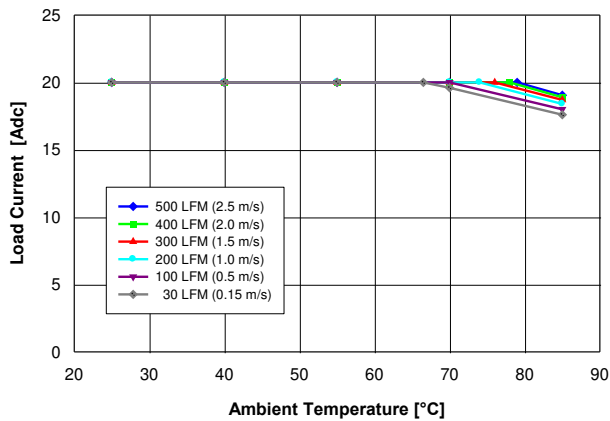


Fig. 2.5V.1: Available load current vs. ambient temperature and airflow rates for $V_{out} = 2.5\text{ V}$ converter mounted vertically with $V_{in} = 5\text{ V}$, air flowing from pin 1 and pin 6 to pins 2 – 5, and maximum MOSFET temperature $\leq 120\text{ }^\circ\text{C}$.

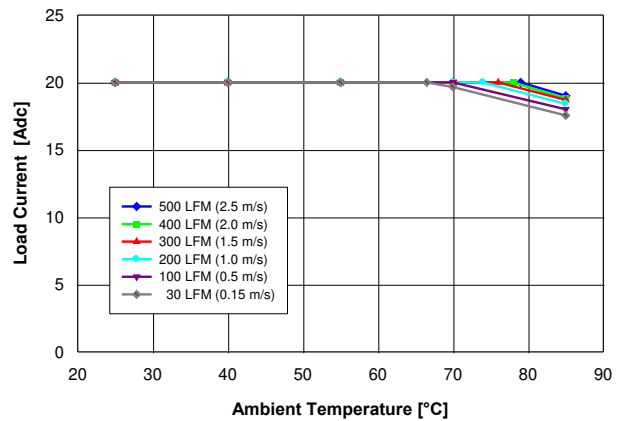


Fig. 2.5V.2: Available load current vs. ambient temperature and airflow rates for $V_{out} = 2.5\text{ V}$ converter mounted horizontally with $V_{in} = 5\text{ V}$, air flowing from pin 1 and pin 6 to pins 2 – 5, and maximum MOSFET temperature $\leq 120\text{ }^\circ\text{C}$.

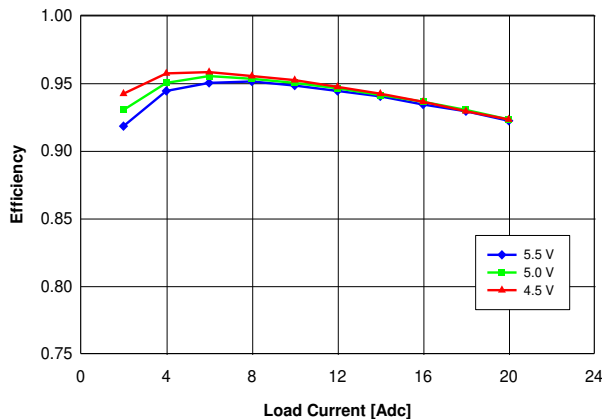


Fig. 2.5V.3: Efficiency vs. load current and input voltage for $V_{out} = 2.5\text{ V}$ converter mounted vertically with air flowing from pin 1 and pin 6 to pins 2 – 5 at a rate of 200 LFM (1 m/s) and $T_a = 25\text{ }^\circ\text{C}$.

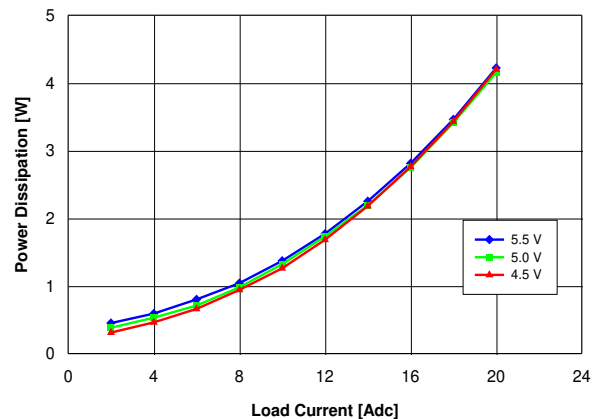


Fig. 2.5V.4: Power loss vs. load current and input voltage for $V_{out} = 2.5\text{ V}$ converter mounted vertically with air flowing from pin 1 and pin 6 to pins 2 – 5 at a rate of 200 LFM (1 m/s) and $T_a = 25\text{ }^\circ\text{C}$.

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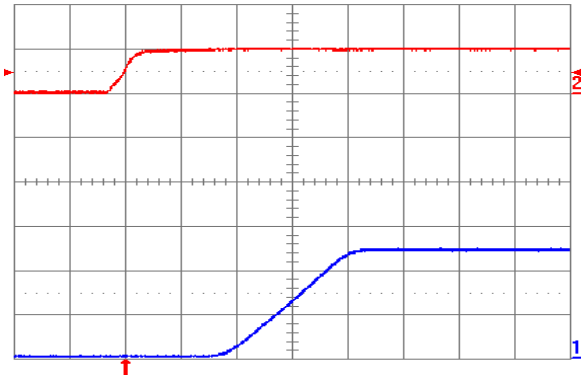


Fig. 2.5V.5: Turn-on transient for $V_{out} = 2.5\text{ V}$ with application of V_{in} at full rated load current (resistive) and $100\ \mu\text{F}$ external capacitance at $V_{in} = 5\text{ V}$. Top trace: V_{in} (5 V/div.); Bottom trace: output voltage (1 V/div.); Time scale: 2 ms/div.

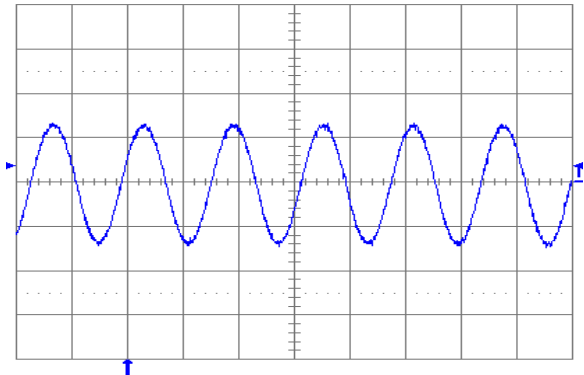


Fig. 2.5V.6: Output voltage ripple (20 mV/div.) at full rated load current into a resistive load with external capacitance $100\ \mu\text{F}$ ceramic + $1\ \mu\text{F}$ ceramic and $V_{in} = 5\text{ V}$ for $V_{out} = 2.5\text{ V}$. Time scale: 2 μs /div.

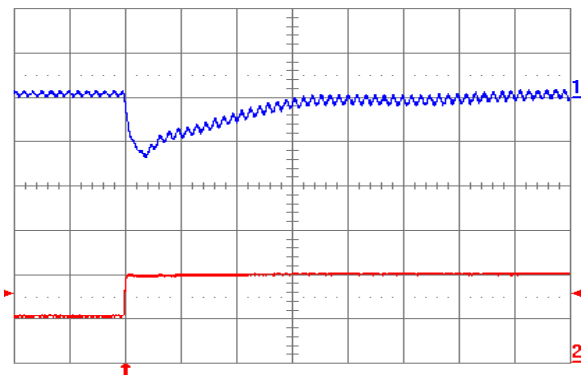


Fig. 2.5V.7: Output voltage response for $V_{out} = 2.5\text{ V}$ to positive load current step change from 10 A to 20 A with slew rate of $5\text{ A}/\mu\text{s}$ at $V_{in} = 5\text{ V}$. Top trace: output voltage (100 mV/div.); Bottom trace: load current (10 A/div.). $C_o = 100\ \mu\text{F}$ ceramic. Time scale: 20 μs /div.

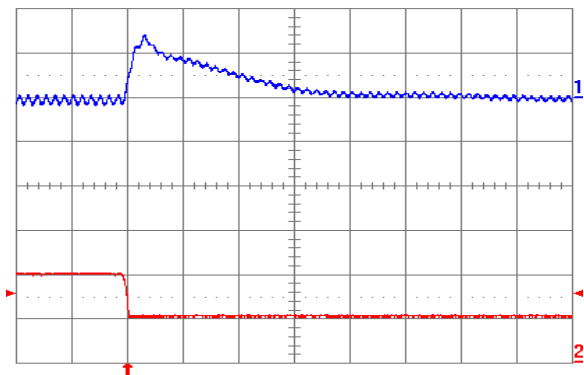


Fig. 2.5V.8: Output voltage response for $V_{out} = 2.5\text{ V}$ to negative load current step change from 20 A to 10 A with slew rate of $-5\text{ A}/\mu\text{s}$ at $V_{in} = 5\text{ V}$. Top trace: output voltage (100 mV/div.); Bottom trace: load current (10 A/div.). $C_o = 100\ \mu\text{F}$ ceramic. Time scale: 20 μs /div.

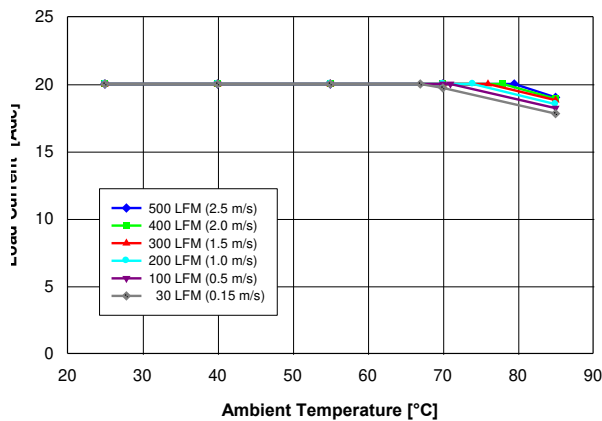


Fig. 2.0V.1: Available load current vs. ambient temperature and airflow rates for $V_{out} = 2.0\text{ V}$ converter mounted vertically with $V_{in} = 5\text{ V}$, air flowing from pin 1 and pin 6 to pins 2 – 5, and maximum MOSFET temperature $\leq 120\text{ }^\circ\text{C}$.

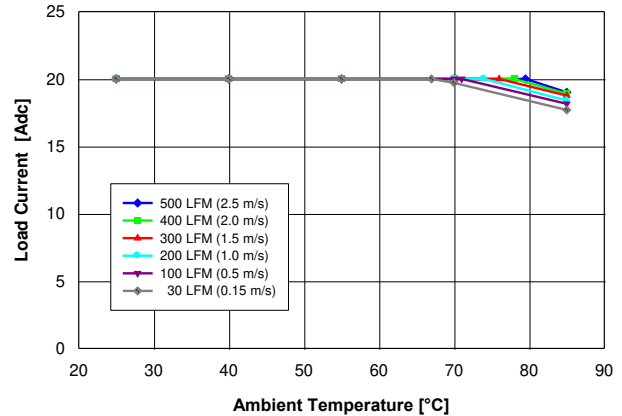


Fig. 2.0V.2: Available load current vs. ambient temperature and airflow rates for $V_{out} = 2.0\text{ V}$ converter mounted horizontally with $V_{in} = 5\text{ V}$, air flowing from pin 1 and pin 6 to pins 2 – 5, and maximum MOSFET temperature $\leq 120\text{ }^\circ\text{C}$.

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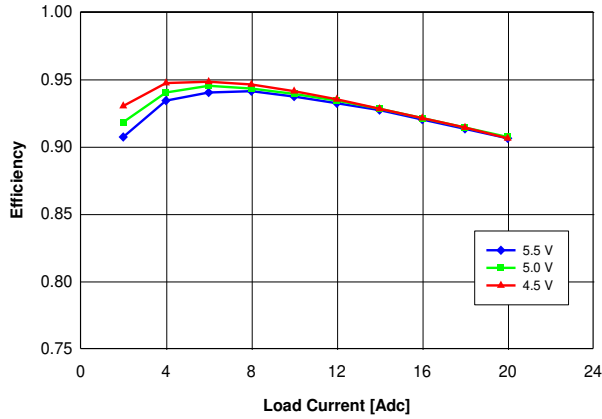


Fig. 2.0V.3: Efficiency vs. load current and input voltage for $V_{out} = 2.0\text{ V}$ converter mounted vertically with air flowing from pin 1 and pin 6 to pins 2 – 5 at a rate of 200 LFM (1 m/s) and $T_a = 25\text{ }^\circ\text{C}$.

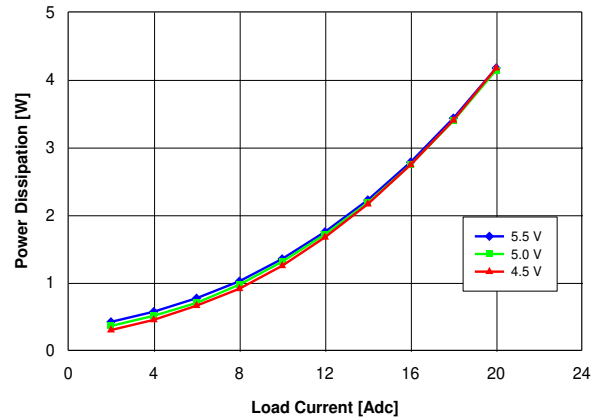


Fig. 2.0V.4: Power loss vs. load current and input voltage for $V_{out} = 2.0\text{ V}$ converter mounted vertically with air flowing from pin 1 and pin 6 to pins 2 – 5 at a rate of 200 LFM (1 m/s) and $T_a = 25\text{ }^\circ\text{C}$.

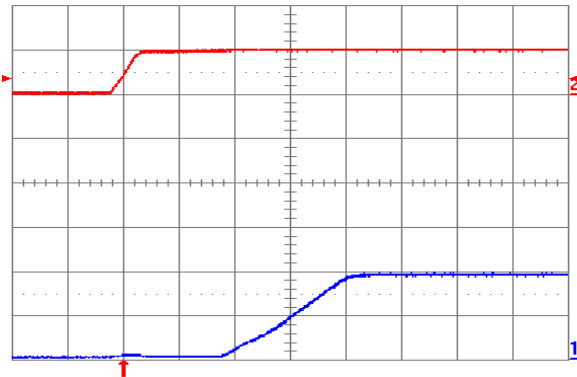


Fig. 2.0V.5: Turn-on transient for $V_{out} = 2.0\text{ V}$ with application of V_{in} at full rated load current (resistive) and $100\text{ }\mu\text{F}$ external capacitance at $V_{in} = 5\text{ V}$. Top trace: V_{in} (5 V/div.); Bottom trace: output voltage (1 V/div.); Time scale: 2 ms/div.

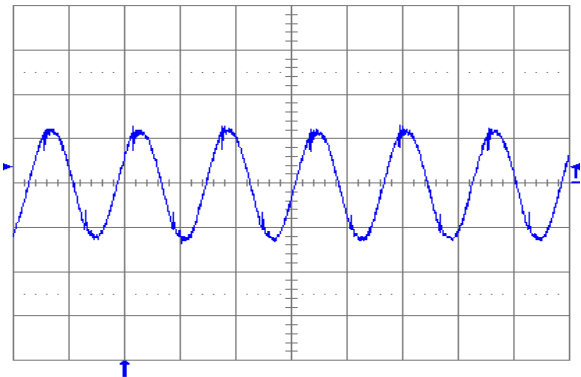


Fig. 2.0V.6: Output voltage ripple (20 mV/div.) at full rated load current into a resistive load with external capacitance $100\text{ }\mu\text{F}$ ceramic + $1\text{ }\mu\text{F}$ ceramic and $V_{in} = 5\text{ V}$ for $V_{out} = 2.0\text{ V}$. Time scale: 2 μs /div.

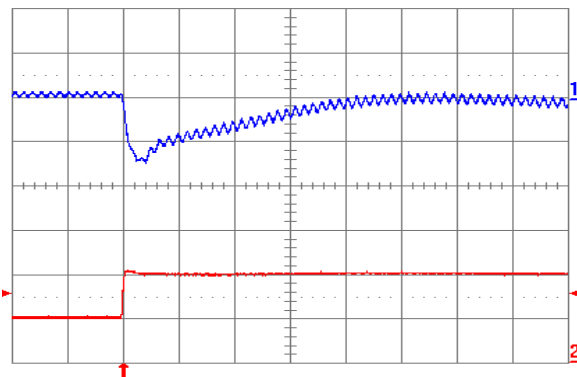


Fig. 2.0V.7: Output voltage response for $V_{out} = 2.0\text{ V}$ to positive load current step change from 10 A to 20 A with slew rate of $5\text{ A}/\mu\text{s}$ at $V_{in} = 5\text{ V}$. Top trace: output voltage (100 mV/div.); Bottom trace: load current (10 A/div.). $C_o = 100\text{ }\mu\text{F}$ ceramic. Time scale: 20 μs /div.

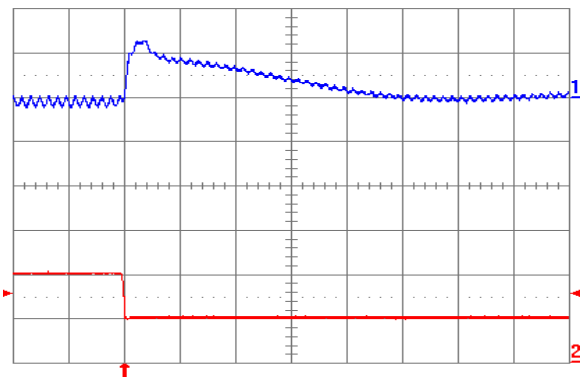


Fig. 2.0V.8: Output voltage response for $V_{out} = 2.0\text{ V}$ to negative load current step change from 20 A to 10 A with slew rate of $-5\text{ A}/\mu\text{s}$ at $V_{in} = 5\text{ V}$. Top trace: output voltage (100 mV/div.); Bottom trace: load current (10 A/div.). $C_o = 100\text{ }\mu\text{F}$ ceramic. Time scale: 20 μs /div.

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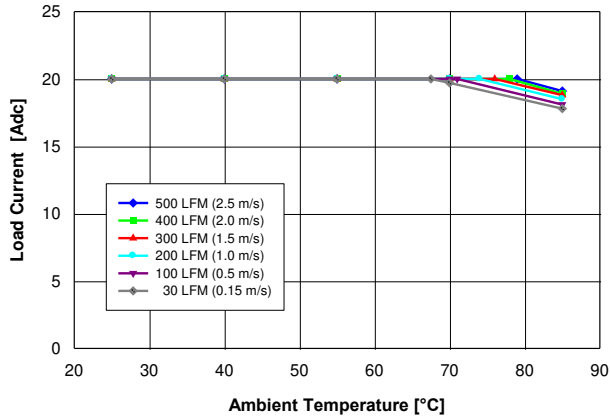


Fig. 1.8V.1: Available load current vs. ambient temperature and airflow rates for $V_{out} = 1.8\text{ V}$ converter mounted vertically with $V_{in} = 5\text{ V}$, air flowing from pin 1 and pin 6 to pins 2 - 5, and maximum MOSFET temperature $\leq 120\text{ }^{\circ}\text{C}$.

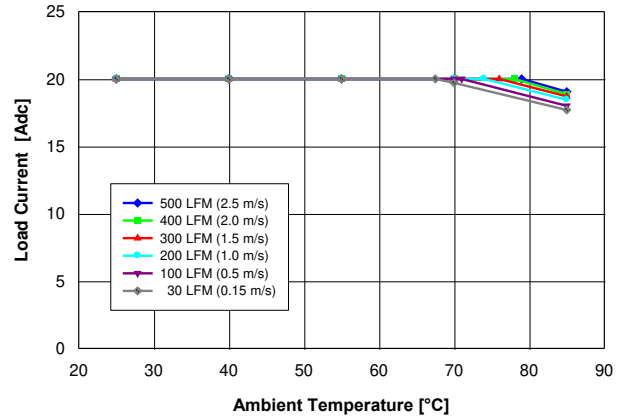


Fig. 1.8V.2: Available load current vs. ambient temperature and airflow rates for $V_{out} = 1.8\text{ V}$ converter mounted horizontally with $V_{in} = 5\text{ V}$, air flowing from pin 1 and pin 6 to pins 2 - 5, and maximum MOSFET temperature $\leq 120\text{ }^{\circ}\text{C}$.

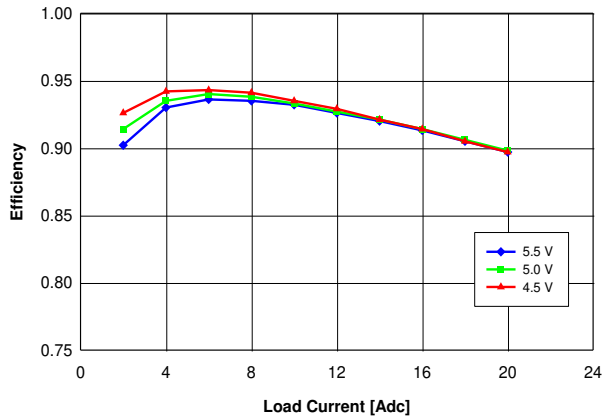


Fig. 1.8V.3: Efficiency vs. load current and input voltage for $V_{out} = 1.8\text{ V}$ converter mounted vertically with air flowing from pin 1 and pin 6 to pins 2 - 5 at a rate of 200 LFM (1 m/s) and $T_a = 25\text{ }^{\circ}\text{C}$.

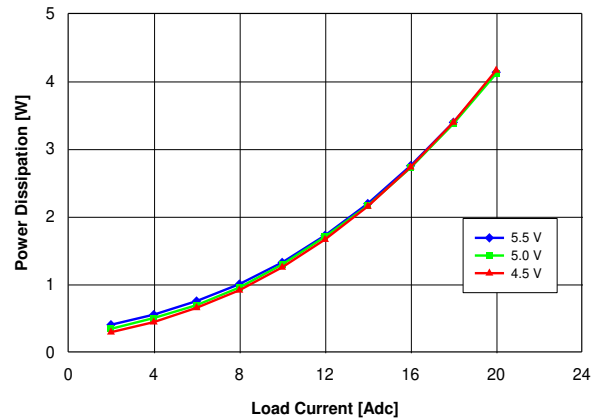


Fig. 1.8V.4: Power loss vs. load current and input voltage for $V_{out} = 1.8\text{ V}$ converter mounted vertically with air flowing from pin 1 and pin 6 to pins 2 - 5 at a rate of 200 LFM (1 m/s) and $T_a = 25\text{ }^{\circ}\text{C}$.

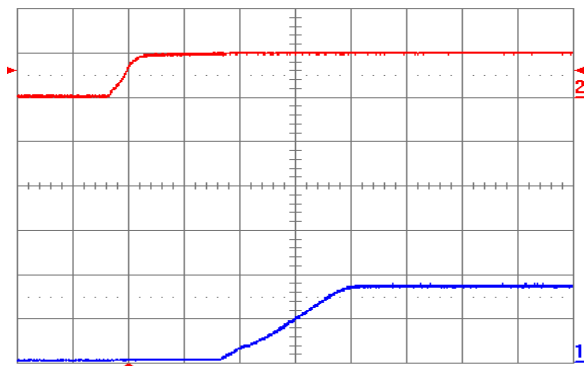


Fig. 1.8V.5: Turn-on transient for $V_{out} = 1.8\text{ V}$ with application of V_{in} at full rated load current (resistive) and $100\text{ }\mu\text{F}$ external capacitance at $V_{in} = 5\text{ V}$. Top trace: V_{in} (5 V/div.); Bottom trace: output voltage (1 V/div.); Time scale: 2 ms/div.

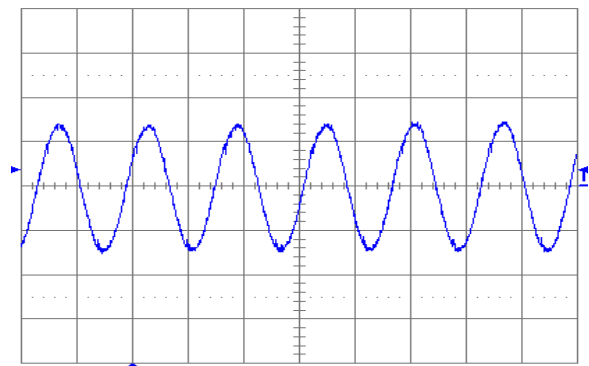


Fig. 1.8V.6: Output voltage ripple (20 mV/div.) at full rated load current into a resistive load with external capacitance $100\text{ }\mu\text{F}$ ceramic + $1\text{ }\mu\text{F}$ ceramic and $V_{in} = 5\text{ V}$ for $V_{out} = 1.8\text{ V}$. Time scale: 2 μs/div.

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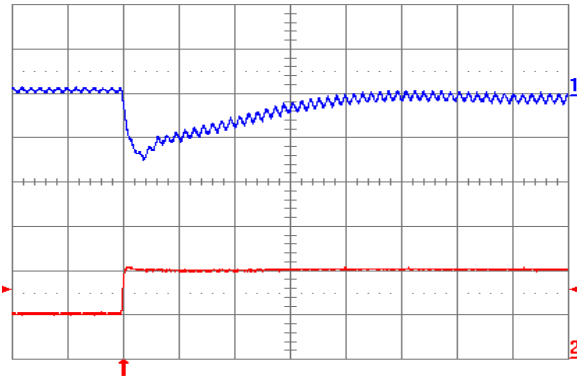


Fig. 1.8V.7: Output voltage response for $V_{out} = 1.8\text{ V}$ to positive load current step change from 10 A to 20 A with slew rate of $5\text{ A}/\mu\text{s}$ at $V_{in} = 5\text{ V}$. Top trace: output voltage (100 mV/div.); Bottom trace: load current (10 A/div.). $C_o = 100\text{ }\mu\text{F}$ ceramic. Time scale: $20\text{ }\mu\text{s}/\text{div}$.

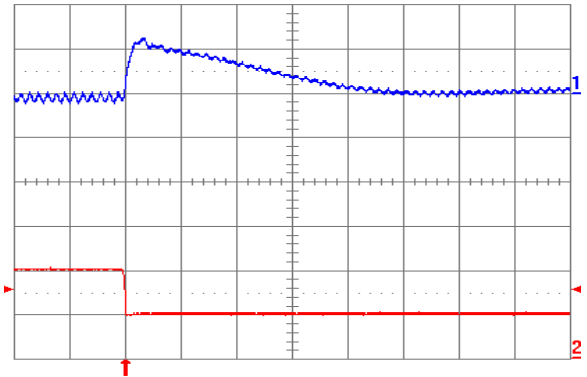


Fig. 1.8V.8: Output voltage response for $V_{out} = 1.8\text{ V}$ to negative load current step change from 20 A to 10 A with slew rate of $-5\text{ A}/\mu\text{s}$ at $V_{in} = 5\text{ V}$. Top trace: output voltage (100 mV/div.); Bottom trace: load current (10 A/div.). $C_o = 100\text{ }\mu\text{F}$ ceramic. Time scale: $20\text{ }\mu\text{s}/\text{div}$.

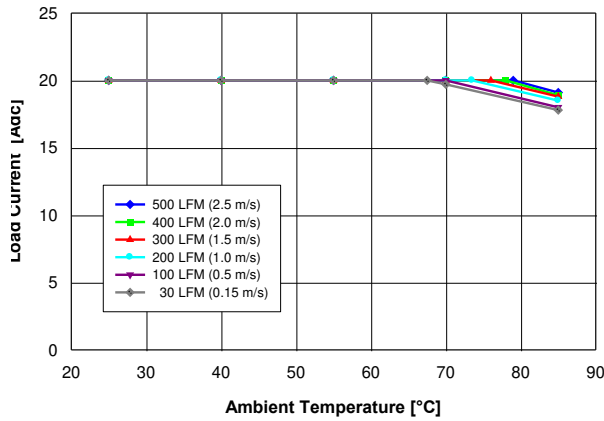


Fig. 1.5V.1: Available load current vs. ambient temperature and airflow rates for $V_{out} = 1.5\text{ V}$ converter mounted vertically with $V_{in} = 5\text{ V}$, air flowing from pin 1 and pin 6 to pins 2 – 5, and maximum MOSFET temperature $\leq 120^\circ\text{C}$.

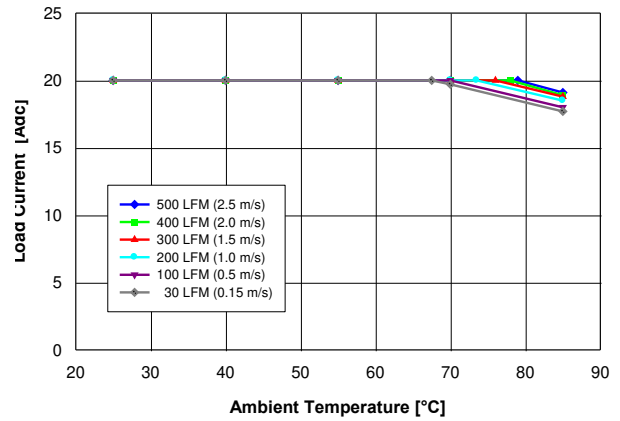


Fig. 1.5V.2: Available load current vs. ambient temperature and airflow rates for $V_{out} = 1.5\text{ V}$ converter mounted horizontally with $V_{in} = 5\text{ V}$, air flowing from pin 1 and pin 6 to pins 2 – 5, and maximum MOSFET temperature $\leq 120^\circ\text{C}$.

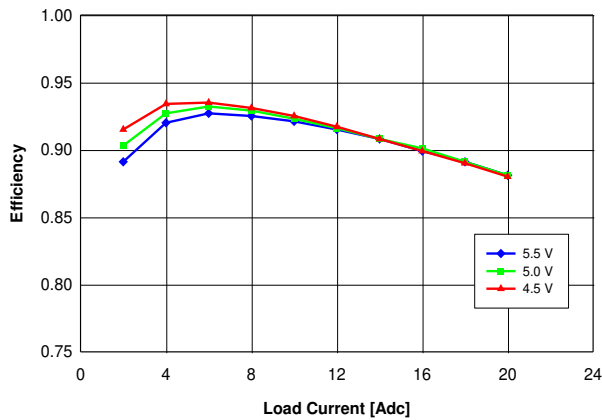


Fig. 1.5V.3: Efficiency vs. load current and input voltage for $V_{out} = 1.5\text{ V}$ converter mounted vertically with air flowing from pin 1 and pin 6 to pins 2 – 5 at a rate of 200 LFM (1 m/s) and $T_a = 25^\circ\text{C}$.

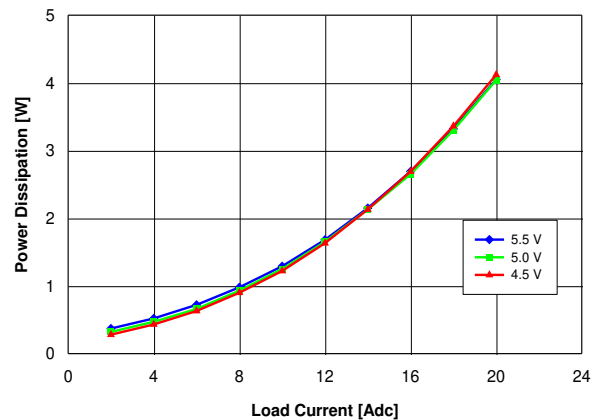


Fig. 1.5V.4: Power loss vs. load current and input voltage for $V_{out} = 1.5\text{ V}$ converter mounted vertically with air flowing from pin 1 and pin 6 to pins 2 – 5 at a rate of 200 LFM (1 m/s) and $T_a = 25^\circ\text{C}$.

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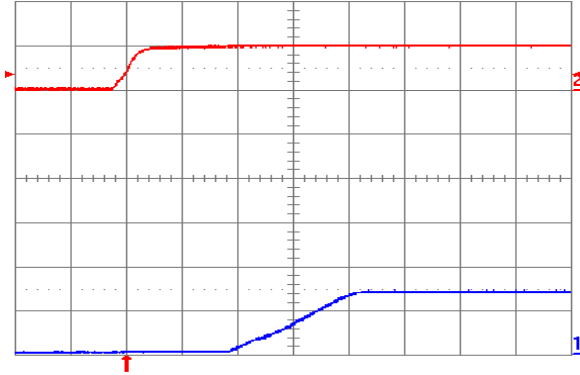


Fig. 1.5V.5: Turn-on transient for $V_{out} = 1.5\text{ V}$ with application of V_{in} at full rated load current (resistive) and $100\ \mu\text{F}$ external capacitance at $V_{in} = 5\text{ V}$. Top trace: V_{in} (5 V/div.); Bottom trace: output voltage (1 V/div.); Time scale: 2 ms/div.

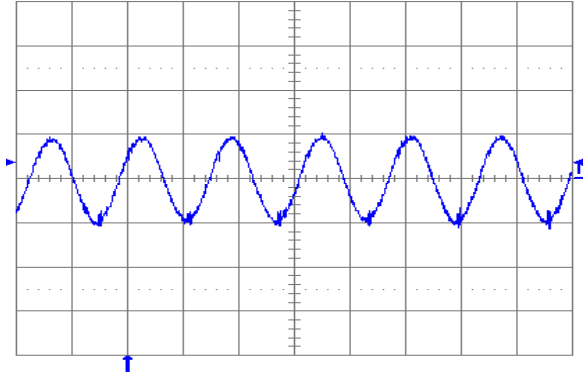


Fig. 1.5V.6: Output voltage ripple (20 mV/div.) at full rated load current into a resistive load with external capacitance $100\ \mu\text{F}$ ceramic + $1\ \mu\text{F}$ ceramic and $V_{in} = 5\text{ V}$ for $V_{out} = 1.5\text{ V}$. Time scale: $2\ \mu\text{s}/\text{div}$.

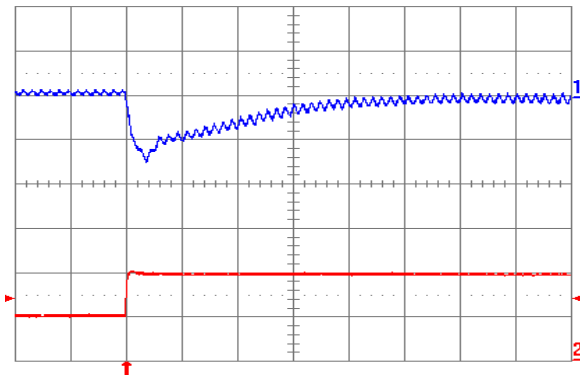


Fig. 1.5V.7: Output voltage response for $V_{out} = 1.5\text{ V}$ to positive load current step change from 10 A to 20 A with slew rate of $5\text{ A}/\mu\text{s}$ at $V_{in} = 5\text{ V}$. Top trace: output voltage (100 mV/div.); Bottom trace: load current (10 A/div.). $C_o = 100\ \mu\text{F}$ ceramic. Time scale: $20\ \mu\text{s}/\text{div}$.

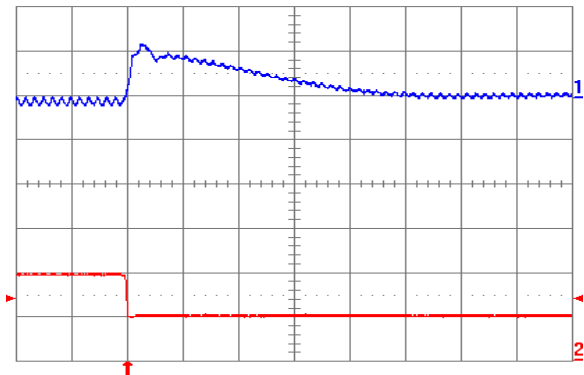


Fig. 1.5V.8: Output voltage response for $V_{out} = 1.5\text{ V}$ to negative load current step change from 20 A to 10 A with slew rate of $-5\text{ A}/\mu\text{s}$ at $V_{in} = 5\text{ V}$. Top trace: output voltage (100 mV/div.); Bottom trace: load current (10 A/div.). $C_o = 100\ \mu\text{F}$ ceramic. Time scale: $20\ \mu\text{s}/\text{div}$.

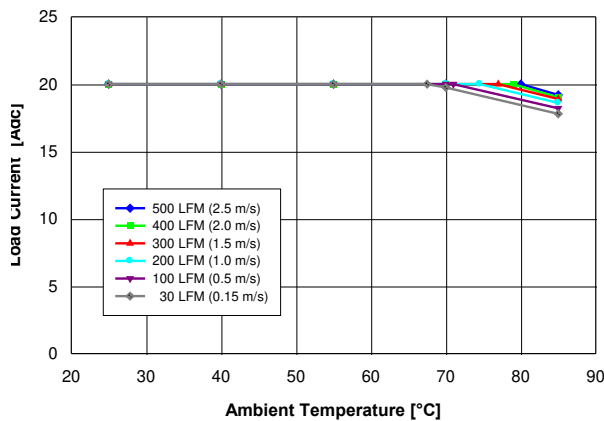


Fig. 1.2V.1: Available load current vs. ambient temperature and airflow rates for $V_{out} = 1.2\text{ V}$ converter mounted vertically with $V_{in} = 5\text{ V}$, air flowing from pin 1 and pin 6 to pins 2 – 5, and maximum MOSFET temperature $\leq 120\text{ }^\circ\text{C}$.

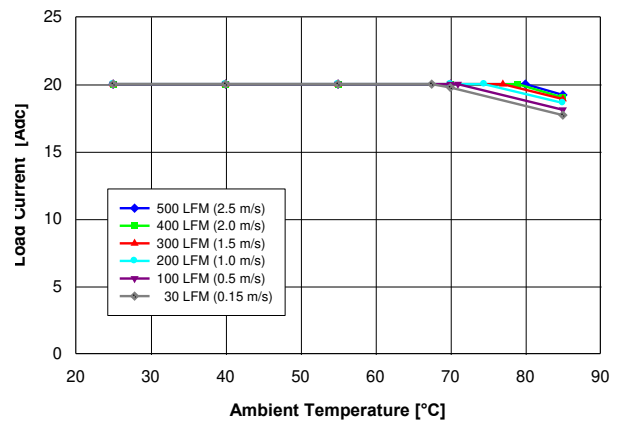


Fig. 1.2V.2: Available load current vs. ambient temperature and airflow rates for $V_{out} = 1.2\text{ V}$ converter mounted horizontally with $V_{in} = 5\text{ V}$, air flowing from pin 1 and pin 6 to pins 2 – 5, and maximum MOSFET temperature $\leq 120\text{ }^\circ\text{C}$.

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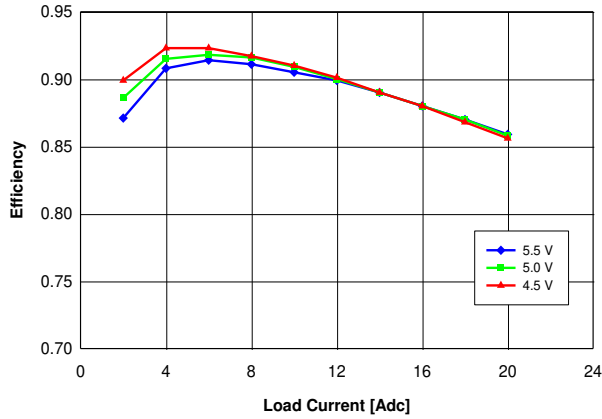


Fig. 1.2V.3: Efficiency vs. load current and input voltage for $V_{out} = 1.2\text{ V}$ converter mounted vertically with air flowing from pin 1 and pin 6 to pins 2 – 5 at a rate of 200 LFM (1 m/s) and $T_a = 25\text{ }^\circ\text{C}$.

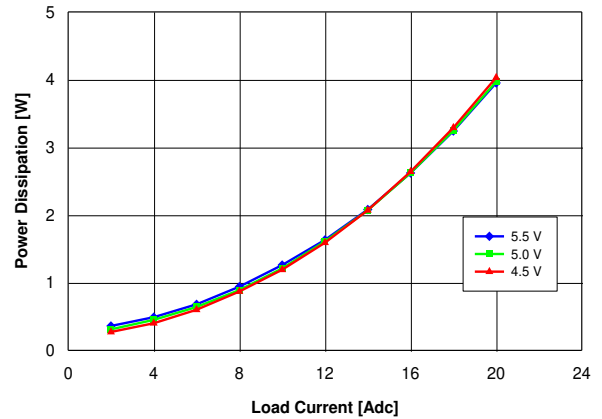


Fig. 1.2V.4: Power loss vs. load current and input voltage for $V_{out} = 1.2\text{ V}$ converter mounted vertically with air flowing from pin 1 and pin 6 to pins 2 – 5 at a rate of 200 LFM (1 m/s) and $T_a = 25\text{ }^\circ\text{C}$.

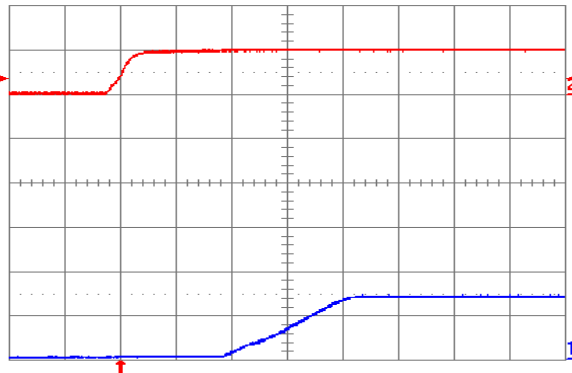


Fig. 1.2V.5: Turn-on transient for $V_{out} = 1.2\text{ V}$ with application of V_{in} at full rated load current (resistive) and $100\text{ }\mu\text{F}$ external capacitance at $V_{in} = 5\text{ V}$. Top trace: V_{in} (5 V/div.); Bottom trace: output voltage (1 V/div.); Time scale: 2 ms/div.

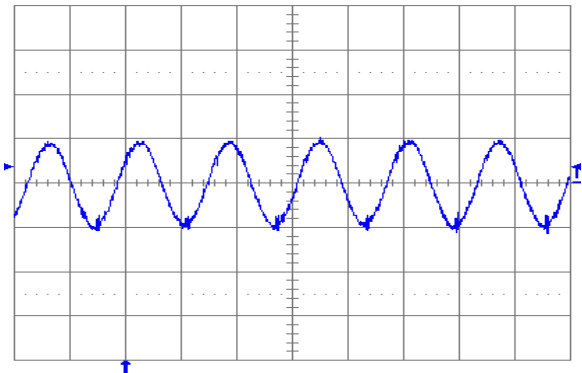


Fig. 1.2V.6: Output voltage ripple (20 mV/div.) at full rated load current into a resistive load with external capacitance $100\text{ }\mu\text{F}$ ceramic + $1\text{ }\mu\text{F}$ ceramic and $V_{in} = 5\text{ V}$ for $V_{out} = 1.2\text{ V}$. Time scale: 2 μs /div.

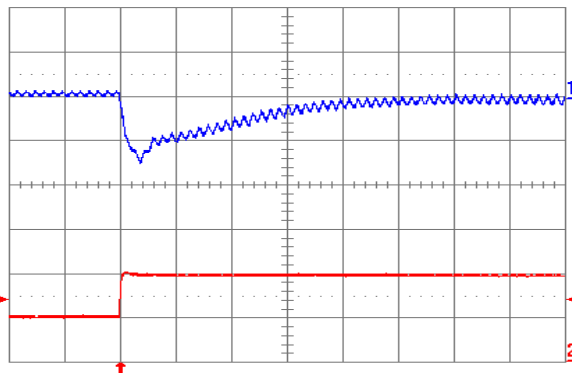


Fig. 1.2V.7: Output voltage response for $V_{out} = 1.2\text{ V}$ to positive load current step change from 10 A to 20 A with slew rate of $5\text{ A}/\mu\text{s}$ at $V_{in} = 5\text{ V}$. Top trace: output voltage (100 mV/div.); Bottom trace: load current (10 A/div.). $C_o = 100\text{ }\mu\text{F}$ ceramic. Time scale: 20 μs /div.

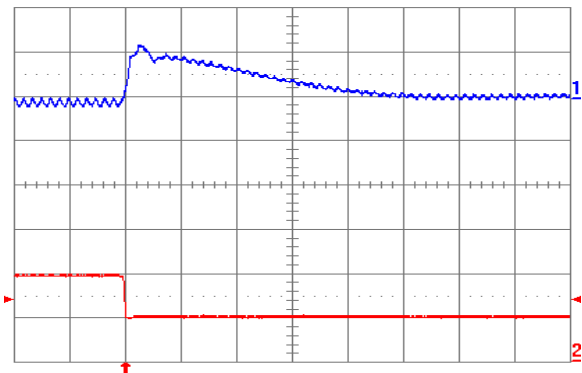


Fig. 1.2V.8: Output voltage response for $V_{out} = 1.2\text{ V}$ to negative load current step change from 20 A to 10 A with slew rate of $-5\text{ A}/\mu\text{s}$ at $V_{in} = 5\text{ V}$. Top trace: output voltage (100 mV/div.); Bottom trace: load current (10 A/div.). $C_o = 100\text{ }\mu\text{F}$ ceramic. Time scale: 20 μs /div.

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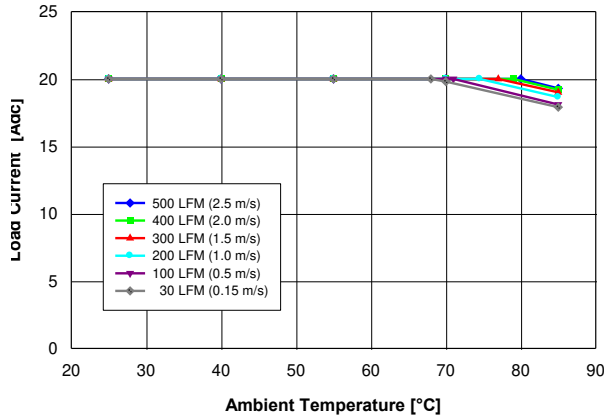


Fig. 1.0V.1: Available load current vs. ambient temperature and airflow rates for $V_{out} = 1.0$ V converter mounted vertically with $V_{in} = 5$ V, air flowing from pin 1 and pin 6 to pins 2 – 5, and maximum MOSFET temperature ≤ 120 °C.

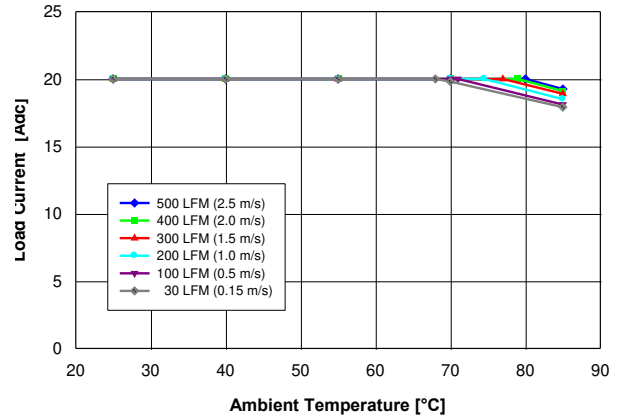


Fig. 1.0V.2: Available load current vs. ambient temperature and airflow rates for $V_{out} = 1.0$ V converter mounted horizontally with $V_{in} = 5$ V, air flowing from pin 1 and pin 6 to pins 2 – 5, and maximum MOSFET temperature ≤ 120 °C.

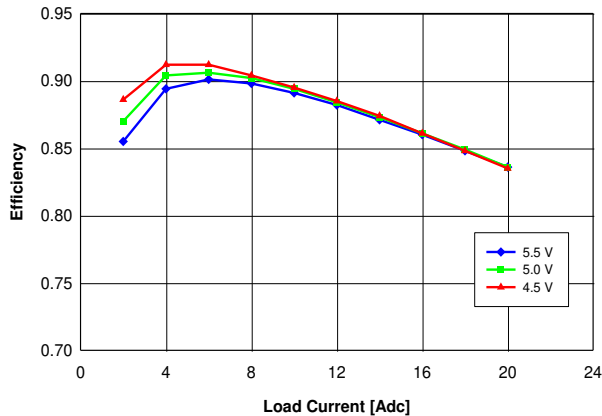


Fig. 1.0V.3: Efficiency vs. load current and input voltage for $V_{out} = 1.0$ V converter mounted vertically with air flowing from pin 1 and pin 6 to pins 2 – 5 at a rate of 200 LFM (1 m/s) and $T_a = 25$ °C.

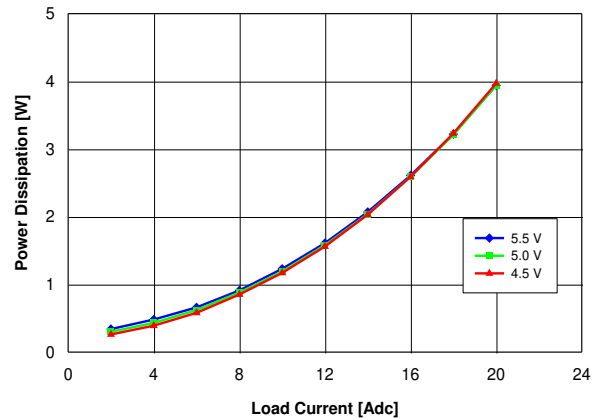


Fig. 1.0V.4: Power loss vs. load current and input voltage for $V_{out} = 1.0$ V converter mounted vertically with air flowing from pin 1 and pin 6 to pins 2 – 5 at a rate of 200 LFM (1 m/s) and $T_a = 25$ °C.

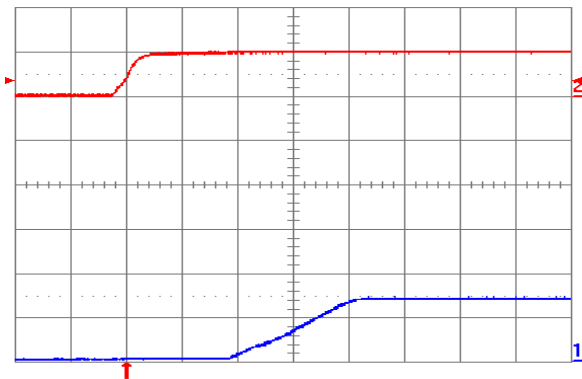


Fig. 1.0V.5: Turn-on transient for $V_{out} = 1.0$ V with application of V_{in} at full rated load current (resistive) and 100 μ F external capacitance at $V_{in} = 5$ V. Top trace: V_{in} (5 V/div.); Bottom trace: output voltage (1 V/div.); Time scale: 2 ms/div.

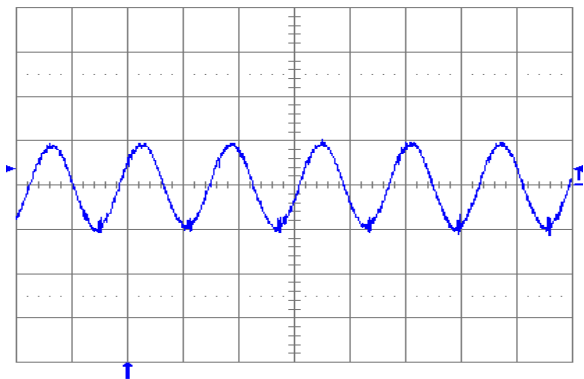


Fig. 1.0V.6: Output voltage ripple (20 mV/div.) at full rated load current into a resistive load with external capacitance 100 μ F ceramic + 1 μ F ceramic and $V_{in} = 5$ V for $V_{out} = 1.0$ V. Time scale: 2 μ s/div.

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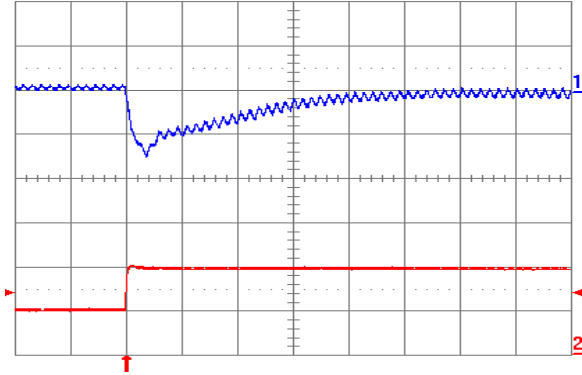


Fig. 1.0V.7: Output voltage response for $V_{out} = 1.0\text{ V}$ to positive load current step change from 10 A to 20 A with slew rate of $5\text{ A}/\mu\text{s}$ at $V_{in} = 5\text{ V}$. Top trace: output voltage (100 mV/div.); Bottom trace: load current (10 A/div.). $C_o = 100\text{ }\mu\text{F}$ ceramic. Time scale: $20\text{ }\mu\text{s}/\text{div}$.

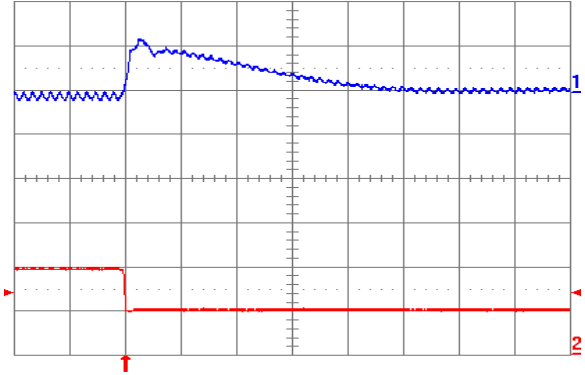


Fig. 1.0V.8: Output voltage response for $V_{out} = 1.0\text{ V}$ to negative load current step change from 20 A to 10 A with slew rate of $-5\text{ A}/\mu\text{s}$ at $V_{in} = 5\text{ V}$. Top trace: output voltage (100 mV/div.); Bottom trace: load current (10 A/div.). $C_o = 100\text{ }\mu\text{F}$ ceramic. Time scale: $20\text{ }\mu\text{s}/\text{div}$.

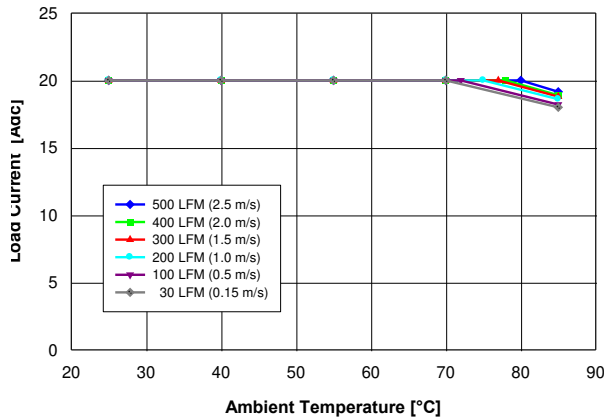


Fig. 0.7525V.1: Available load current vs. ambient temperature and airflow rates for $V_{out} = 0.7525\text{ V}$ converter mounted vertically with $V_{in} = 5\text{ V}$, air flowing from pin 1 and pin 6 to pins 2 – 5, and maximum MOSFET temperature $\leq 120\text{ }^\circ\text{C}$.

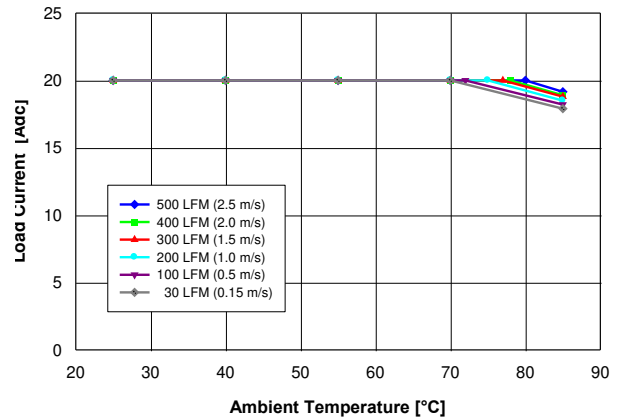


Fig. 0.7525V.2: Available load current vs. ambient temperature and airflow rates for $V_{out} = 0.7525\text{ V}$ converter mounted horizontally with $V_{in} = 5\text{ V}$, air flowing from pin 1 and pin 6 to pins 2 – 5, and maximum MOSFET temperature $\leq 120\text{ }^\circ\text{C}$.

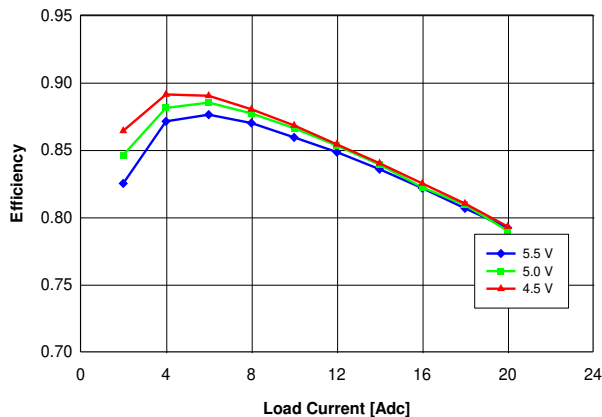


Fig. 0.7525V.3: Efficiency vs. load current and input voltage for $V_{out} = 0.7525\text{ V}$ converter mounted vertically with air flowing from pin 1 and pin 6 to pins 2 – 5 at a rate of 200 LFM (1 m/s) and $T_a = 25\text{ }^\circ\text{C}$.

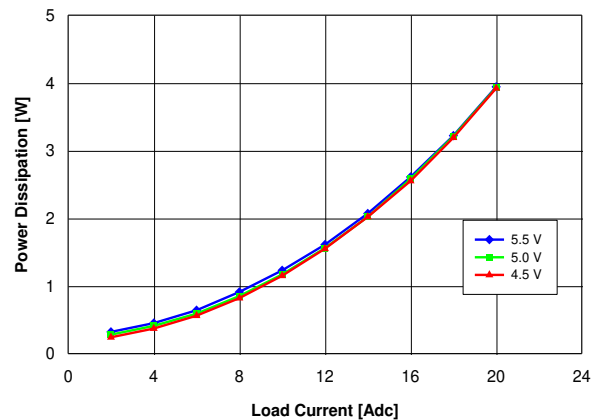


Fig. 0.7525V.4: Power loss vs. load current and input voltage for $V_{out} = 0.7525\text{ V}$ converter mounted vertically with air flowing from pin 1 and pin 6 to pins 2 – 5 at a rate of 200 LFM (1 m/s) and $T_a = 25\text{ }^\circ\text{C}$.

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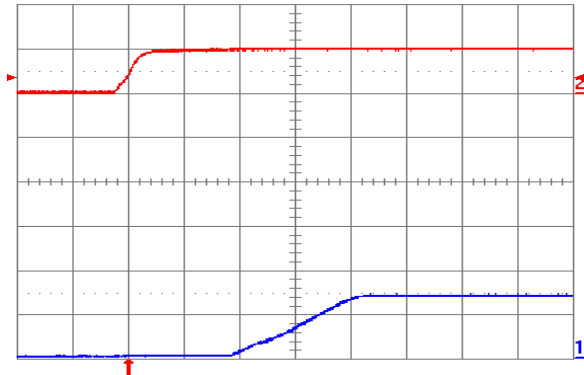


Fig. 0.7525V.5: Turn-on transient for $V_{out} = 0.7525$ V with application of V_{in} at full rated load current (resistive) and $100 \mu\text{F}$ external capacitance at $V_{in} = 5$ V. Top trace: V_{in} (5 V/div.); Bottom trace: output voltage (1 V/div.); Time scale: 2 ms/div.

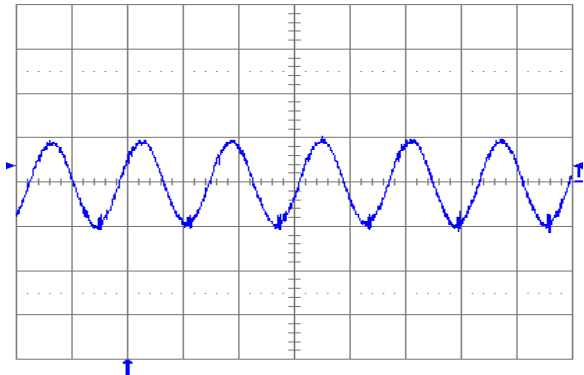


Fig. 0.7525V.6: Output voltage ripple (20 mV/div.) at full rated load current into a resistive load with external capacitance $100 \mu\text{F}$ ceramic + $1 \mu\text{F}$ ceramic and $V_{in} = 5$ V for $V_{out} = 0.7525$ V. Time scale: 2 μs /div.

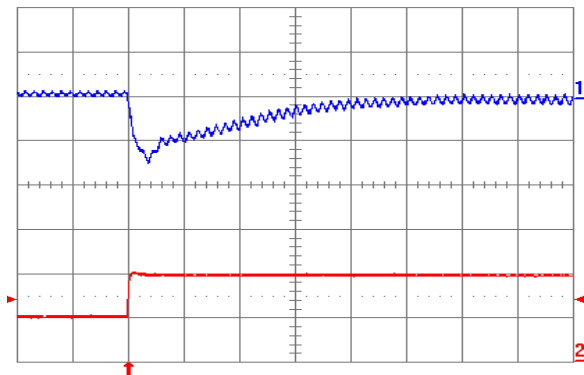


Fig. 0.7525V.7: Output voltage response for $V_{out} = 0.7525$ V to positive load current step change from 10 A to 20 A with slew rate of $5 \text{ A}/\mu\text{s}$ at $V_{in} = 5$ V. Top trace: output voltage (100 mV/div.); Bottom trace: load current (10 A/div.). $C_o = 100 \mu\text{F}$ ceramic. Time scale: 20 μs /div.

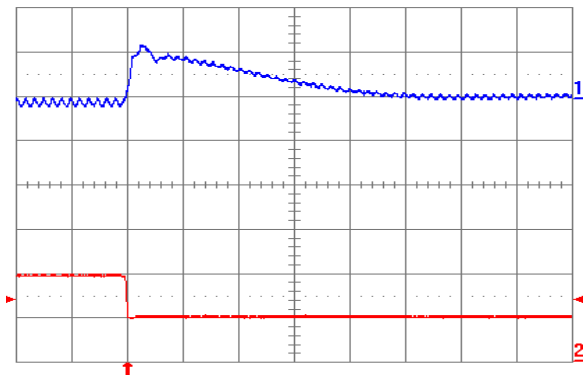
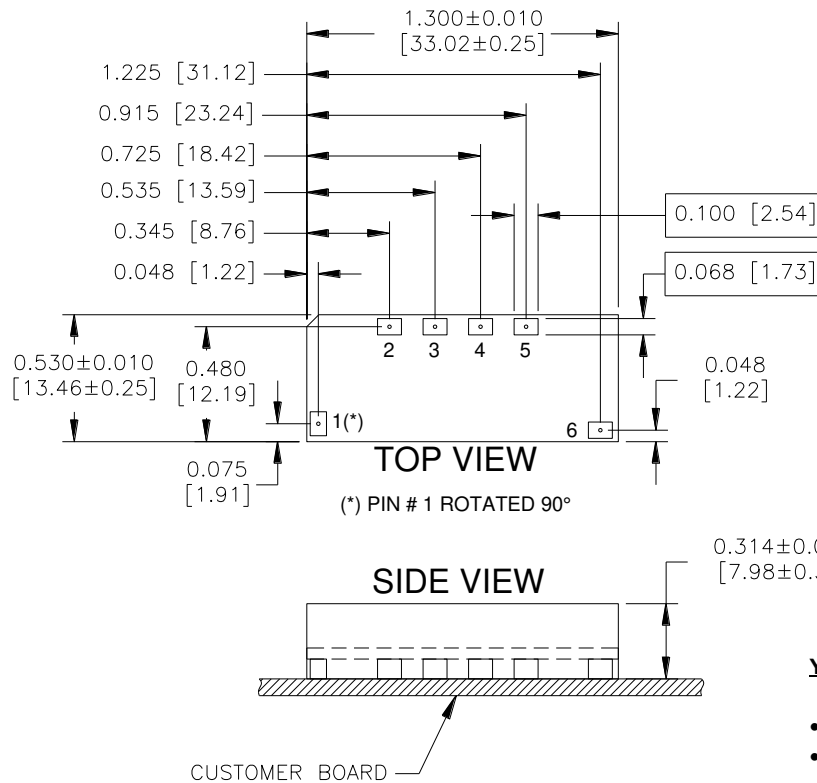


Fig. 0.7525V.8: Output voltage response for $V_{out} = 0.7525$ V to negative load current step change from 20 A to 10 A with slew rate of $-5 \text{ A}/\mu\text{s}$ at $V_{in} = 5$ V. Top trace: output voltage (100 mV/div.); Bottom trace: load current (10 A/div.). $C_o = 100 \mu\text{F}$ ceramic. Time scale: 20 μs /div.

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5. PHYSICAL INFORMATION



| PAD/PIN CONNECTIONS | |
|---------------------|----------|
| Pad/Pin # | Function |
| 1 | ON/OFF |
| 2 | SENSE |
| 3 | TRIM |
| 4 | Vout |
| 5 | GND |
| 6 | Vin |

YNC05S20 Pinout (Surface Mount)

YNC05S20 Platform Notes

- All dimensions are in inches [mm]
- Connector Material: Copper
- Connector Finish: Gold over Nickel
- Module Weight: 0.22 oz [6.12 g]
- Module Height: 0.327" Max., 0.301" Min.
- Recommended Surface-Mount Pads:
Min. 0.080" X 0.112" [2.03 x 2.84]

6. ORDERING INFORMATION

| PRODUCT SERIES | INPUT VOLTAGE | MOUNTING SCHEME | RATED LOAD CURRENT | ENABLE LOGIC | ROHS COMPATIBLE |
|----------------|----------------|-------------------|------------------------------|--|--|
| YNC | 05 | S | 20 | - | 0 |
| Y-Series | 4.5 to 5.5 VDC | S ⇒ Surface-Mount | 20 A (0.7525 V to 3.63 V) | 0 ⇒ Standard (Positive Logic) D ⇒ Opposite of Standard (Negative Logic) | No Suffix ⇒ RoHS lead-solder-exempt compliant G ⇒ RoHS compliant for all six substances |

The example above describes P/N YNC05S20-0: 4.5V – 5.5 V input, surface mount, 20 A at 0.7525 V to 3.63 V output, standard enable logic and RoHS lead solder exemption. Please consult factory regarding availability of a specific version.

For more information on these products consult: tech.support@psbel.com

NUCLEAR AND MEDICAL APPLICATIONS - Products are not designed or intended for use as critical components in life support systems, equipment used in hazardous environments, or nuclear control systems.

TECHNICAL REVISIONS - The appearance of products, including safety agency certifications pictured on labels, may change depending on the date manufactured. Specifications are subject to change without notice.

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