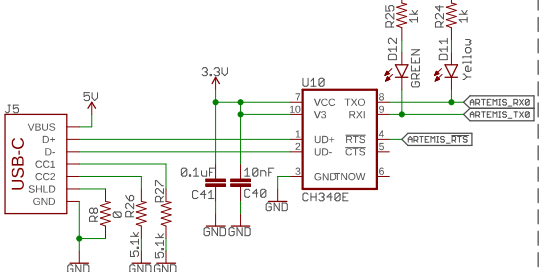
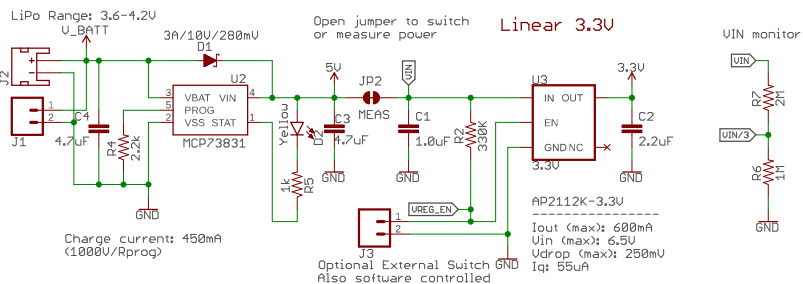


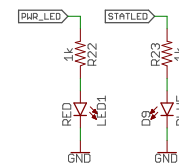
USB-to-Serial Converter



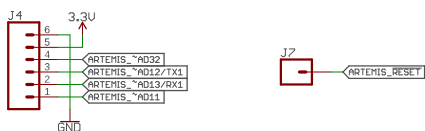
LiPo Charger



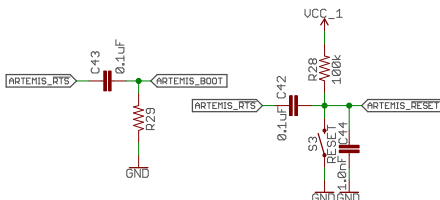
LEDs



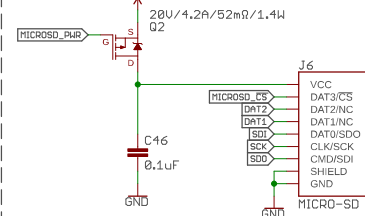
I/O, Power Headers



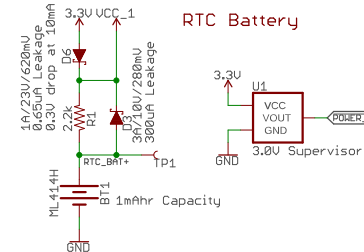
Bootloader Reset Circuit



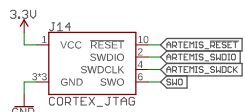
µSD



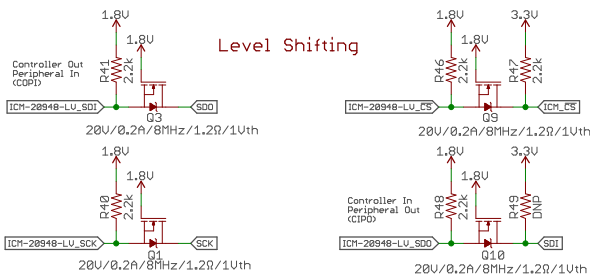
RTC Battery



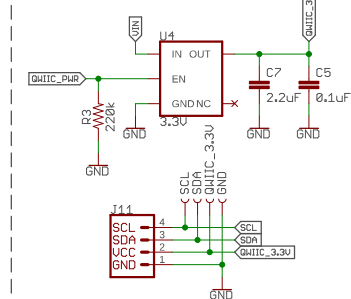
SWD Program/Debug Interface



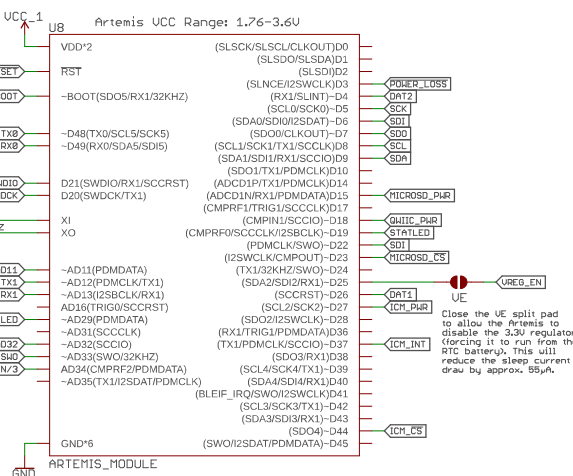
Level Shifting



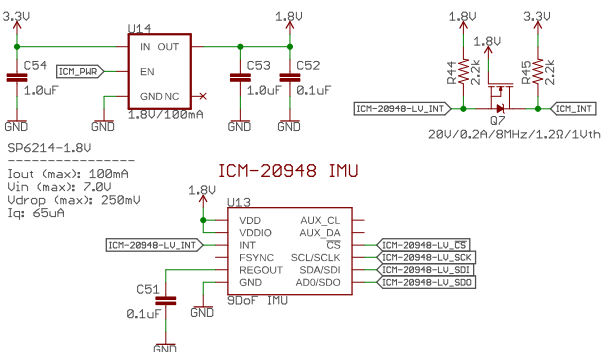
Qiic



Artemis



Voltage Regulator



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TITLE: SparkFun_Artemis_OpenLog

Design by: N. Seidle

REV: v10

Date: 9/9/2020 3:02 PM

Sheet: 1/1

Change from XB4 to U10;
ICM 209 moved from 22 to 27
SD CS moved from 10 to 23
Add PHR_LED on 29
Add VREG_EN on 28
Add SDIO connection to SD
Add VIN monitor on AR24 (R6/R7)
Qiic_PWR (18) will be active high