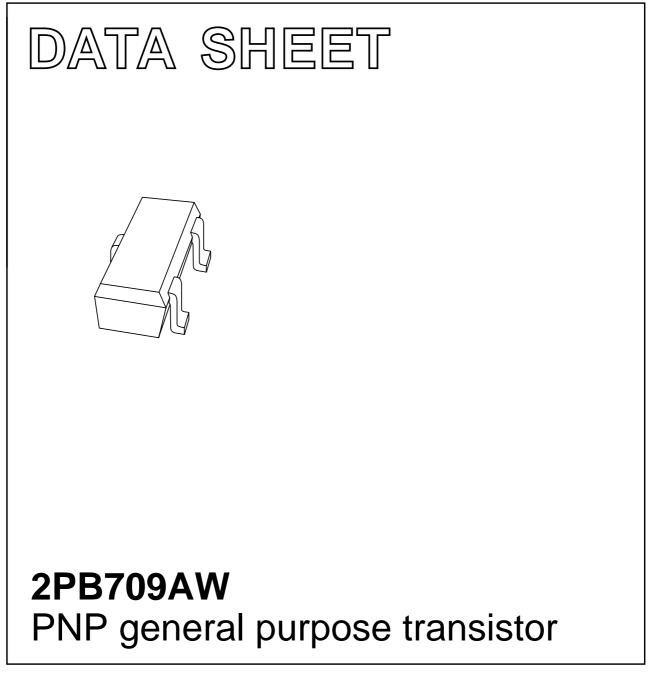
DISCRETE SEMICONDUCTORS



Product specification

2002 Jun 26



FEATURES

- High collector current (max. 100 mA)
- Low collector-emitter saturation voltage (max. 500 mV).

APPLICATIONS

• General purpose switching and amplification.

DESCRIPTION

PNP transistor in an SC-70 (SOT323) plastic package. NPN complement: 2PD601AW

MARKING

| TYPE NUMBER | MARKING CODE ⁽¹⁾ |
|-------------|-----------------------------|
| 2PB709AQW | N5* |
| 2PB709ARW | N7* |
| 2PB709ASW | N9* |

Note

- 1. * = p: made in Hong Kong.
 - * = t: made in Malaysia.

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

| SYMBOL | PARAMETER | CONDITIONS | MIN. | MAX. | UNIT |
|------------------|-------------------------------|----------------------------------|------|------|------|
| V _{CBO} | collector-base voltage | open emitter | - | -45 | V |
| V _{CEO} | collector-emitter voltage | open base | - | -45 | V |
| V _{EBO} | emitter-base voltage | open collector | _ | -6 | V |
| I _C | collector current (DC) | | _ | -100 | mA |
| I _{CM} | peak collector current | | _ | -200 | mA |
| P _{tot} | total power dissipation | T _{amb} ≤ 25 °C; note 1 | _ | 200 | mW |
| T _{stg} | storage temperature | | -65 | +150 | °C |
| Tj | junction temperature | | - | 150 | °C |
| T _{amb} | operating ambient temperature | | -65 | +150 | °C |

Note

1. For mounting conditions, see "Thermal considerations and footprint design for SOT323 in the General Part of Data Handbook SC18".

PINNING

| PIN | DESCRIPTION | |
|-----|-------------|--|
| 1 | base | |
| 2 | emitter | |
| 3 | collector | |

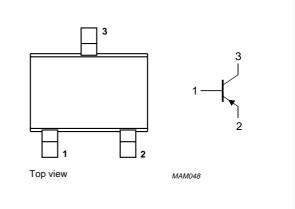


Fig.1 Simplified outline SC-70 (SOT323) and symbol.

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THERMAL CHARACTERISTICS

| SYMBOL | PARAMETER | CONDITIONS | VALUE | UNIT |
|---------------------|---|------------|-------|------|
| R _{th j-a} | thermal resistance from junction to ambient | note 1 | 625 | K/W |

Note

1. For mounting conditions, see "Thermal considerations and footprint design for SOT323 in the General Part of Data Handbook SC18".

CHARACTERISTICS

 T_{amb} = 25 °C unless otherwise specified.

| SYMBOL | PARAMETER | CONDITIONS | MIN. | MAX. | UNIT |
|--------------------|--------------------------------------|--|------|------|------|
| I _{CBO} | collector-base cut-off current | $I_E = 0; V_{CB} = -45 V$ | _ | -10 | nA |
| | | $I_E = 0; V_{CB} = -45 \text{ V}; T_j = 150 \text{ °C}$ | - | -5 | μA |
| I _{EBO} | emitter-base cut-off current | $I_{C} = 0; V_{EB} = -5 V$ | _ | -10 | nA |
| h _{FE} | DC current gain | $I_{C} = -2 \text{ mA}; V_{CE} = -10 \text{ V}$ | | | |
| | 2PB709AQW | | 160 | 260 | |
| | 2PB709ARW | | 210 | 340 | |
| | 2PB709ASW | | 290 | 460 | |
| V _{CEsat} | collector-emitter saturation voltage | $I_{C} = -100 \text{ mA}; I_{B} = -10 \text{ mA};$ note 1 | - | -500 | mV |
| C _c | collector capacitance | $I_E = i_e = 0; V_{CB} = -10 V;$ f = 1 MHz | - | 5 | pF |
| f _T | transition frequency | $I_{C} = -1 \text{ mA}; V_{CE} = -10 \text{ V};$ | | | |
| | 2PB709AQW | f = 100 MHz | 60 | _ | MHz |
| | 2PB709ARW | | 70 | _ | MHz |
| | 2PB709ASW | | 80 | _ | MHz |

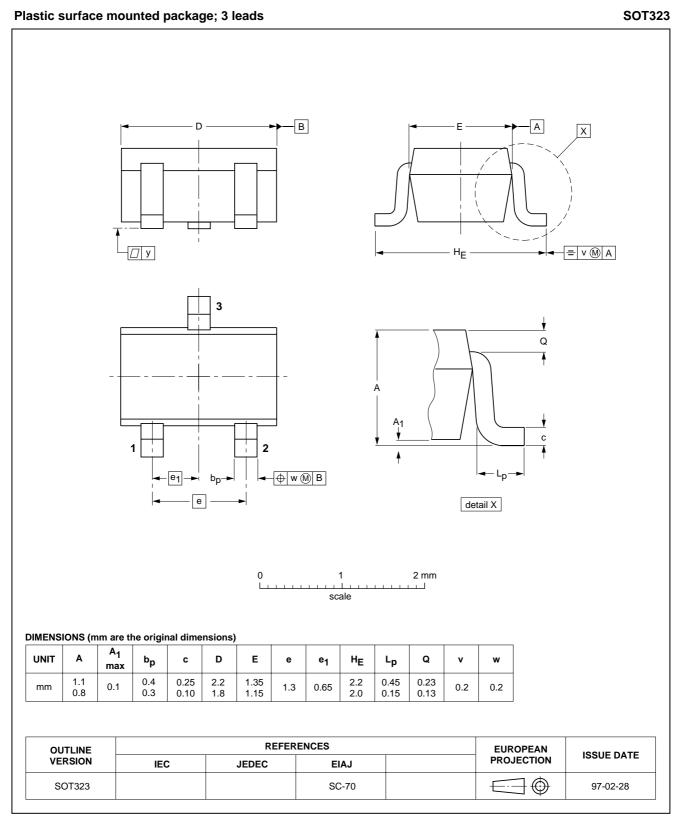
Note

1. Pulse test: $t_p \leq 300 \ \mu s; \ \delta \leq 0.02.$

2PB709AW

PNP general purpose transistor

PACKAGE OUTLINE



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DATA SHEET STATUS

| DATA SHEET STATUS ⁽¹⁾ | PRODUCT STATUS ⁽²⁾ | DEFINITIONS |
|----------------------------------|----------------------------------|--|
| Objective data | Development | This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice. |
| Preliminary data | Qualification | This data sheet contains data from the preliminary specification. Supplementary data will be published at a later date. Philips Semiconductors reserves the right to change the specification without notice, in order to improve the design and supply the best possible product. |
| Product data | Production | This data sheet contains data from the product specification. Philips Semiconductors reserves the right to make changes at any time in order to improve the design, manufacturing and supply. Changes will be communicated according to the Customer Product/Process Change Notification (CPCN) procedure SNW-SQ-650A. |

Notes

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- 2. The product status of the device(s) described in this data sheet may have changed since this data sheet was published. The latest information is available on the Internet at URL http://www.semiconductors.philips.com.

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