

# RQ1C075UN

# Nch 20V 7.5A Small Signal MOSFET

V <sub>DSS</sub>	20V
R <sub>DS(on)</sub> (Max.)	16mΩ
I <sub>D</sub>	±7.5A
P <sub>D</sub>	1.5W

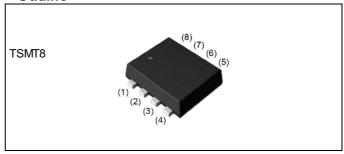
# ● Features

- 1) Low on resistance
- 2) Built-in G-S protection diode
- 3) Small surface mount package(TSMT8)
- 4) Pb-free lead plating; RoHS compliant

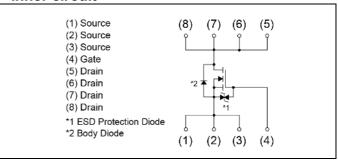
# Application

Switching

## Outline



## ●Inner circuit



Packaging specifications

	Packing	Embossed Tape
	Reel size (mm)	180
Туре	Tape width (mm)	8
	Basic ordering unit (pcs)	3000
	Taping code	TR
	Marking	XH

## ● **Absolute maximum ratings** (T<sub>a</sub> = 25°C ,unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain - Source voltage	V <sub>DSS</sub>	20	V
Continuous drain current	I <sub>D</sub>	±7.5	Α
Pulsed drain current	I <sub>DP</sub> *1	±30	Α
Gate - Source voltage	V <sub>GSS</sub>	±10	V
Dower discinction	P <sub>D</sub> *2	1.5	W
Power dissipation	P <sub>D</sub> *3	1.1	W
Junction temperature	T <sub>j</sub>	150	°C
Operating junction and storage temperature range	T <sub>stg</sub>	-55 to +150	°C

## ●Thermal resistance

Doromotor	Cumb ol	Values			Linit
Parameter	Symbol	Min.	Тур.	Max.	Unit
Thermal registance innetion, ambient	R <sub>thJA</sub> *2	-	-	83.3	°C/W
Thermal resistance, junction - ambient	R <sub>thJA</sub> *3	-	1	113	°C/W

# ● Electrical characteristics (T<sub>a</sub> = 25°C)

Doromotor	Cumb of	Conditions	Values			Unit
Parameter	Symbol Conditions		Min.	Тур.	Max.	Unit
Drain - Source breakdown voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> = 1mA	20	-	-	V
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_{j}}$	I <sub>D</sub> = 1mA referenced to 25°C	-	29	-	mV/°C
Zero gate voltage drain current	I <sub>DSS</sub>	V <sub>DS</sub> = 20V, V <sub>GS</sub> = 0V	-	-	1	μA
Gate - Source leakage current	I <sub>GSS</sub>	$V_{GS} = \pm 10V$ , $V_{DS} = 0V$	-	-	±10	μA
Gate threshold voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = 10V, I <sub>D</sub> = 1mA	0.3	-	1.0	V
Gate threshold voltage temperature coefficient	$\frac{\Delta V_{GS(th)}}{\Delta T_j}$	I <sub>D</sub> = 1mA referenced to 25°C	-	-1.6	-	mV/°C
		V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 7.5A	-	11	16	
Static drain - source	D *4	V <sub>GS</sub> = 2.5V, I <sub>D</sub> = 7.5A	-	14	20	
on - state resistance	R <sub>DS(on)</sub> *4	V <sub>GS</sub> = 1.8V, I <sub>D</sub> = 3.7A	-	17	24	mΩ
		V <sub>GS</sub> = 1.5V, I <sub>D</sub> = 1.5A	-	20	40	
Gate resistance	$R_G$	R <sub>G</sub> f = 1MHz, open drain		5	1	Ω
Forward Transfer Admittance	Y <sub>fs</sub>  *4	V <sub>DS</sub> = 10V, I <sub>D</sub> = 7.5A	7	-	-	S

<sup>\*1</sup> Pw≦10µs , Duty cycle≦1%

<sup>\*2</sup> Mounted on a ceramic board (30×30×0.8mm)

<sup>\*3</sup> Mounted on a FR4 (25×25×0.8mm)

<sup>\*4</sup> Pulsed

# ●Electrical characteristics (T<sub>a</sub> = 25°C)

Darameter	Cumahad	Conditions	Values			Unit
Parameter	Symbol Conditions —		Min.	Тур.	Max.	Unit
Input capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0V	-	1400	-	
Output capacitance C <sub>oss</sub>		V <sub>DS</sub> = 10V	-	310	-	pF
Reverse transfer capacitance	C <sub>rss</sub>	f = 1MHz	-	210	-	
Turn - on delay time	t <sub>d(on)</sub> *4	$V_{DD} \simeq 10V, V_{GS} = 4.5V$	-	15	-	
Rise time	t <sub>r</sub> *4	I <sub>D</sub> = 3.7A	-	50	-	no
Turn - off delay time	t <sub>d(off)</sub> *4	$R_L \simeq 2.7\Omega$	-	100	-	ns
Fall time	t <sub>f</sub> *4	$R_G = 10\Omega$	-	85	-	

# • Gate charge characteristics $(T_a = 25^{\circ}C)$

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Parameter	Symbol	Conditions	Values			Unit
raianietei	Symbol	Conditions	Min.	Тур.	Max.	Offic
Total gate charge	Q <sub>g</sub> *4	V <sub>DD</sub> ≃ 10V,	-	18	-	
Gate - Source charge	Q <sub>gs</sub> *4	I <sub>D</sub> = 7.5A, V <sub>GS</sub> = 4.5V	-	3.2	-	nC
Gate - Drain charge	Q <sub>gd</sub> *4	$V_{GS} = 4.5V$	-	2.9	-	

# ●Body diode electrical characteristics (Source-Drain) (T<sub>a</sub> = 25°C)

Darameter	Symbol	Conditions		Values		
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Continuous forward current	I <sub>S</sub>	T = 25°C	-	-	1	Α
Pulse forward current	I <sub>SP</sub> *1	T <sub>a</sub> = 25℃	-	-	30	Α
Forward voltage	V <sub>SD</sub> *4	V <sub>GS</sub> = 0V, I <sub>S</sub> = 7.5A	-	-	1.2	V

Fig.1 Power Dissipation Derating Curve

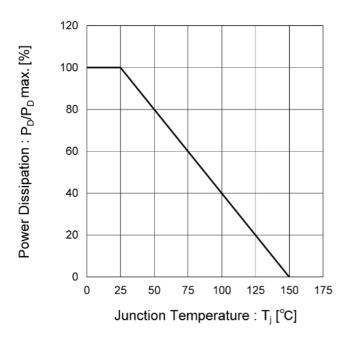
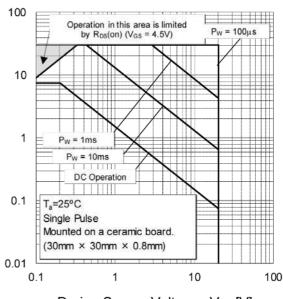


Fig.2 Maximum Safe Operating Area



Drain Current : Ip [A]

Drain - Source Voltage : V<sub>DS</sub> [V]

Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

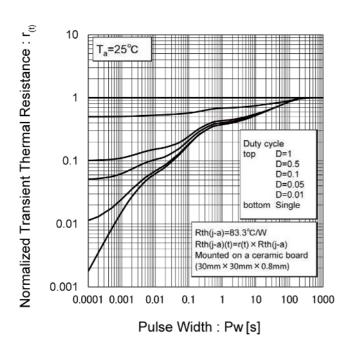


Fig.4 Single Pulse Maximum Power dissipation

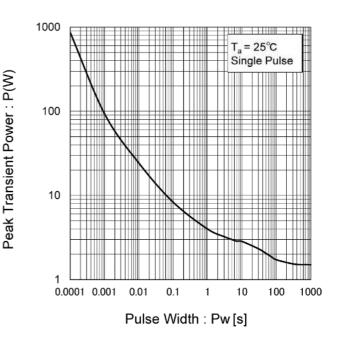


Fig.5 Typical Output Characteristics(I)

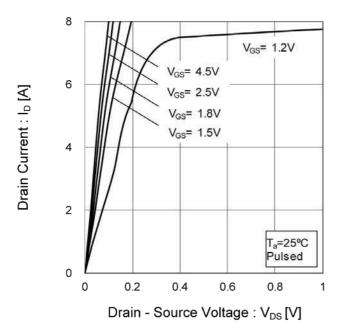
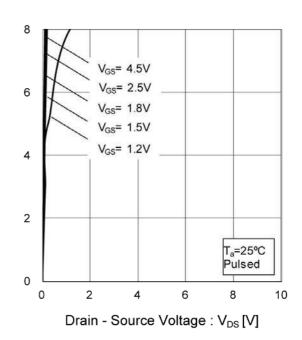


Fig.6 Typical Output Characteristics(II)



Drain Current : I<sub>D</sub> [A]

Fig.7 Breakdown Voltage vs.
Junction Temperature

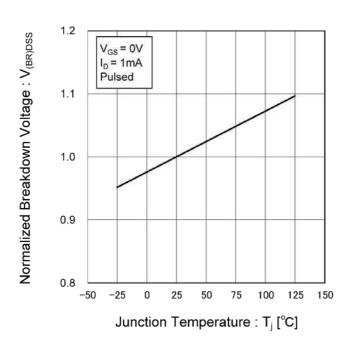
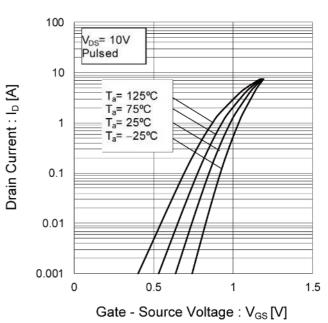


Fig.8 Typical Transfer Characteristics



Gate Threshold Voltage: V<sub>GS(th)</sub> [V]

Fig.9 Gate Threshold Voltage vs.
Junction Temperature

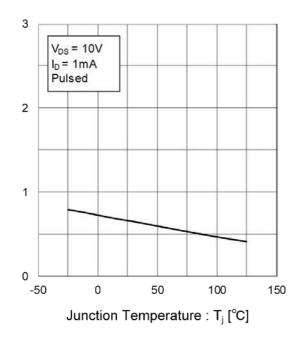


Fig.10 Forward Transfer Admittance vs.
Drain Current

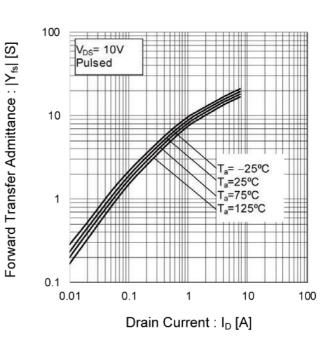


Fig.11 Drain Current Derating Curve

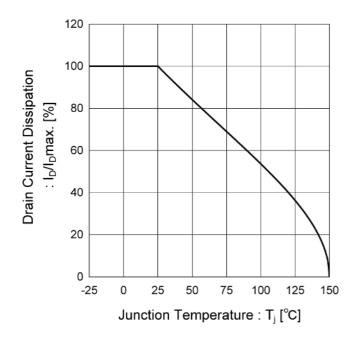


Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage

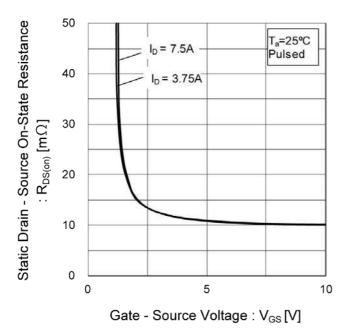


Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature

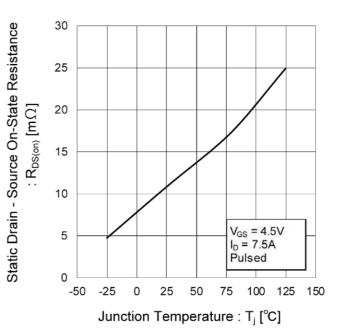


Fig.14 Static Drain - Source On - State Resistance vs. Drain Current (I)

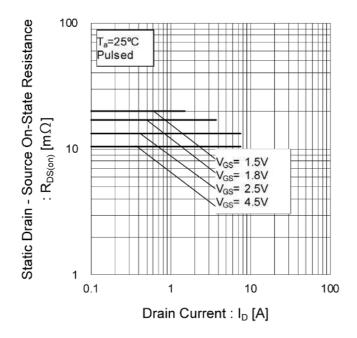


Fig.15 Static Drain - Source On - State Resistance vs. Drain Current (II)

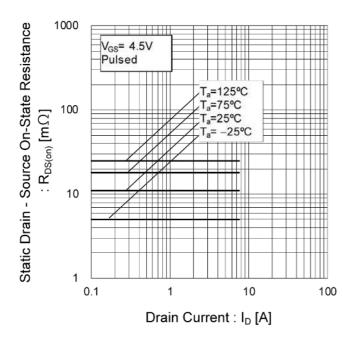


Fig.16 Static Drain - Source On - State
Resistance vs. Drain Current (III)

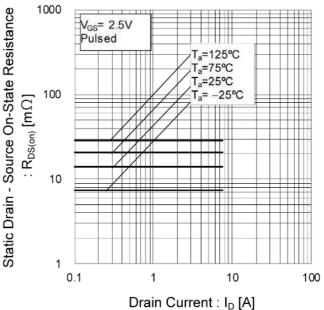


Fig.17 Static Drain - Source On - State Resistance vs. Drain Current (IV)

Static Drain - Source On-State Resistance On-S

Fig.18 Static Drain - Source On - State Resistance vs. Drain Current (V)

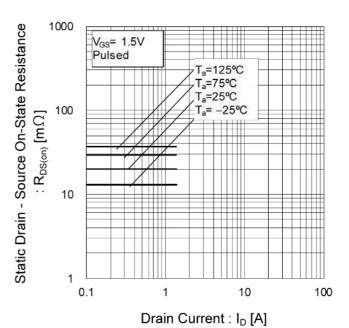


Fig.19 Typical Capacitance vs.

Drain - Source Voltage

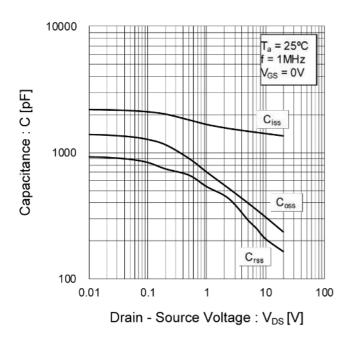


Fig.20 Switching Characteristics

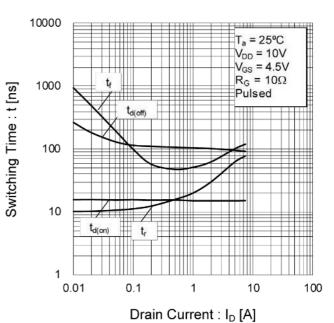


Fig.21 Dynamic Input Characteristics

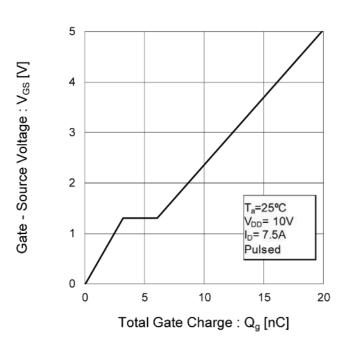
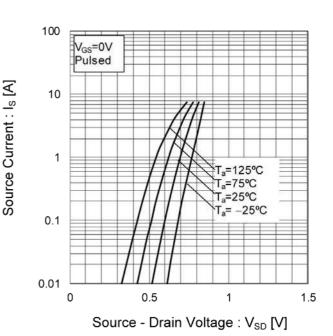


Fig.22 Source Current vs.

Source Drain Voltage



## Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

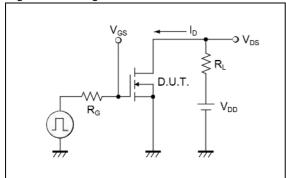


Fig.2-1 Gate Charge Measurement Circuit

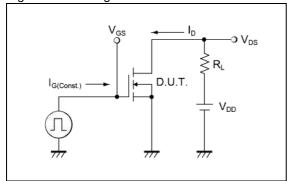


Fig.1-2 Switching Waveforms

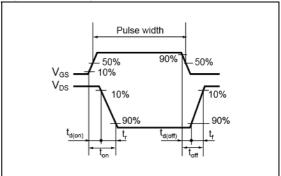
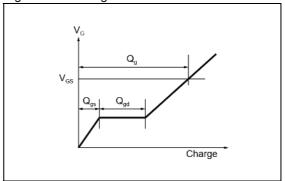


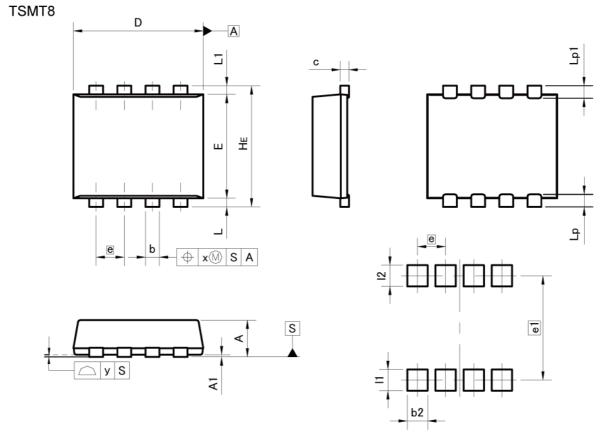
Fig.2-2 Gate Charge Waveform



## Notice

This product might cause chip aging and breakdown under the large electrified environment. Please consider to design ESD protection circuit.

## Dimensions



Pattern of terminal position areas [Not a pattern of soldering pads]

DIM	MILIM	ETERS	INC	HES
DIM [	MIN	MAX	MIN	MAX
Α	0.75	0.85	0.030	0.033
A1	0.00	0.05	0.000	0.002
b	0.27	0.37	0.011	0.015
С	0.12	0.22	0.005	0.009
D	2.90	3.10	0.114	0.122
E	2.30	2.50	0.091	0.098
е	0.	65	0.0	26
HE	2.70	2.90	0.106	0.114
L	0.10	0.30	0.004	0.012
L1	0.10	0.30	0.004	0.012
Lp	0.19	0.39	0.007	0.015
Lp1	0.19	0.39	0.007	0.015
x	9 <del>78</del>	0.10	, <del>, , ,</del>	0.004
у	10 <del>10</del>	0.10	1000	0.004

DIM	MILIMETERS		INC	HES
DIM L	MIN	MAX	MIN	MAX
b2	27-44	0.47	186	0.019
e1	2.	41	0.0	95
11	59 <del>-11</del>	0.49	) <del> </del>	0.019
12	-	0.49	-	0.019

Dimension in mm/inches



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JÁPAN	USA	EU	CHINA
CLASSⅢ	CLASSⅢ	CLASS II b	CLASSIII
CLASSIV	CLASSIII	CLASSⅢ	CLASSIII

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  - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
  - [f] Sealing or coating our Products with resin or other coating materials
  - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
  - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
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- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

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#### **Precaution for Electrostatic**

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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  - [c] the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
  may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
  exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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