

DATA SHEET ELECTROSTATIC DISCHARGE PROTECTION DEVICES INDUSTRIAL / CONSUMER UAT52A05L02

RoHS compliant & Halogen free



Electrostatic Discharged Protection Devices (ESD) Data Sheet

Description

This is ultra low capacitance TVS arrays designed to protect high speed data interfaces. It has been specifically designed to protect sensitive components which is connected to high-speed data and transmission lines from overvoltage caused by electrostatic (ESD), cable discharge events (CDE) and electrical fast transients (EFT).

Features

- IEC61000-4-2 ESD 15KV Air, 8KV contact compliance
- SOT-523 surface mount package
- Protects two high speed data lines
- Working voltage: 5V
- Ultra low capacitance and clamping voltages
- Low leakage current
- Solid-state silicon avalanche technology
- Lead Free/RoHS compliant
- Solder reflow temperature: Pure Tin-Sn, 260~270°C
- Flammability rating UL 94V-0
- Meets MSL level 1, per J-STD-020
- Marking: B L5

Applications

- HDMI interface protection
- Mobile display digital interface
- RF/Antenna circuits
- USB 2.0 & Firewire ports

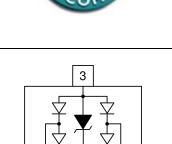
Maximum Ratings

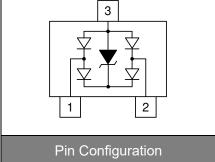
Jul. 04, 2023 V.2

Rating	Symbol	Value	Unit	
ESD voltage (Contact discharge)	M	±8	- kV	
ESD voltage (Air discharge)	V _{ESD}	±15		
Storage & operating temperature range	T _{STG} ,TJ	-55~+150	°C	









 GaAs photodetector protection HBT power Amp protection

Infiniband transceiver protection



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Electrical Characteristics (T_J=25 $^{\circ}$ C)

Parameter	Symbol	Condition	Min.	Тур.	Max.	Unit
Reverse stand-off voltage	V _{RWM}				5	V
Reverse breakdown voltage	V _{BR}	I _{BR} =1mA	6			V
Reverse leakage current	I _R	V _R =5V Each I/O pin			0.5	μA
Clamping voltage (tp=8/20µs)	Vc	I _{PP} =1A			9.8	V
Clamping voltage (tp=8/20µs)	Vc	I _{PP} =3A			15	V
Peak pulse current (tp=8/20µs)	IPP				3	А
Off state junction capacitance	CJ	0Vdc,f=1MHz Between I/O pins and GND		0.8		pF
Off state junction capacitance	CJ	0Vdc,f=1MHz Between I/O pins		0.4		pF

Typical Characteristics Curves

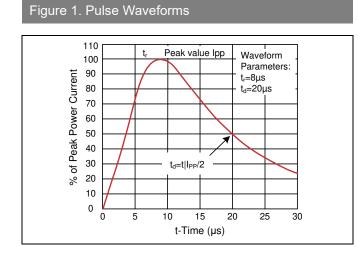


Figure 3. Clamping Voltage vs. Peak Pulse Current

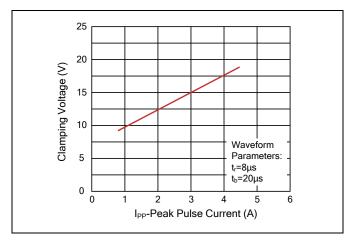
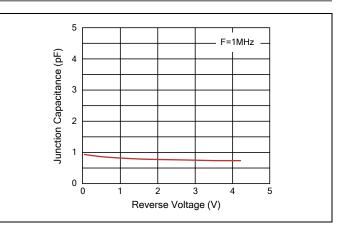
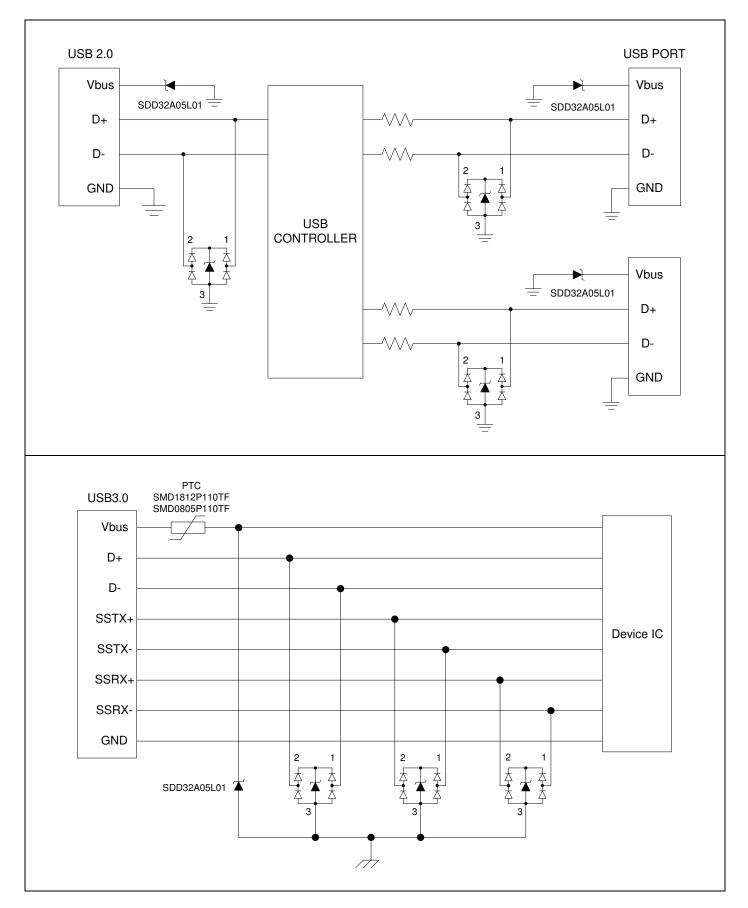


Figure 2. Capacitance vs. Reverse Voltage

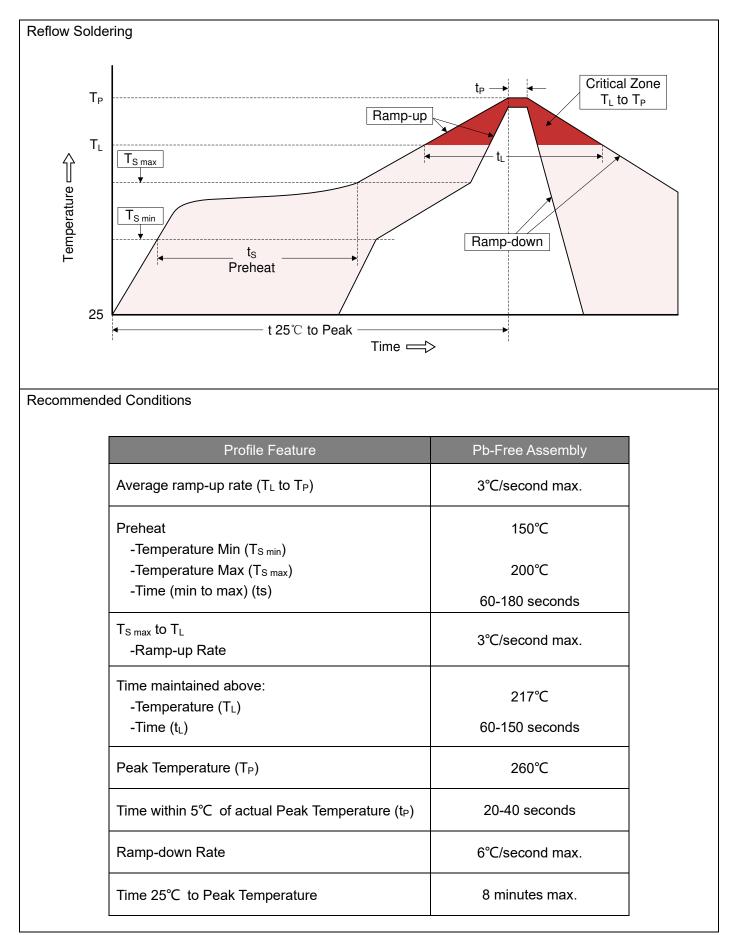


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Applications Information

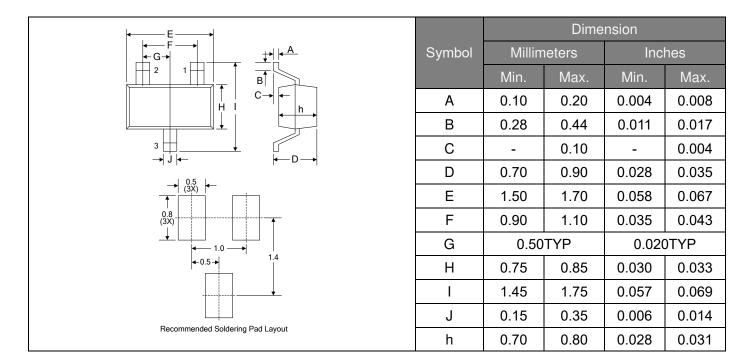


Recommended Soldering Conditions

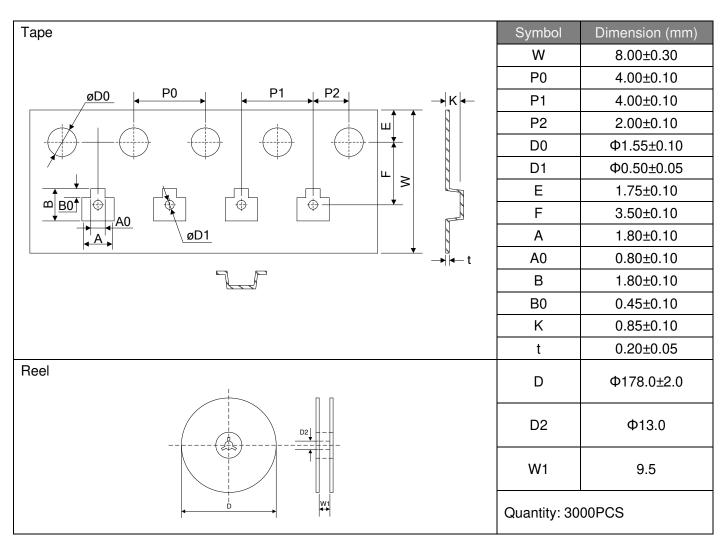


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Dimensions (SOT-523)



Packaging



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