### **Features**

- Low R<sub>DS(on)</sub>
- Ultra-low Q<sub>G</sub> For High Efficiency
- Logic Level
- Light Weight 0.068 grams
- New Compact Hermetic Package
- Source Sense Pin
- Total Dose
  - Rated to 300 krad
- Single Event
  - SEE immunity for LET of 83.7 MeV/mg/cm<sup>2</sup> with V<sub>DS</sub> up to 100% of rated Breakdown
- Low Dose Rate at 100 mRad/sec
  - Maintains Pre-Rad specification
- Neutron
  - Maintains Pre-Rad specification for up to 1 x 10<sup>15</sup> Neutrons/cm<sup>2</sup>

### **Applications**

- Satellite and Avionics
- Deep Space Probes
- High Speed Rad Hard DC-DC Conversion
- Rad Hard Motor Controllers





## FBG10N05A

Rad Hard eGaN<sup>®</sup> 100 V, 5 A, 45 mΩ Surface Mount (FSMD-A)

### **Description**

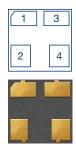
EPC Space series of eGaN® power switching HEMTs have been specifically designed for critical applications in the high reliability or commercial satellite space environments. These devices have exceptionally high electron mobility and a low temperature coefficient resulting in very low  $R_{\text{DS(on)}}$  values. The lateral structure of the die provides for very low gate charge ( $Q_{\text{G}}$ ) and extremely fast switching times. These features enable faster power supply switching frequencies resulting in higher power densities, higher efficiencies and more compact packaging.

### **Thermal Characteristics**

Symbol	Parameter-Conditions	Value	Units
$R_{\theta JA}$	Thermal Resistance Junction to Ambient (Note 3)	62	°C/W
$R_{\theta JC}$	Thermal Resistance Junction to Case	10.57	G/VV

### I/O Pin Assignment (Bottom View)

Pin	Symbol	Description
1	G	Gate
2	D	Drain
3	SS	Source Sense
4	S	Source



### **Absolute Maximum Rating** ( $T_C = 25^{\circ}C$ unless otherwise noted)

Symbol	Parameter-Conditions	Value	Units
V <sub>DS</sub>	Drain to Source Voltage (Note 1)	100	V
I <sub>D</sub>	Continuous Drain Current ID @ V <sub>GS</sub> = 5 V 5		۸
I <sub>DM</sub>	Single-Pulse Drain Current t <sub>pulse</sub> ≤ 80 μs	20	А
V <sub>GS</sub>	Gate to Source Voltage (Note 2)	+6 / -4	V
$T_J, T_{STG}$	Operating and Storage Junction Temperature Range -55 to +150		°C
T <sub>sol</sub>	Package Mounting Surface Temperature	260	°C
ESD	ESD Class	ΔΑ	



# Electrical Characteristics ( $T_C = 25$ °C unless otherwise noted. Typical (TYP) values are for reference only.)

Parameter	Symbol	Test Cond	ditions	MIN	TYP	MAX	Units
Minimum Drain to Source Voltage	V <sub>DSMIN</sub>	$V_G = 0 V$		100	-	_	V
Drain to Source Leakage		V <sub>DS</sub> = 100 V	T <sub>C</sub> = 25°C	-	10	60	
	I <sub>DSS</sub>	$V_{GS} = 0 V$	T <sub>C</sub> = 125°C	-	100	150	
Gate to Source Forward Leakage	I <sub>GSSF</sub>	V <sub>GS</sub> = 5 V	T <sub>C</sub> = 25°C	-	250	500	μA
Gate to Source Reverse Leakage	I <sub>GSSR</sub>	V <sub>GS</sub> = -4 V	T <sub>C</sub> = 25°C	-60	-20		
Gate to Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = 1.2 \text{ mA}$	T <sub>C</sub> = 25°C	0.8	1.2	2.5	V
Gate to Source Threshold Voltage Temperature Coefficient	$\Delta V_{GS(th)}/\Delta T$	$V_{DS} = V_{GS}, I_{D} = 1.2 \text{ mA}$	-55°C < T <sub>A</sub> < 150°C	-	3.8	-	mV/°C
Drain to Source Resistance (Note 4)	R <sub>DS(on)</sub>	$I_D = 5 A, V_{GS} = 5 V$	T <sub>C</sub> = 25°C	-	36	45	mΩ
Source to Drain Forward Voltage (Note 5)	V <sub>SD</sub>	$I_S = 0.5 \text{ A}, V_G = 0 \text{ V}$	T <sub>C</sub> = 25°C		2.5		V

## **Dynamic Characteristics** ( $T_C = 25^{\circ}C$ unless otherwise noted. Typical (TYP) values are for reference only.)

Parameter	Symbol	Test Conditions	MIN	TYP	MAX	Units
Input Capacitance	C <sub>ISS</sub>			207	233	
Output Capacitance	C <sub>OSS</sub>	$f = 1 \text{ MHz}, V_{DS} = 50 \text{ V}, V_{GS} = 0 \text{ V} \text{ (Note 6)}$		133	170	pF
Reverse transfer Capacitance	C <sub>RSS</sub>			19	23	
Gate Resistance	R <sub>G</sub>	$f = 1 \text{ MHz}, V_{DS} = V_{GS} = 0 \text{ V}$		0.4		Ω
Total Gate Charge (Note 7)	$Q_{G}$	$I_D = 5 \text{ A}, V_{GS} = 5 \text{ V}, V_{DS} = 50 \text{ V}$		1.7	2.2	
Gate to Drain Charge (Note 7)	$Q_{GD}$	$I_D = 5 \text{ A}, V_{GS} = 5 \text{ V}, V_{DS} = 50 \text{ V}$		0.2	0.6	
Gate to Source Charge (Note 7)	$Q_{GS}$	$I_D = 5 \text{ A}, V_{GS} = 5 \text{ V}, V_{DS} = 50 \text{ V}$		0.5	1.0	nC
Output Charge (Note 8)	Q <sub>OSS</sub>	V <sub>GS</sub> = 0 V, V <sub>DS</sub> = 50 V		9.1	-	
Source to Drain Recovery Charge	Q <sub>RR</sub>	I <sub>D</sub> = 5 A, V <sub>DS</sub> = 50 V		<1		



### **Radiation Characteristics**

EPC Space eGaN® HEMTs are tested according to MIL-STD-750 Method 1019 for total ionizing dose validation. Every manufacturing lot is tested for total ionizing dose of Gamma radiation with an in-situ bias for (i)  $V_{GS} = 5 \text{ V}$ , (ii)  $V_{DS} = V_{GS} = 0 \text{ V}$  and (iii)  $V_{DS} = 80\% \text{ B}_{VDSS}$ .

Electrical Characteristics up to 300 krads ( $T_C = 25^{\circ}$ C unless otherwise noted. Typical (TYP) values are for reference only.)

Parameter	Symbol	Test Conditions	MIN	TYP	MAX	Units
Maximum Drain to Source Voltage	V <sub>DSMAX</sub>	$V_{GS} = 0 V$	-	-	100	V
Gate to Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}$ , $I_D = 1.2 \text{ mA}$	0.8	1.2	2.5	V
Drain to Source Leakage	I <sub>DSS</sub>	$V_{DS} = 100 \text{ V}, V_{GS} = 0 \text{ V}$	-	10	250	
Gate to Source Forward Leakage	I <sub>GSS</sub>	V <sub>GS</sub> = 5 V	-	250	500	μA
Gate to Source Reverse Leakage	I <sub>GSS</sub>	V <sub>GS</sub> = -4 V	-	-20	-60	
Drain to Source Resistance (Note 4)	R <sub>DS(on)</sub>	$I_D = 5 A, V_{GS} = 5 V$	-	-	45	mΩ

## **Typical Single Event Effect Safe Operating Area**

Note: All Single Event Effect testing is performed on the K-500 Cyclotron at Texas A&M University

Test		Envir		V <sub>DS</sub> Vol	tage ( V)	
	lon	LET MeV/mg/cm <sup>2</sup>	Range µm	Energy MeV	V <sub>GS</sub> = 0 V	$V_{GS} = -4V$
See SOA	Xe	50	131	1653	100	100
	Au	83.7	130	2482	100	100

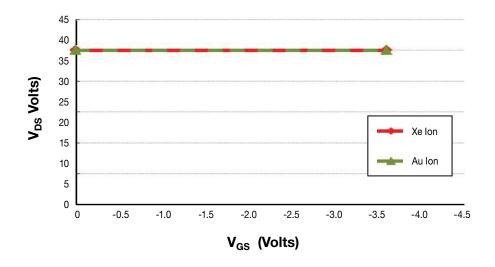


Figure 1. Typical Single Event Effect Safe Operating Area

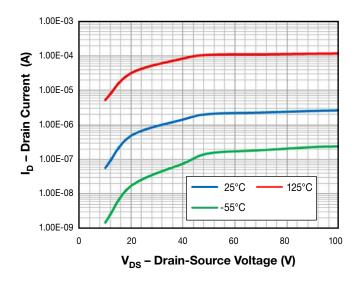


Figure 2. Typical Drain-Source Leakage Current vs. Ambient Temperature

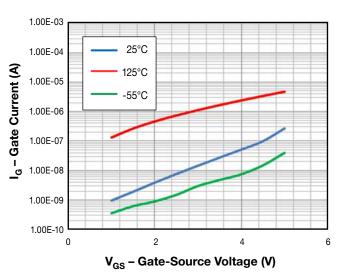


Figure 3. Gate-Source Leakage Current vs. Ambient Temperature

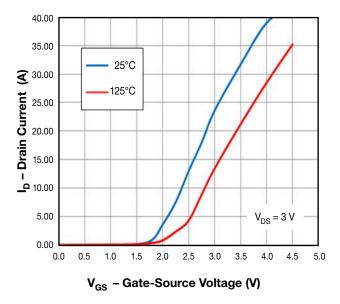


Figure 4. Typical Gate-Drain Transfer Characteristic

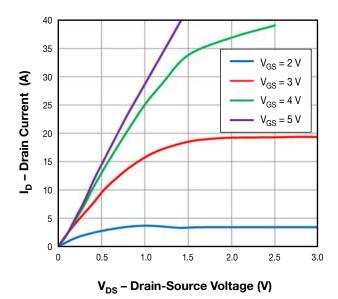


Figure 5. Typical Output Characteristics

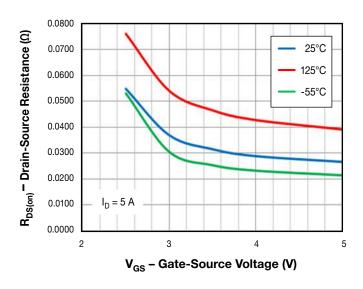


Figure 6. Typical Drain-Source ON Resistance vs. Gate-Source Voltage vs. Ambient Temperature

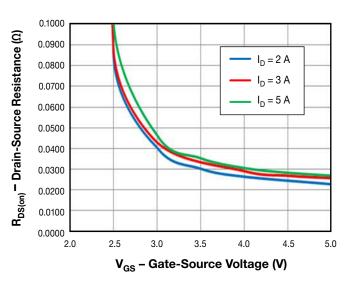


Figure 7. Typical Drain-Source ON Resistance vs. Gate-Source Voltage vs. Drain Current

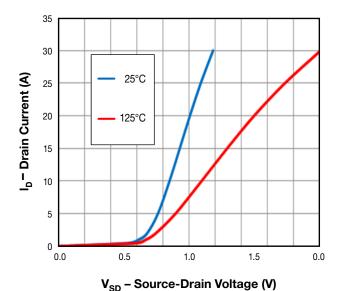


Figure 8. Typical Source-Drain Voltage vs. Temperature

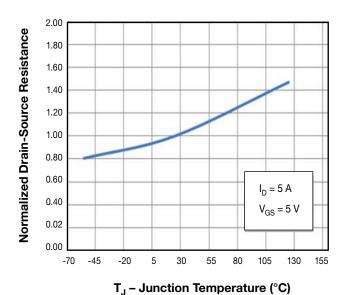
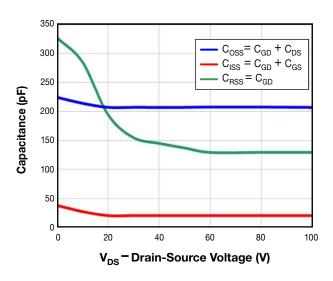


Figure 9. Normalized Drain-Source ON Resistance vs. Ambient Temperature



5.0 4.5 V<sub>GS</sub> - Gate-Source Voltage (V) 4.0 3.5 3.0 2.5 2.0 1.5  $I_D = 5 A$ 1.0 0.5 0 0 1.0 1.5 2.0 Q<sub>G</sub> - Drain Charge (nC)

Figure 10. Typical Inter-Electrode Capacitance vs.

Drain-Source Voltage

Figure 11. Typical Gate Charge vs. Gate to Source Voltage

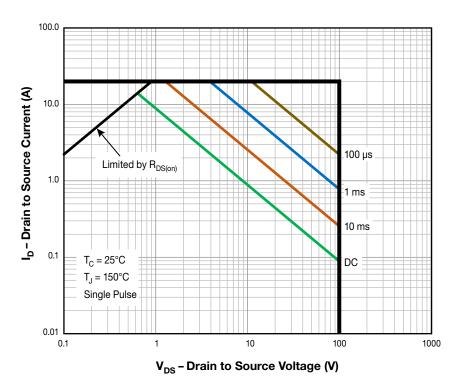


Figure 12. Safe Operating Area

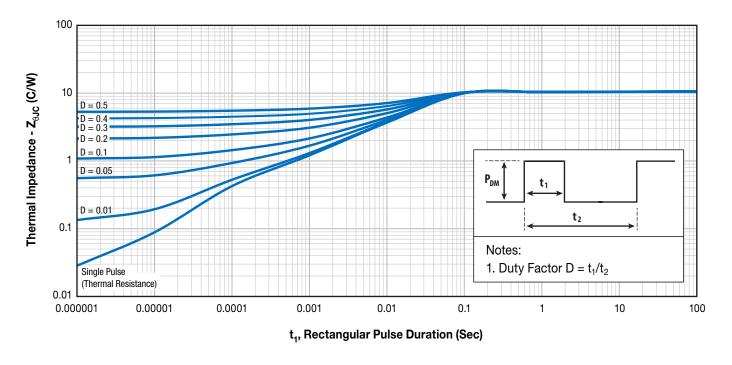


Figure 13. Transient Thermal Impedance, Junction to Case

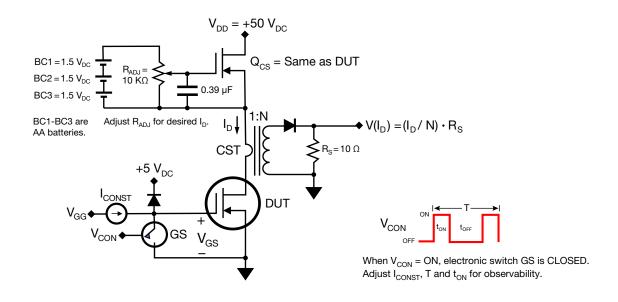


Figure 14. Charge Test Circuit

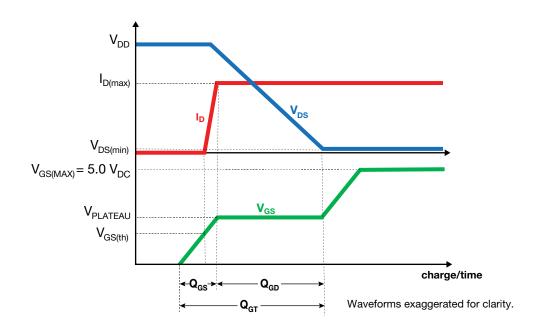
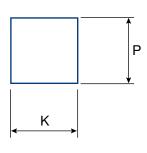


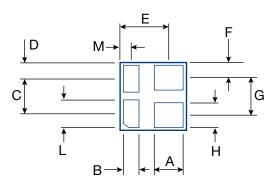
Figure 15. Typical Gate Charge Test Waveform



## Package Outline and Dimensions



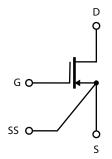




Symbol	Incl	nes	Millim	Note	
Syllibol	MIN	MAX	MIN	MAX	INOIG
Α	0.052	0.062	1.320	1.574	
В	0.027	0.037	0.685	0.939	
С	0.063	0.073	1.600	1.854	
D	0.026	0.036	0.660	0.914	
E	0.092	0.102	2.336	2.590	
F	0.024	0.034	0.609	0.863	
G	0.068	0.078	1.727	1.981	
Н	0.042	0.052	1.066	1.320	
J	0.080	0.090	2.032	2.286	Ref.only
K	0.125	0.135	3.175	3.429	
L	0.048	0.058	1.219	1.473	
М	0.016	0.026	0.406	0.664	
Р	0.125	0.135	3.175	3.429	

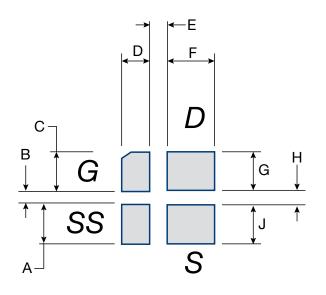
Standard Terminal Pad finish is a solder alloy of 63%Pb 37%Sn

# **Package Connections**



**NOTE:** SS pin is connected directly to source of internal die.

## **FSMD-A Footprint for Printed Circuit Board Design**



Symbol	Incl	nes	Millim	Note	
Cymson	MIN	MAX	MIN	MAX	11010
Α	0.051	0.061	1.295	1.549	
В	0.010	0.020	0.254	0.508	
С	0.051	0.061	1.295	1.549	
D	0.033	0.043	0.838	1.092	
E	0.020	0.030	0.508	0.762	
F	0.058	0.068	1.473	1.727	
G	0.048	0.058	1.219	1.473	
Н	0.015	0.025	0.381	0.635	
J	0.048	0.058	1.219	1.473	

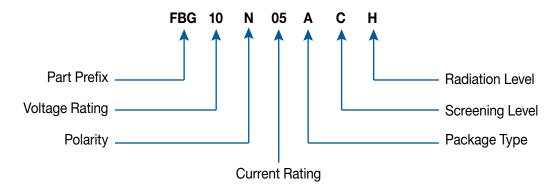


### **Notes**

- Note 1. NEVER exceed the absolute maximum V<sub>DS</sub> of the device otherwise permanent damage/destruction may result.
- Note 2. NEVER exceed the absolute maximum  $V_{GS}$  of the device otherwise permanent damage/destruction may result. We recommend use at no greater than +5 V as the HEMT is fully conducting at this point.
- Note 3. R<sub>0JA</sub> measured with FSMD-A package mounted to double-sided PCB, 0.063" thickness with 1.0 square inches of copper area on the top (mounting side) and a flood etch (3 square inches) on the bottom side.
- Note 4. Measured using four wire (Kelvin) sensing and pulse measurement techniques. Measurement pulse width is 80 μs and duty cycle is 1%, maximum.
- Note 5. Operation of the device in the third quadrant region is not recommended.
- Note 6.  $C_{ISS} = C_{GS} + C_{GD}$  with  $C_{DS}$  shorted.  $C_{OSS} = C_{DS} + C_{GD}$ .  $C_{RSS} = C_{GD}$ .
- Note 7. The gate charge parameters are measured using the circuit shown in Figure 11. Qs and associated components BT1, P1 and C1 form a high speed current source that serves as the test load for the DUT. A constant gate current ( $I_{const}$ ) of 1.5-3 mA is provided to the Gate of the DUT during the time that the ground switch ( $I_{const}$ ) is OFF ( $I_{const}$ ). The DUT is switched ON and OFF using ground-sensed switch GS. The gate current is adjusted to yield the desired charge per unit time ( $I_{const}$  · time per division) on the measuring oscilloscope. The GS pulse drive ON time ( $I_{const}$ ) is adjusted for the desired observability of the gate-source voltage ( $I_{const}$ ) waveform. The maximum duty cycle of the ground switch ( $I_{const}$ ) should be set to 1% maximum. Please note that all gate-related signals are referenced to the "Source Sense" pin on the package. At all times during the measurement, the maximum gate-source voltage is clamped to 5  $I_{DC}$ .
- Note 8. Guaranteed by design/device construction. Not tested.



## **EPC Space Part Number Information**



# **Ordering Information Availability**

Screening Options	Rad Assurance Options
1 character	1 character
C = Developmental Unit S = Space Level <sup>1</sup>	H = 1000 krad, LET = 84

Part Number	Screening Level	Shipping
FBG10N05A*C	Developmental Units	Waffla trava
FBG10N05A*S	Space Level	Waffle trays

<sup>&</sup>lt;sup>1</sup> Screening and qualification consistent to an equivalent MIL-PRF-19500 specification.

FBG10N05AC devices are intended for engineering development purposes only and are NOT intended to be used as flight units.

EPC Space Rad Hard HEMT are not sensitive to Total Ionizing Dose as such the H level covers the R,F,G radiation levels.



# Screening Flow Equivalent to a MIL-PRF-19500 General Specification

	EPC SPACE Qual Flow Equivalen	t to a MIL-PRF-19500 Spec	ification				
Operation	Test	Test Methods Per Mil STD 750	Sample Size	Space Level	СОТ		
Due Assembly	Probe Testing	EPC SPACE Internal	100%	✓	✓		
Pre-Assembly	Visual inspection	EPC SPACE Internal	100%		✓		
Doot Accombly	Die Shear	2,017	5	✓	✓		
Post-Assembly	X-Ray	2076	5	✓	✓		
	Serilialization		100%	✓			
	Electricals	3411,3413,3421,3404	100%	✓	✓		
	Temp Cycling	1051	100%	✓			
	Constant Acceleration	2006	100%	✓			
	PIND	2052	100%	✓			
	Initial Electricals (Read and Record)	3411,3413,3421,3404	100%	✓			
	HTGB	1042 Condition B	100%	✓			
	Interim Electricals (Read and Record)	3411,3413,3421,3404	100%	✓			
	HTRB	1042 Condition A 240 Hours	100%	✓			
Screening	Final Electricals (Read and Record)	3411,3413,3421,3404	100%	✓			
	Final Electricals (High and Low Temperatures)	3411,3413,3421,3404	100%	✓			
	Deltas	Per Procurement Specification	100%	✓			
	Percent Defective Allowable	Per Procurement Specification	100%	✓			
	Dynamic RDSON	EPC SPACE Internal	100%	✓			
	OutLiers Removal	EPC SPACE Internal	100%	✓			
	X-RAY	2076	100%	✓			
Pre-Assembly  Post-Assembly  Post-Assembly  Die Shear X-Ray  Serilialization Electricals Temp Cycling Constant Accelerating PIND Initial Electricals (Reference of the properties) Interim Electricals (Reference of the properties)  Percent Defective And Dynamic RDSON OutLiers Removal X-RAY Tinning Hermetic Seal, Fine Final Electricals  A-2 DC Static Test A-7 Gate Charges A-7 Capacitance  Group A Inspection (Conformance)  Group C Inspection (Conformance)  Group D Inspection (Conformance)  Group D Inspection (Conformance)  Group D Inspection (Conformance)  TID SEE	Tinning		100%	✓			
	Hermetic Seal, Fine & Gross Leak	1071	100%	✓			
	Final Electricals	3411,3413,3421,3404	100%	✓			
	A-2 DC Static Tests at 25°C	3411,3413,3421,3404	116	✓			
	A-3 High & Low Temp DC Static Tests	3411,3413,3421,3404	116	✓			
(Conformance)	A-7 Gate Charges	3471 Condition B	45	✓			
	A-7 Capacitance	3473	45	✓			
(a : ` \	B-1, B-2, B-3, B-4, B-5		1071 100% ✓  3411,3413,3421,3404 100% ✓  3411,3413,3421,3404 116 ✓  3411,3413,3421,3404 116 ✓  3471 Condition B 45 ✓  3473 45 ✓  ample base equivalent to a MIL-PRF-19500 flow or as required by procurement specificcation  Sample base performed yearly per package style equivalent to a				
	C-1, C-2, C-3, C-4, C-6, C-7	Per Procurement Specification         100%         ✓           Per Procurement Specification         100%         ✓           EPC SPACE Internal         100%         ✓           EPC SPACE Internal         100%         ✓           2076         100%         ✓           1071         100%         ✓           3411,3413,3421,3404         116         ✓           3471 Condition B         45         ✓           3473         45         ✓           Sample base equivalent to a MIL-PRF-19500 flow or as required by procurement specification           Sample base performed yearly per package style equivalent to a MIL-PRF-19500 flow or as required by procurement specification           1019         15         ✓           1080         5         ✓					
Group D Inspection	TID	1019	15	0%			
(Conformance)	Initial Electricals (Read and Record)   3411,3413,3421,3404     HTGB	5	✓				
Group A Inspection (Conformance)  Group B Inspection (Conformance)  Group C Inspection (Conformance)  Group D Inspection (Conformance)  Group E Inspection (Qualification	E-1, E-2, E-5, E-6 E-7	Performed during product introduction or a major process equivalent to a MIL-PRF-19500 flow or as required I			nge		
	E8 Switching		ent specification				



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#### **Patents**

EPC Corporation and EPC Space hold numerous worldwide patents. Any that apply to the product(s) listed in this document are identified by markings on the product(s) or on internal components of the product(s) in accordance with local patent laws.

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### **Revisions**

Datasheet Revision	Product Status
REV P#	Proposal/development
REV Q#	Characterization and Qualification
M-700-003-E	Production Released

Information subject to change without notice.

Revised February, 2023