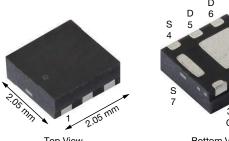
www.vishay.com

Vishay Siliconix

Automotive N-Channel 60 V (D-S) 175 °C MOSFET

PowerPAK® SC-70W-6L Single



Marking Code: Q9XXXX

ORDERING INFORMATION

Lead (Pb)-free and halogen-free

Bottom View

PRODUCT SUMMARY	
V _{DS} (V)	60
$R_{DS(on)}(\Omega)$ at $V_{GS} = 10 \text{ V}$	0.0390
$R_{DS(on)}$ (Ω) at $V_{GS} = 4.5 \text{ V}$	0.0526
I _D (A)	9
Configuration	Single

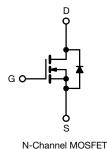
FEATURES

- TrenchFET® power MOSFET
- AEC-Q101 qualified
- Wettable flank terminals
- 100 % R_a and UIS tested
- · Material categorization: for definitions of compliance please see www.vishav.com/doc?99912





HALOGEN FREE



(for detailed order number please see www.vishay.com/doc?79776)

PowerPAK SC-70W-6L
SOA444CE IM

PARAMETER		SYMBOL	LIMIT	UNIT	
Drain-source voltage		V _{DS}	60		
Gate-source voltage		V _{GS}	± 20	V	
Continuous drain current	T _C = 25 °C ^a	1	9		
	T _C = 125 °C	- I _D	7.7		
Continuous source current (diode conduction) ^a		Is	9	Α	
Pulsed drain current ^b		I _{DM}	28		
Single pulse avalanche current	L = 0.1 mH	I _{AS}	12.5		
Single pulse avalanche energy	L=0.1 IIII	E _{AS}	7.81	mJ	
Maximum power dissipation	T _C = 25 °C	T _C = 25 °C		W	
	T _C = 125 °C	P_{D}	4.5	VV	
Operating junction and storage temperature range		T _J , T _{stg}	-55 to +175	°C	
Soldering recommendations (peak temperature) d, e		-	260	-0	

SQA444CEJW

THERMAL RESISTANCE RATINGS					
PARAMETER		SYMBOL	LIMIT	UNIT	
Junction-to-ambient	PCB mount c	R_{thJA}	90	°C/W	
Junction-to-case (drain)		R_{thJC}	11	C/VV	

Notes

- a. Package limited
- b. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %
- c. When mounted on 1" square PCB (FR4 material)
- See solder profile (www.vishay.com/doc?73257). The PowerPAK SC-70W-6L is a leadless package and features wettable flank terminals. The end of the lead terminal is plated with tin.
- e. Rework conditions: manual soldering with a soldering iron is not recommended for leadless components



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PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT	
Static					•			
Drain-source breakdown voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$		60	-	-	V	
Gate-source threshold voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$		1.5	2.0	2.5		
Gate-source leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$		-	-	± 100	nA	
Zero gate voltage drain current	I _{DSS}	$V_{GS} = 0 V$	V _{DS} = 60 V	-	-	1		
		V _{GS} = 0 V	V _{DS} = 60 V, T _J = 125 °C	-	-	50	μΑ	
		V _{GS} = 0 V	V _{DS} = 60 V, T _J = 175 °C	-	-	250		
On-state drain current ^a	I _{D(on)}	V _{GS} = 10 V	$V_{DS} \ge 5 \text{ V}$	10	-	-	Α	
Drain-source on-state resistance a	_(0.1)	V _{GS} = 10 V	I _D = 4.5 A	-	0.0318	0.0390	Ω	
		V _{GS} = 10 V	I _D = 4.5 A, T _J = 125 °C	-	-	0.0623		
	R _{DS(on)}	V _{GS} = 10 V	I _D = 4.5 A, T _J = 175 °C	-	-	0.0764		
		V _{GS} = 4.5 V	I _D = 3 A	-	0.0429	0.0526		
Forward transconductance b	9 _{fs}	V _{DS} :	= 15 V, I _D = 4.5 A	-	17	-	S	
Dynamic ^b		•		I.	•	I.		
Input capacitance	C _{iss}			-	378	530	pF	
Output capacitance	Coss	$V_{GS} = 0 V$	V _{DS} = 25 V, f = 1 MHz	-	215	300		
Reverse transfer capacitance	C _{rss}	1	,	-	12	17		
Total gate charge c	Qg			-	6	9	nC	
Gate-source charge ^c	Q _{gs}	V _{GS} = 10 V	$V_{DS} = 30 \text{ V}, I_{D} = 5 \text{ A}$	-	1.52	-		
Gate-drain charge c	Q _{gd}	1		-	0.55	-		
Gate resistance	R _g	f = 1 MHz		0.225	0.450	0.675	Ω	
Turn-on delay time ^c	t _{d(on)}	$V_{DD} = 30 \text{ V, } R_L = 6 \Omega$ $I_D \cong 5 \text{ A, } V_{GEN} = 10 \text{ V, } R_g = 1 \Omega$		-	8	12	ns	
Rise time ^c	t _r			-	3	6		
Turn-off delay time c	t _{d(off)}			-	13	20		
Fall time ^c	t _f			-	3	6		
Source-Drain Diode Ratings and Charact	eristics ^b			L		L		
Pulsed current a	I _{SM}			-	-	28	Α	
Forward voltage	V _{SD}	I _F = 4.5 A, V _{GS} = 0 V		-	0.84	1.2	V	
Body diode reverse recovery time	t _{rr}			-	16	32	ns	
Body diode reverse recovery charge	Q _{rr}	I _F = 2 A, di/dt = 100 A/μs		-	8.4	17	nC	
Reverse recovery fall time	t _a			_	9	-		
Reverse recovery rise time	t _b			-	7	-	ns	
Body diode peak reverse recovery current	I _{RM(REC)}			_	-1.0	-	Α	

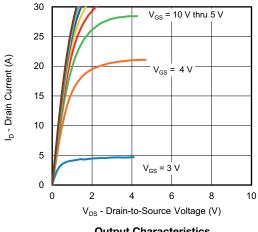
Notes

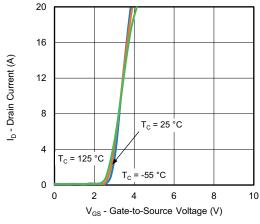
- a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %
- b. Guaranteed by design, not subject to production testing
- c. Independent of operating temperature

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



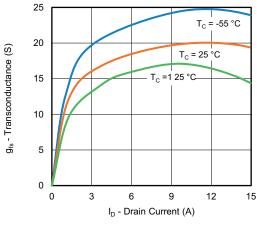
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)

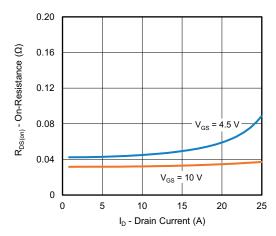




Output Characteristics

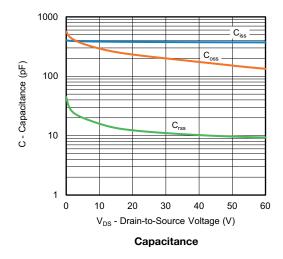


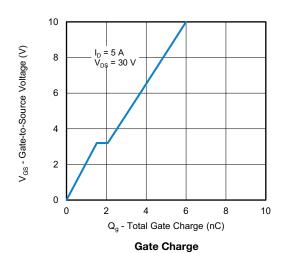




Transconductance

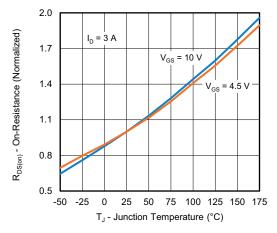
On-Resistance vs. Drain Current



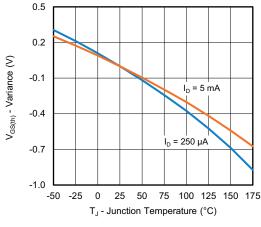




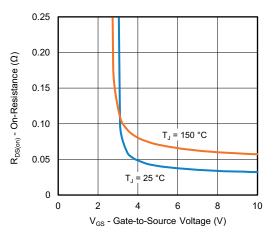
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



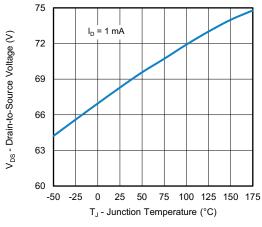
On-Resistance vs. Junction Temperature



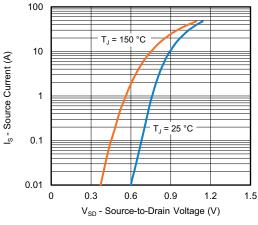
Threshold Voltage



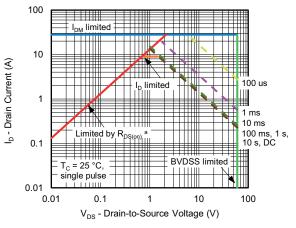
On-Resistance vs. Gate-to-Source Voltage



Drain Source Breakdown vs. Junction Temperature



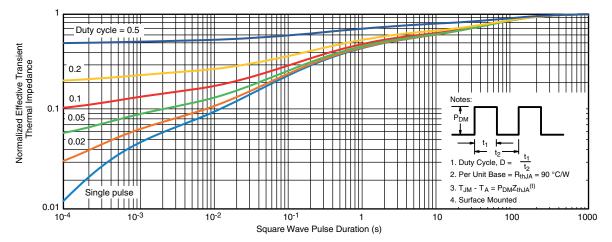
Source Drain Diode Forward Voltage



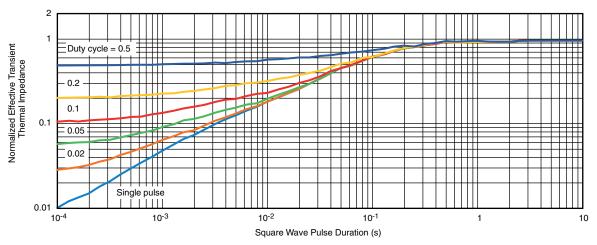
Safe Operating Area



THERMAL RATINGS (T_A = 25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Case

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