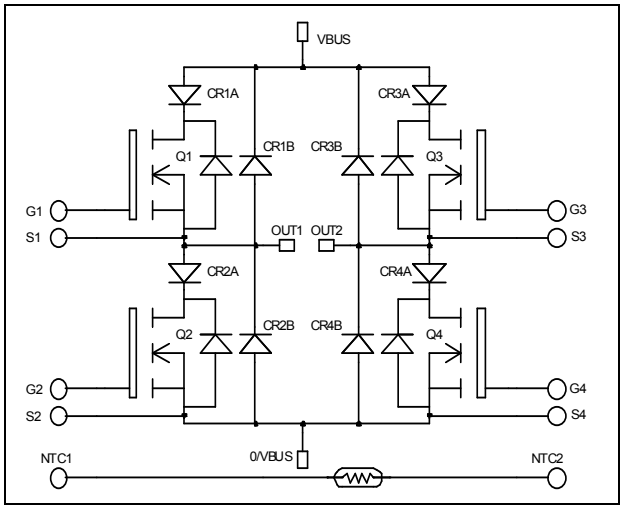
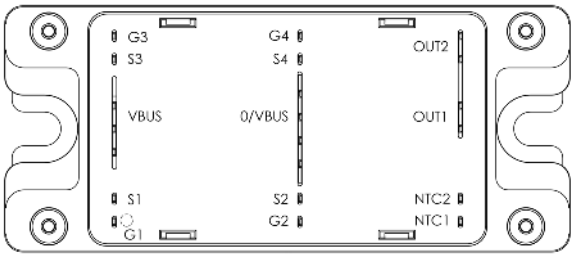
APTC60HM45SCTG

Full – Bridge Series & SiC parallel diodes
Super Junction MOSFET Power Module

V_{DSS} = 600V
R_{DSon} = 45mΩ max @ Tj = 25°C
I_D = 49A @ Tc = 25°C





Application

- Motor control
- Switched Mode Power Supplies
- Uninterruptible Power Supplies

Features

- CoolMOS™
  - Ultra low R_{DSon}
  - Low Miller capacitance
  - Ultra low gate charge
  - Avalanche energy rated
- ***Parallel SiC Schottky Diode***
  - Zero reverse recovery
  - Zero forward recovery
  - Temperature Independent switching behavior
  - Positive temperature coefficient on VF
- Kelvin source for easy drive
- Very low stray inductance
- Lead frames for power connections
- Internal thermistor for temperature monitoring
- High level of integration

Benefits

- Outstanding performance at high frequency operation
- Direct mounting to heatsink (isolated package)
- Low junction to case thermal resistance
- Solderable terminals both for power and signal for easy PCB mounting
- Low profile
- RoHS compliant

All ratings @ T_j = 25°C unless otherwise specified

Absolute maximum ratings

Symbol	*Parameter*		*Max ratings*	*Unit*
V_{DSS}	Drain - Source Voltage		600	V
I_D	Continuous Drain Current	T_c = 25°C	49	A
		T_c = 80°C	38	
I_{DM}	Pulsed Drain current		130	
V_{GS}	Gate - Source Voltage		±20	V
R_{DSon}	Drain - Source ON Resistance		45	mΩ
P_D	Maximum Power Dissipation	T_c = 25°C	250	W
I_{AR}	Avalanche current (repetitive and non repetitive)		15	A
E_{AR}	Repetitive Avalanche Energy		3	mJ
E_{AS}	Single Pulse Avalanche Energy		1900	

CAUTION: These Devices are sensitive to Electrostatic Discharge. Proper Handing Procedures Should Be Followed. See application note APT0502 on www.microsemi.com

Electrical Characteristics

Symbol	Characteristic	Test Conditions		Min	Typ	Max	Unit
I_{DSS}	Zero Gate Voltage Drain Current	V_{GS} = 0V, V_{DS} = 600V	T_j = 25°C			25	µA
		V_{GS} = 0V, V_{DS} = 600V	T_j = 125°C			250	
$R_{DS(on)}$	Drain – Source on Resistance	V_{GS} = 10V, I_D = 22.5A			40	45	mΩ
$V_{GS(th)}$	Gate Threshold Voltage	V_{GS} = V_{DS}, I_D = 3mA		2.1	3	3.9	V
I_{GSS}	Gate – Source Leakage Current	V_{GS} = ±20 V, V_{DS} = 0V				100	nA

Dynamic Characteristics

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
C_{iss}	Input Capacitance	V_{GS} = 0V ; V_{DS} = 25V f = 1MHz		7.2		nF
C_{oss}	Output Capacitance			8.5		
Q_g	Total gate Charge	V_{GS} = 10V V_{Bus} = 300V I_D = 44A		150		nC
Q_{gs}	Gate – Source Charge			34		
Q_{gd}	Gate – Drain Charge			51		
$T_{d(on)}$	Turn-on Delay Time	**Inductive switching @ 125°C** V_{GS} = 10V V_{Bus} = 400V I_D = 50A R_G = 5Ω		21		ns
T_r	Rise Time			30		
$T_{d(off)}$	Turn-off Delay Time			100		
T_f	Fall Time			45		
E_{on}	Turn-on Switching Energy	**Inductive switching @ 25°C** V_{GS} = 10V ; V_{Bus} = 400V I_D = 50A ; R_G = 5Ω		405		µJ
E_{off}	Turn-off Switching Energy			520		
E_{on}	Turn-on Switching Energy	**Inductive switching @ 125°C** V_{GS} = 10V ; V_{Bus} = 400V I_D = 50A ; R_G = 5Ω		658		µJ
E_{off}	Turn-off Switching Energy			635		
R_{thJC}	Junction to Case Thermal Resistance				0.5	°C/W

Series diode ratings and characteristics

Symbol	Characteristic	Test Conditions		Min	Typ	Max	Unit
V_{RRM}	Peak Repetitive Reverse Voltage					600	V
I_{RM}	Reverse Leakage Current	V_R=600V				50	µA
I_F	DC Forward current		Tc = 80°C		50		A
V_F	Diode Forward Voltage	I_F = 50A V_{GE} = 0V	T_j = 25°C		1.6	2	V
			T_j = 150°C		1.5		
t_{rr}	Reverse Recovery Time	I_F = 50A V_R = 300V di/dt =1800A/µs	T_j = 25°C		100		ns
			T_j = 150°C		150		
Q_{rr}	Reverse Recovery Charge		T_j = 25°C		2.6		µC
			T_j = 150°C		5.4		
E_{rr}	Reverse Recovery Energy		T_j = 25°C		0.60		mJ
			T_j = 150°C		1.2		
R_{thJC}	Junction to Case Thermal Resistance					1.42	°C/W

Parallel diode ratings and characteristics

Symbol	Characteristic	Test Conditions		Min	Typ	Max	Unit
V_{RRM}	Maximum Peak Repetitive Reverse Voltage			600			V
I_{RM}	Maximum Reverse Leakage Current	V_R=600V	T_j = 25°C		100	400	µA
			T_j = 175°C		200	2000	
I_F	DC Forward Current		Tc = 100°C		20		A
V_F	Diode Forward Voltage	I_F = 20A	T_j = 25°C		1.6	1.8	V
			T_j = 175°C		2.0	2.4	
Q_C	Total Capacitive Charge	I_F = 20A, V_R = 300V di/dt =800A/µs			28		nC
C	Total Capacitance	f = 1MHz, V_R = 200V			130		pF
		f = 1MHz, V_R = 400V			100		
R_{thJC}	Junction to Case Thermal Resistance					1.5	°C/W

Thermal and package characteristics

Symbol	Characteristic			Min	Max	Unit
V_{ISOL}	RMS Isolation Voltage, any terminal to case t =1 min, 50/60Hz			4000		V
T_J	Operating junction temperature range			-40	150	°C
T_{JOP}	Recommended junction temperature under switching conditions			-40	$T_J$max -25	
T_{STG}	Storage Temperature Range			-40	125	
T_C	Operating Case Temperature			-40	100	
Torque	Mounting torque	To Heatsink	M5	1.5	4.7	N.m
Wt	Package Weight				160	g

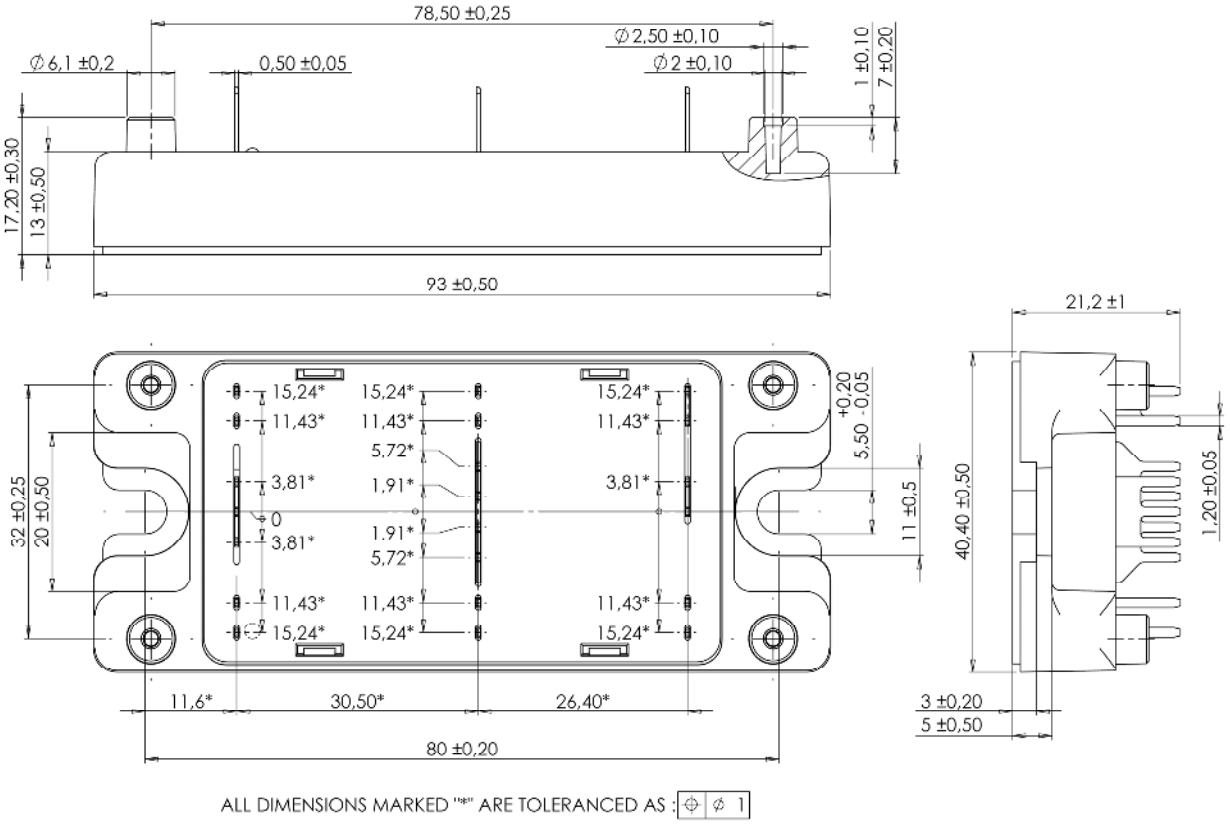
Temperature sensor NTC (see application note APT0406 on www.microsemi.com).

Symbol	Characteristic		Min	Typ	Max	Unit
R_{25}	Resistance @ 25°C			50		kΩ
$\Delta R_{25}/R_{25}$				5		%
$B_{25/85}$	T_{25} = 298.15 K			3952		K
ΔB/B		T_C=100°C		4		%

$$R_T = \frac{R_{25}}{\exp\left[B_{25/85}\left(\frac{1}{T_{25}} - \frac{1}{T}\right)\right]}$$

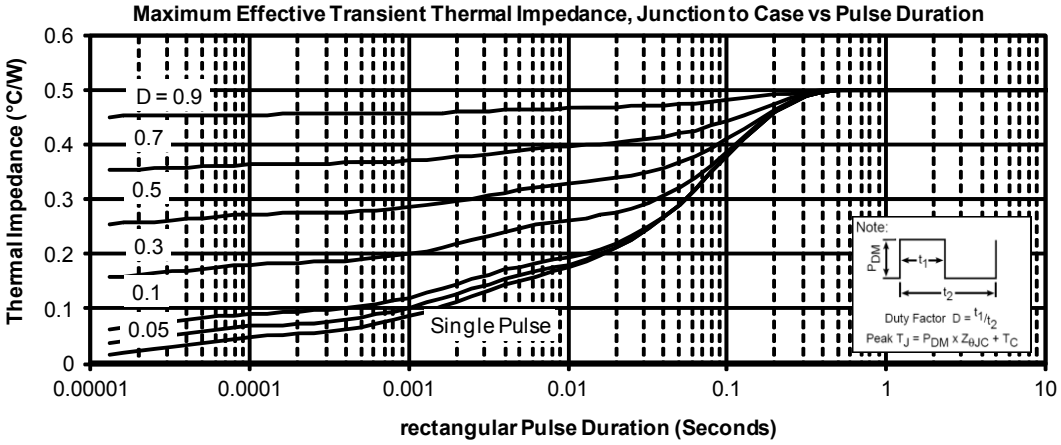
T: Thermistor temperature
R_T: Thermistor value at T

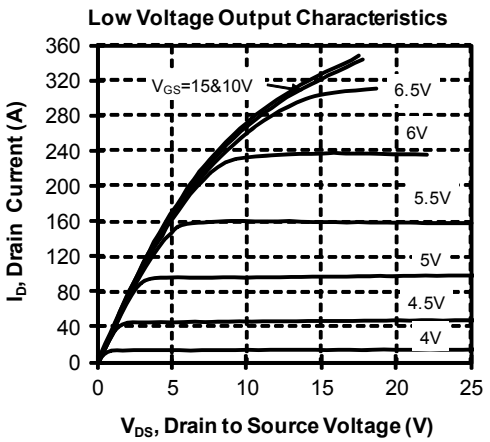
SP4 Package outline (dimensions in mm)

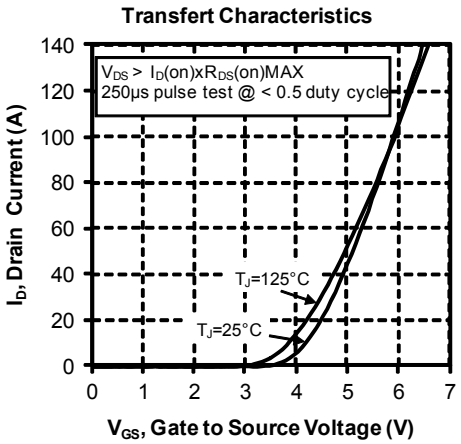


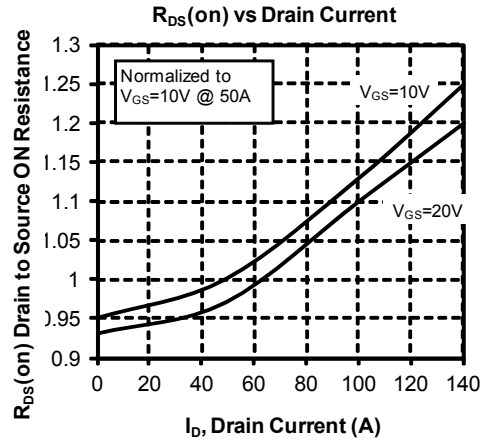
See application note APT0501 - Mounting Instructions for SP4 Power Modules on www.microsemi.com

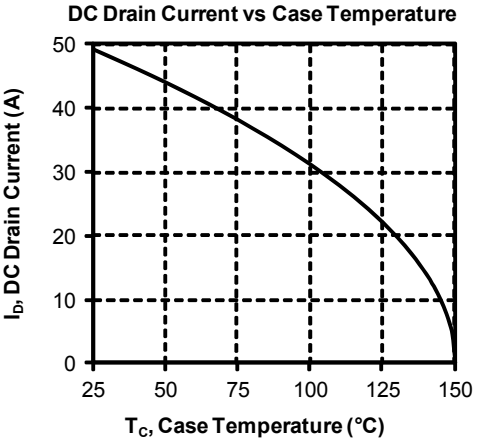
Typical CoolMOS Performance Curve



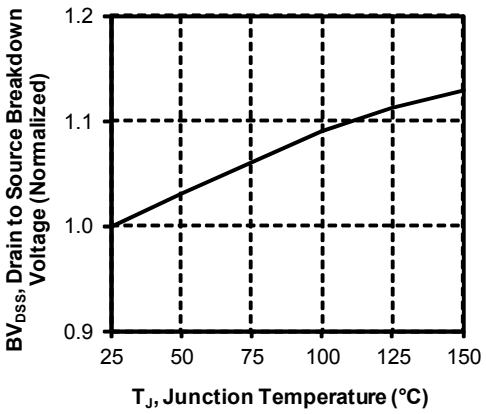




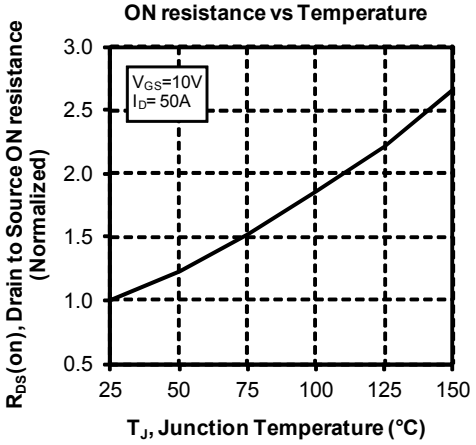




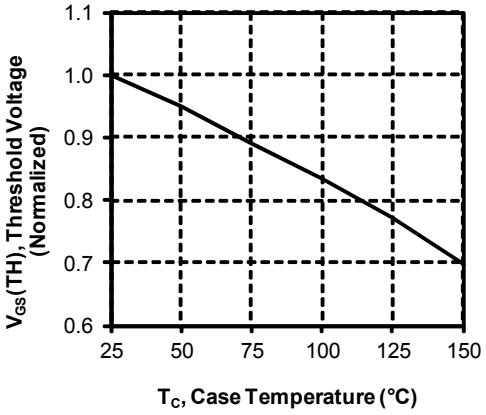
Breakdown Voltage vs Temperature



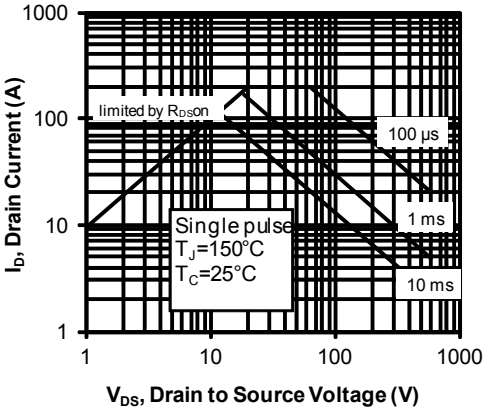
ON resistance vs Temperature



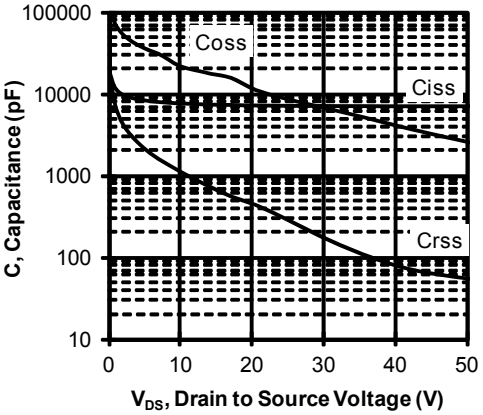
Threshold Voltage vs Temperature



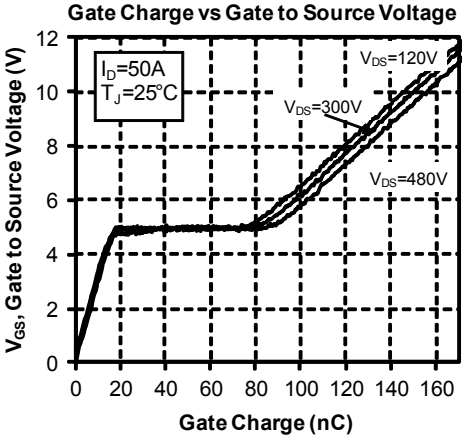
Maximum Safe Operating Area



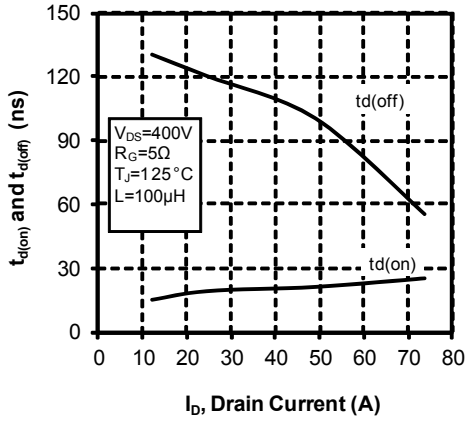
Capacitance vs Drain to Source Voltage



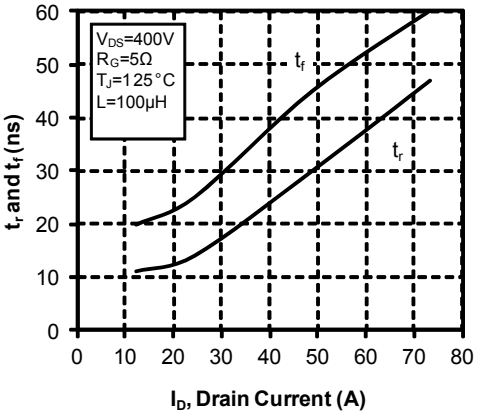
Gate Charge vs Gate to Source Voltage



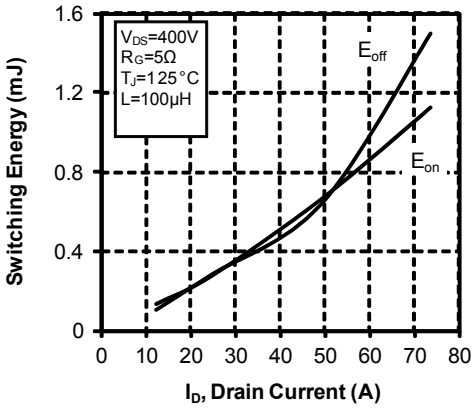
Delay Times vs Current



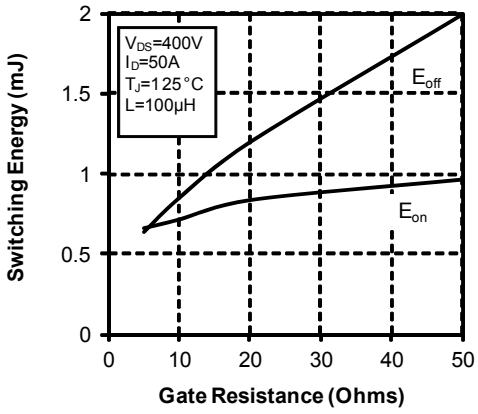
Rise and Fall times vs Current



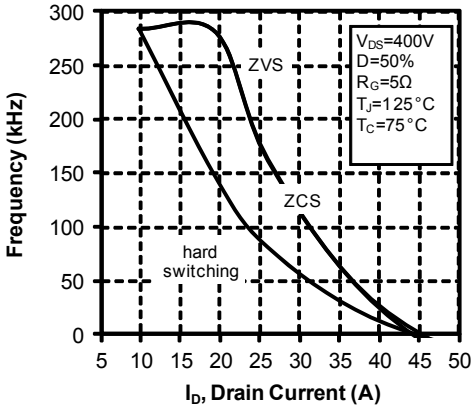
Switching Energy vs Current



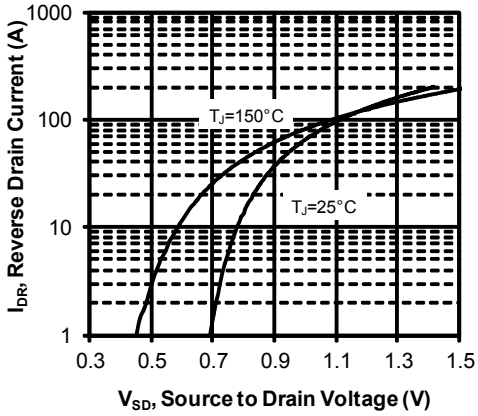
Switching Energy vs Gate Resistance



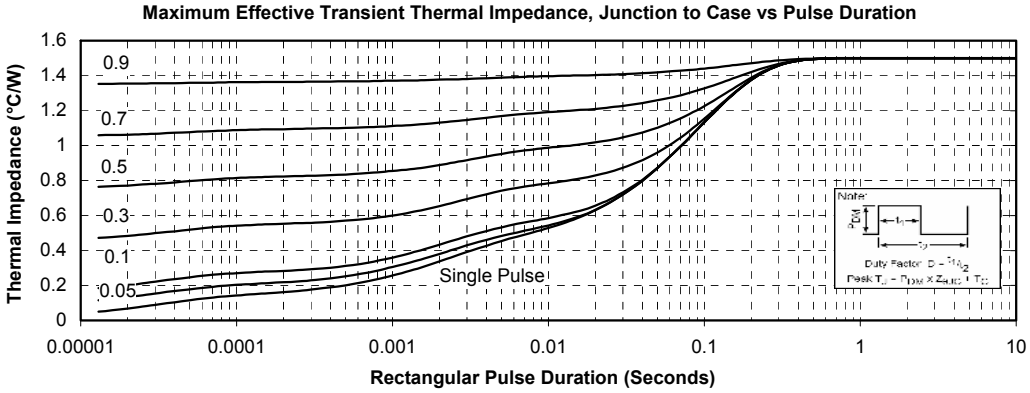
Operating Frequency vs Drain Current

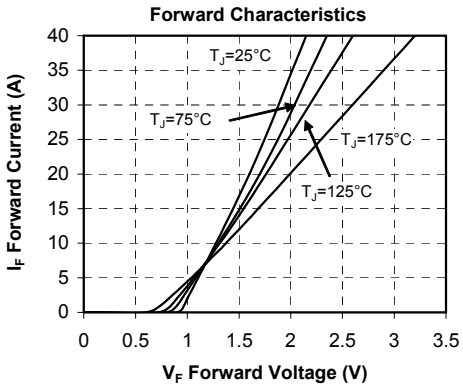


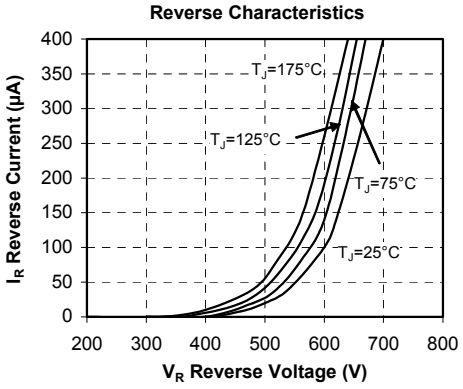
Source to Drain Diode Forward Voltage

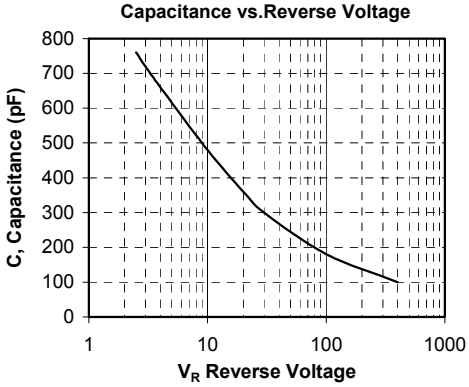


Typical SiC Diode Performance Curve









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