PCFG40T65SQF

IGBT Die

Using novel field stop IGBT technology, ON Semiconductor's new series of field stop 4th generation IGBTs offer the optimum performance for solar inverter and UPS applications where low conduction and switching losses are essential.

Features

- Maximum Junction Temperature: T_J = 175°C
- Positive Temperature Co−efficient for Easy Parallel Operating
- High Current Capability
- Low Saturation Voltage: $V_{CE(sat)}$ = 1.6 V (Typ.) @ I_C = 40 A
- High Input Impedance
- Fast Switching
- Tighten Parameter Distribution

Typical Applications

- Solar Inverters
- UPS Systems

MECHANICAL DATA

Parameter	Mils	μm
Die Size	140.94 × 140.94	3580 x 3580
Gate Pad Size	113.4 × 113.14	2880 x 2874
Emitter Pad Size	15.63 × 19.19	397 x 487.6
Die Thickness	2.48	63
Scribe Width	80 μm	
Top Metal	5 μm AlSiCu	
Back Metal	1.05 μm Al/NiV/Ag	
Topside Passivation	Silicon Nitride	
Wafer Diameter	200 mm	
Max Possible Die Per Wafer	1986	
Recommended Storage Environment	In original container, in dry nitrogen, < 3 months at ambient temperature of 23°C	

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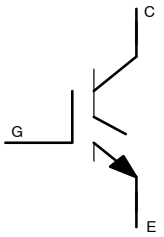
V_{RCE} = 650 V
I_C = Limited by $T_{j(max)}$



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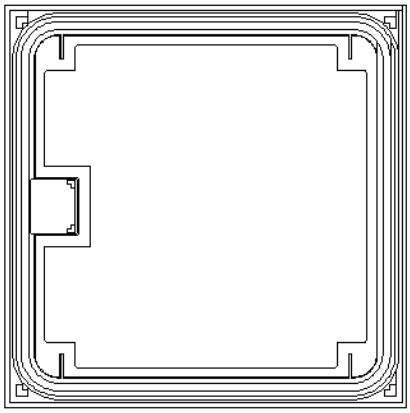


DIE Outline

ORDERING INFORMATION

Device	Inking?	Shipping Method
PCFG40T65SQF	No	Sawn Wafer on Tape

MAXIMUM RATINGS

Parameter	Symbol	Value	Unit
Collector to Emitter Voltage, T_J = 25°C	V_{CES}	650	V
Gate to Emitter Voltage	V_{GES}	±20	V
Collector Current @T_C = 25°C	I_C	(Note 1)	A
Pulsed Collector Current	I_{CM}	160	A
Operating Junction Temperature	T_J	−40 to +175	°C
Storage Temperature Range	T_{STG}	−17 to +25	°C

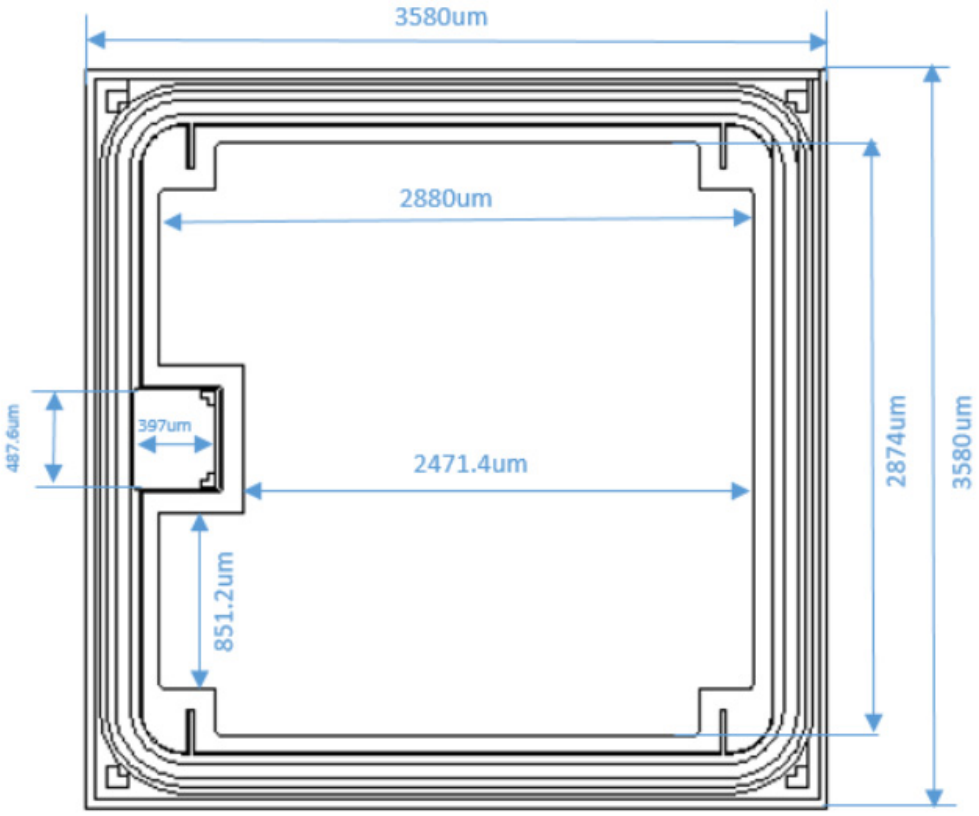
Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.
1. Depending on the thermal properties of assembly.
2. Not subject to production test − verified by design/characterization.

ELECTRICAL CHARACTERISTICS (T_J = 25°C unless otherwise specified)

Parameter	Test Conditions	Symbol	Min	Typ	Max	Unit
OFF CHARACTERISTICS						
Collector−Emitter Breakdown Voltage	V_{GE} = 0 V, I_C = 1 mA	BV_{CES}	650			V
Temperature Coefficient of Breakdown Voltage	I_C = 1 mA, reference to 25°C	$\Delta BV_{CES}/\Delta T_J$		0.6		V/°C
Collector−Emitter Cutoff Current	V_{GE} = 0 V, V_{CE} = V_{CES}	I_{DSS}			250	μA
Gate Leakage Current	V_{CE} = 0 V, V_{GE} = V_{GES}	I_{GSS}			±400	nA
ON CHARACTERISTICS						
G−E Threshold Voltage	V_{GE} = V_{CE}, I_C = 40 mA	$V_{GE(th)}$	2.6	4.5	6.4	V
Collector−Emitter Saturation Voltage	I_C = 40 A, V_{GE} = 15 V	$V_{CE(sat)}$		1.6	2.1	V
	I_C = 40 A, V_{GE} = 15 V, T_C = 175°C			1.92		V
DYNAMIC CHARACTERISTICS						
Input Capacitance	V_{GE} = 0 V, V_{CE} = 30 V, f = 1 MHz	C_{ies}		2620		pF
Output Capacitance		Coes		60		
Reverse Transfer Capacitance		C_{res}		9		
GATE CHARGE CHARACTERISTICS						
Total Gate Charge	V_{CE} = 400 V, I_C = 40 A, V_{GE} = 15 V	Q_g		80		nC
Gate to Emitter Charge		Q_{ge}		15		
Gate to Collector Charge		Q_{gc}		20		

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

*Switching characteristics and thermal properties depend strongly on module design and mounting technology.
For ordering, technique and other information on ON Semiconductor automotive bare die products, please contact automotivebaredie@onsemi.com.



(all dimensions in μm)

Figure 1. Die Layout

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:
Literature Distribution Center for ON Semiconductor
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