



PJX8808

20V N-Channel Enhancement Mode MOSFET

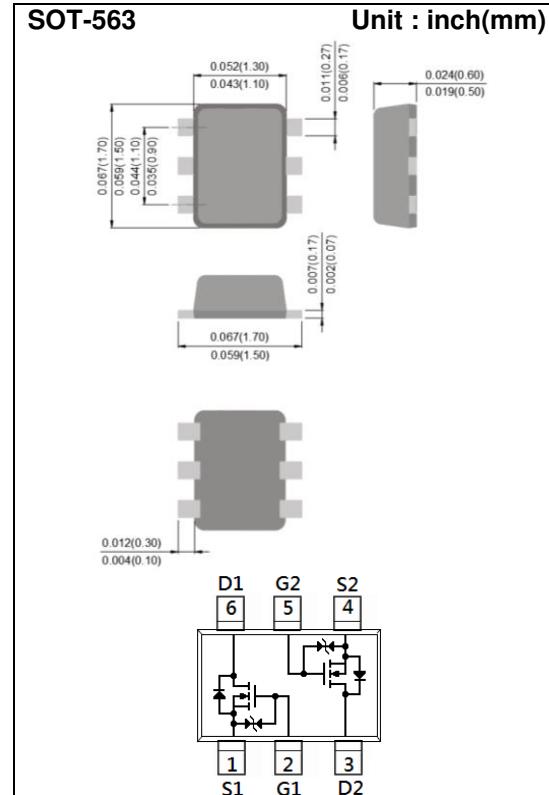
Voltage **20 V** **Current** **500mA**

Features

- Low Voltage Drive (1.2V).
- Advanced Trench Process Technology
- Specially Designed for Switch Load, PWM Application, etc.
- ESD Protected
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

Mechanical Data

- Case : SOT-563 Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight : 0.0026 grams
- Marking : X08



Maximum Ratings and Thermal Characteristics ($T_A=25^\circ\text{C}$ unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNITS
Drain-Source Voltage	V_{DS}	20	V
Gate-Source Voltage	V_{GS}	± 10	V
Continuous Drain Current	I_D	500	mA
Pulsed Drain Current	I_{DM}	1000	mA
Power Dissipation	$T_A=25^\circ\text{C}$	300	mW
		2.8	$\text{mW}/^\circ\text{C}$
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55~150	$^\circ\text{C}$
Typical Thermal Resistance - Junction to Ambient (Note 3)	$R_{\theta JA}$	417	$^\circ\text{C}/\text{W}$



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Electrical Characteristics ($T_A=25^\circ\text{C}$ unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$	20	-	-	V
Gate Threshold Voltage	$V_{\text{GS(th)}}$	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=250\mu\text{A}$	0.3	0.64	0.9	V
Drain-Source On-State Resistance	$R_{\text{DS(on)}}$	$V_{\text{GS}}=4.5\text{V}, I_{\text{D}}=500\text{mA}$	-	310	400	$\text{m}\Omega$
		$V_{\text{GS}}=2.5\text{V}, I_{\text{D}}=200\text{mA}$	-	360	650	
		$V_{\text{GS}}=1.8\text{V}, I_{\text{D}}=100\text{mA}$	-	430	800	
		$V_{\text{GS}}=1.5\text{V}, I_{\text{D}}=50\text{mA}$	-	510	1200	
		$V_{\text{GS}}=1.2\text{V}, I_{\text{D}}=20\text{mA}$	-	710	3000	
Zero Gate Voltage Drain Current	I_{DSS}	$V_{\text{DS}}=16\text{V}, V_{\text{GS}}=0\text{V}$	-	-	1	μA
Gate-Source Leakage Current	I_{GSS}	$V_{\text{GS}}=\pm 8\text{V}, V_{\text{DS}}=0\text{V}$	-	± 0.5	± 10	μA
Dynamic ^(Note 5)						
Total Gate Charge	Q_g	$V_{\text{DS}}=10\text{V}, I_{\text{D}}=500\text{mA}, V_{\text{GS}}=4.5\text{V}^{(\text{Note 1,2})}$	-	1.4	-	nC
Gate-Source Charge	Q_{gs}		-	0.22	-	
Gate-Drain Charge	Q_{gd}		-	0.21	-	
Input Capacitance	C_{iss}	$V_{\text{DS}}=10\text{V}, V_{\text{GS}}=0\text{V}, f=1.0\text{MHZ}$	-	67	-	pF
Output Capacitance	C_{oss}		-	19	-	
Reverse Transfer Capacitance	C_{rss}		-	6	-	
Turn-On Delay Time	$t_{\text{d(on)}}$	$V_{\text{DD}}=10\text{V}, I_{\text{D}}=150\text{mA}, V_{\text{GS}}=4.0\text{V}, R_{\text{G}}=10\Omega^{(\text{Note 1,2})}$	-	2.8	-	ns
Turn-On Rise Time	t_{r}		-	20	-	
Turn-Off Delay Time	$t_{\text{d(off)}}$		-	23	-	
Turn-Off Fall Time	t_{f}		-	23	-	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	I_{s}	---	-	-	500	mA
Diode Forward Voltage	V_{SD}	$I_{\text{s}}=500\text{mA}, V_{\text{GS}}=0\text{V}$	-	0.87	1.3	V

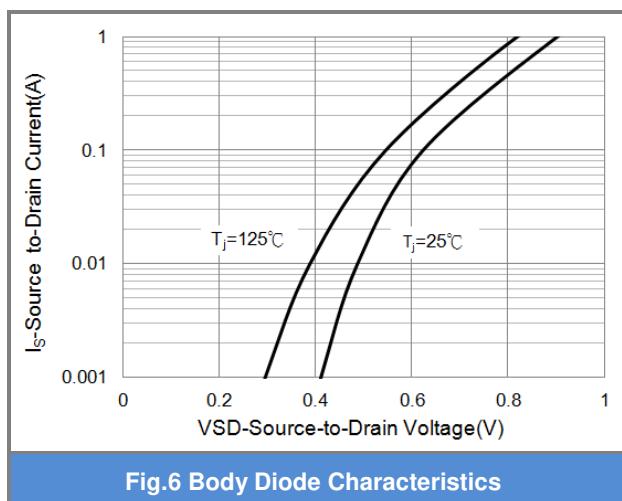
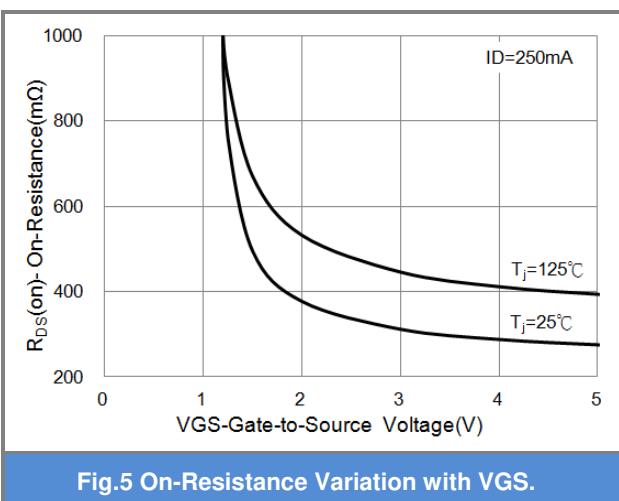
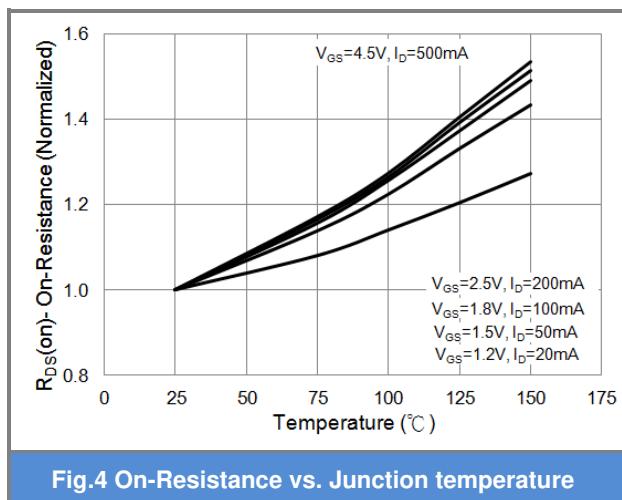
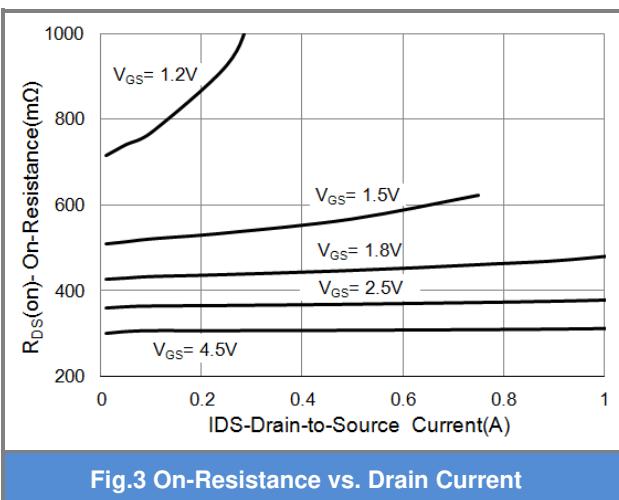
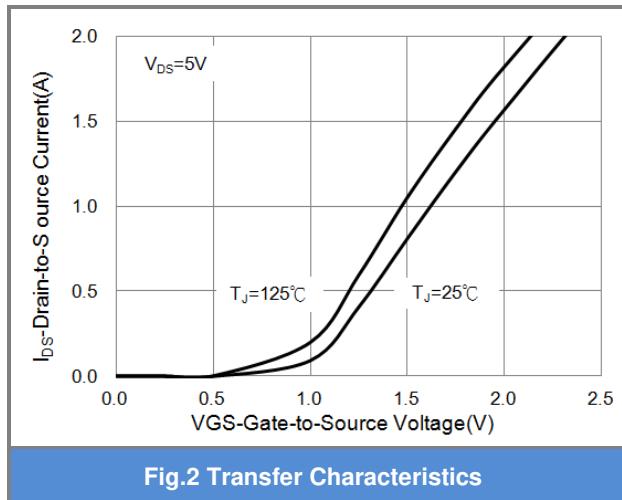
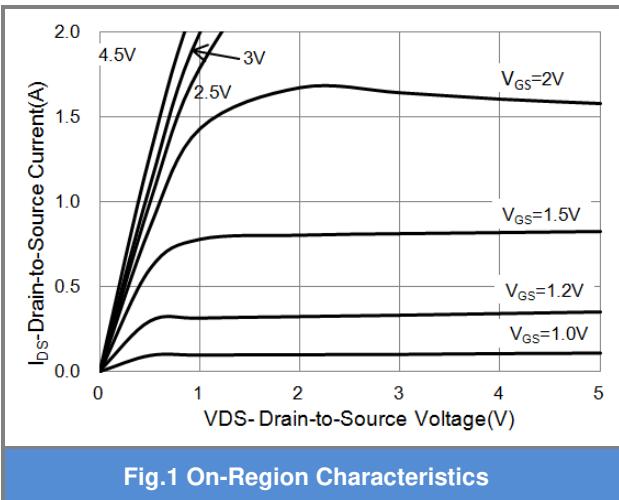
NOTES :

1. Pulse width $\leq 300\mu\text{s}$, Duty cycle $\leq 2\%$
2. Essentially independent of operating temperature typical characteristics.
3. R_{OJA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper.
4. The maximum current rating is package limited.
5. Guaranteed by design, not subject to production testing.



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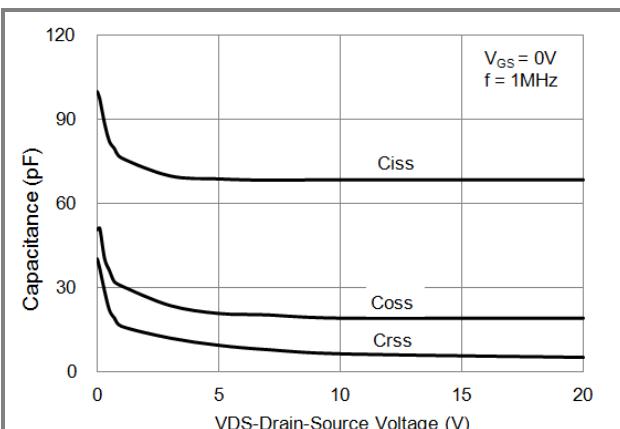
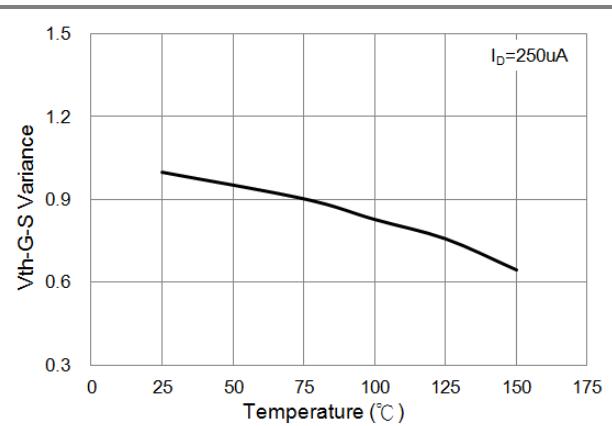
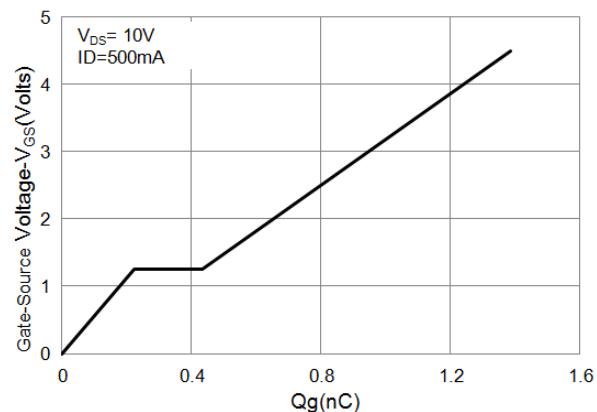
TYPICAL CHARACTERISTIC CURVES





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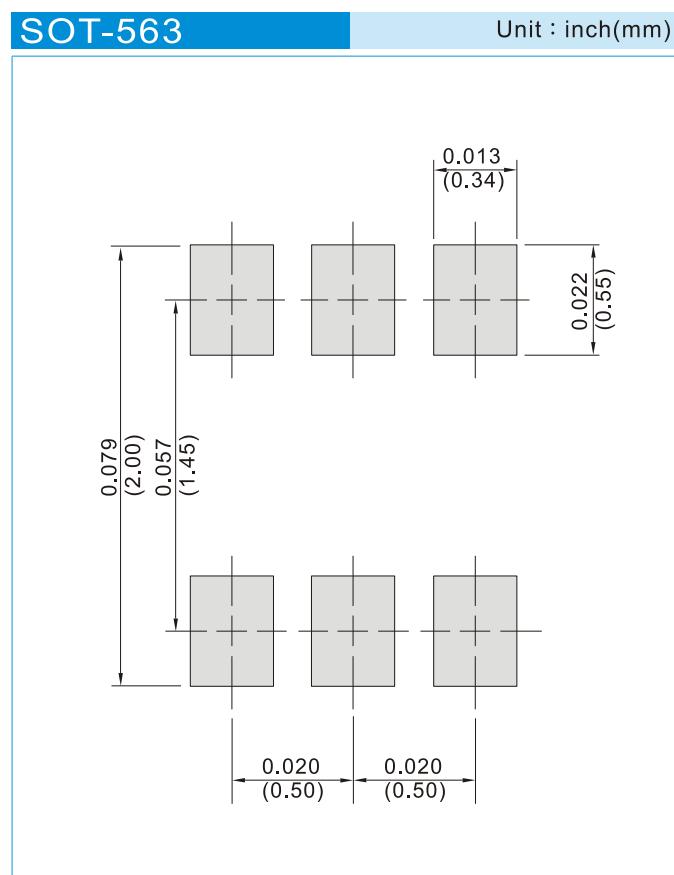


PJX8808

Part No. Packing Code Version

Part No. Packing Code	Package Type	Packing Type	Marking	Version
PJX8808_R1_00001	SOT-563	4K pcs / 7" reel	X08	Halogen free RoHS compliant
PJX8808_R2_00001	SOT-563	10K pcs / 13" reel	X08	Halogen free RoHS compliant

Mounting Pad Layout





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