C6D20065A

6th Generation 650 V, 20 A Silicon Carbide Schottky Diode

Description

With the performance advantages of a Silicon Carbide (SiC) Schottky Barrier diode, power electronics systems can expect to meet higher e iciency standards than Si-based solutions, while also reaching higher frequencies and power densities. SiC diodes can be easily paralleled to meet various application demands, without concern of thermal runaway. In combination with the reduced cooling requirements and improved thermal performance of SiC products, SiC diodes are able to provide lower overall system costs in a variety of diverse applications.

Package Types: TO-220-2
Marking: C6D20065A

Features

- Low Forward Voltage (V_F) Drop with Positive Temperature Coe icient
- Zero Reverse Recovery Current / Forward Recovery Voltage
- Temperature-Independent Switching Behavior

Applications

- Industrial Switched Mode Power Supplies
- Uninterruptible & AUX Power Supplies
- Boost for PFC & DC-DC Stages
- Solar Inverters

Maximum Ratings ($T_C = 25°C$ Unless Otherwise Specified)

Parameter	Symbol	Value	Unit	Test Conditions	Notes
Repetitive Peak Reverse Voltage	V_{RRM}	650	V		
DC Blocking Voltage	V_{DC}	650			
Continuous Forward Current	I_F	66	A	$T_J = 25$ °C	Fig. 3
		33		$T_J = 125$ °C	
		21		$T_J = 150$ °C	
Repetitive Peak Forward Surge Current	I_{FRM}	75		$T_C = 25$ °C, $t_p = 10$ ms, Half Sine Wave	
		42		$T_C = 110$ °C, $t_p = 10$ ms, Half Sine Wave	
Non-Repetitive Forward Surge Current	I_{FSM}	125		$T_C = 25$ °C, $t_p = 10$ ms, Half Sine Wave	Fig. 8
		99		$T_C = 110$ °C, $t_p = 10$ ms, Half Sine Wave	
Non-Repetitive Peak Forward Surge Current	$I_{F,Max}$	1475		$T_C = 25$ °C, $t_p = 10$ µs, Pulse	
		1225		$T_C = 110$°C, $t_p = 10$ µs, Pulse	
Power Dissipation	P_{tot}	166	W	$T_J = 25$ °C	Fig. 4
		51		$T_J = 110$ °C	
i^2t Value	i^2t	83	A^2s	$T_C = 25$ °C, $t_p = 10$ ms	
		51		$T_C = 110$°C, $t_p = 10$ ms	

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Electrical Characteristics

Parameter	Symbol	Typ.	Max.	Unit	Test Conditions	Notes
Forward Voltage	V_F	1.27	1.50	V	$I_F = 20$ A, $T_j = 25$ °C	Fig. 1
		1.37	1.60		$I_F = 20$ A, $T_j = 175$ °C	
Reverse Current	I_R	5	30	µA	$V_R = 650$ V, $T_j = 25$ °C	Fig. 2
		40	300		$V_R = 650$ V, $T_j = 175$ °C	
Total Capacitive Charge	Q_C	62		nC	$V_R = 400$ V, $T_j = 25$ °C	Fig. 5
Total Capacitance	C	1153		pF	$V_R = 0$ V, T_j	

Typical Performance

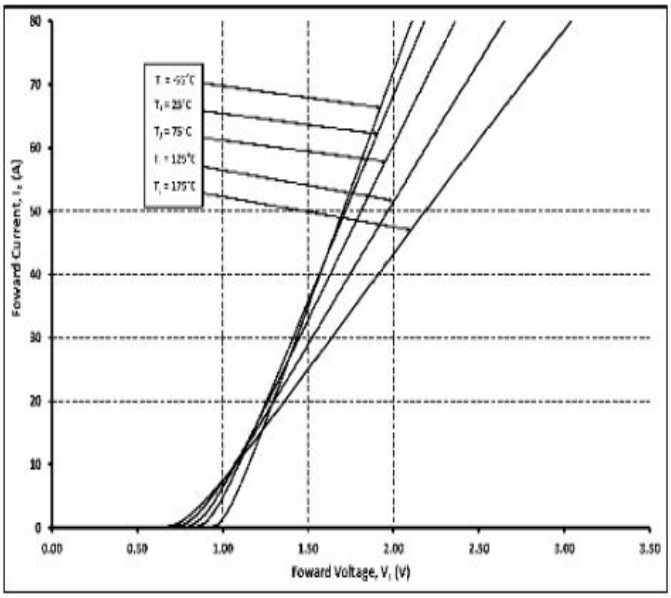


Figure 1
Forward Characteristics

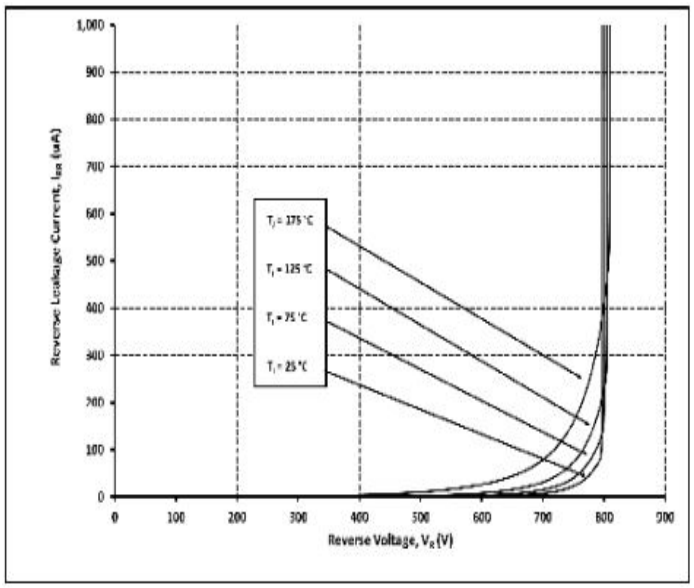


Figure 2
Reverse Characteristics

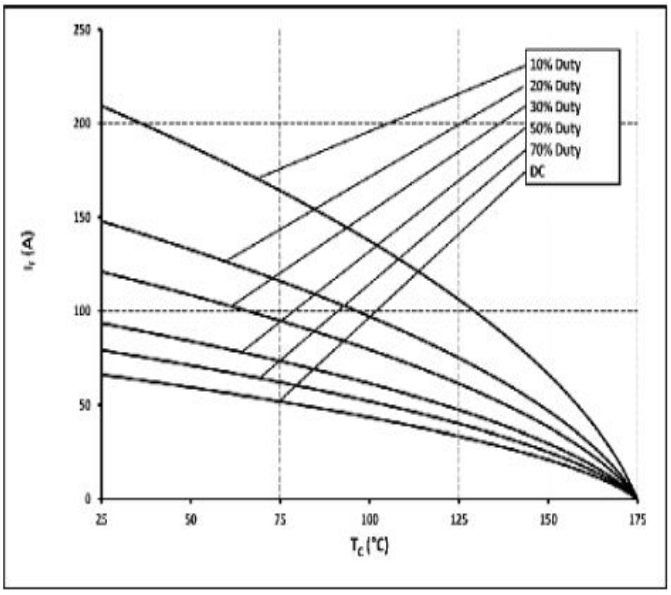


Figure 3
Current Derating

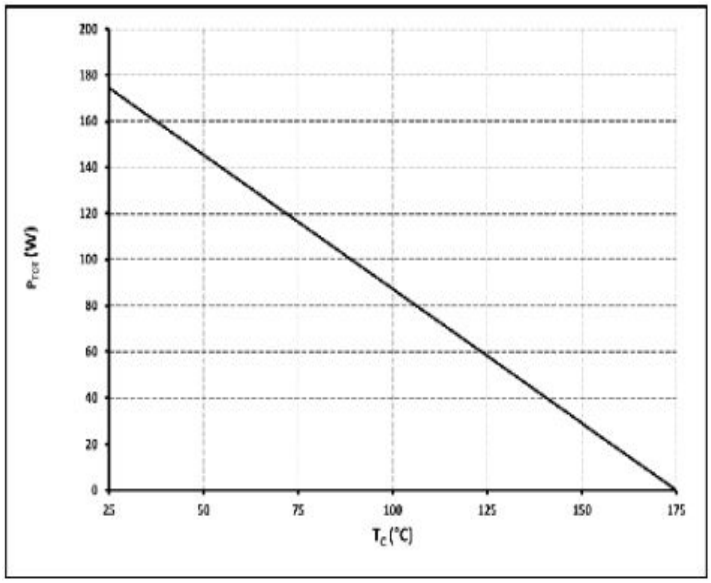


Figure 4
Power Derating

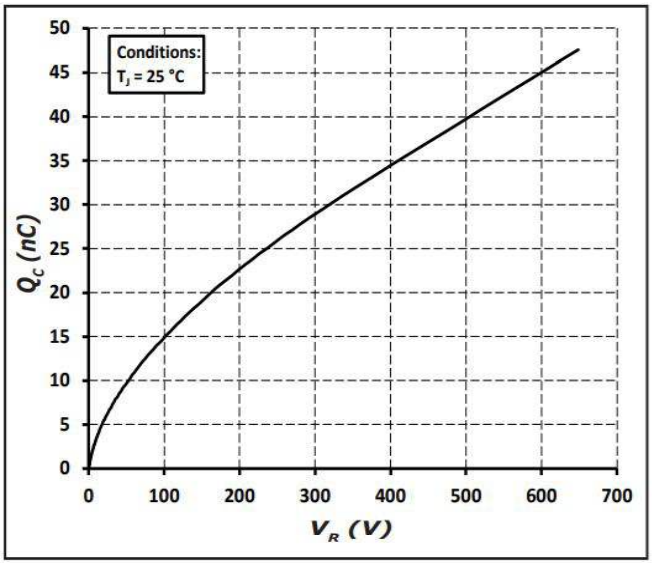


Figure 5
Total Capacitance vs. Reverse Voltage

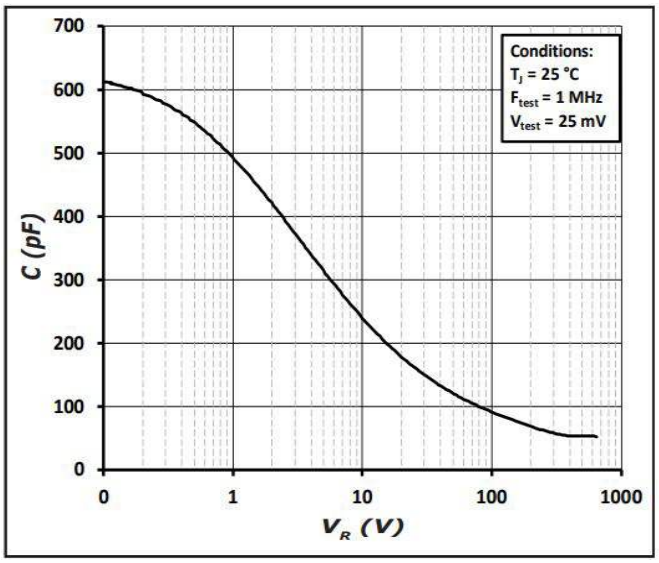


Figure 6
Capacitace vs. Reverse Voltage

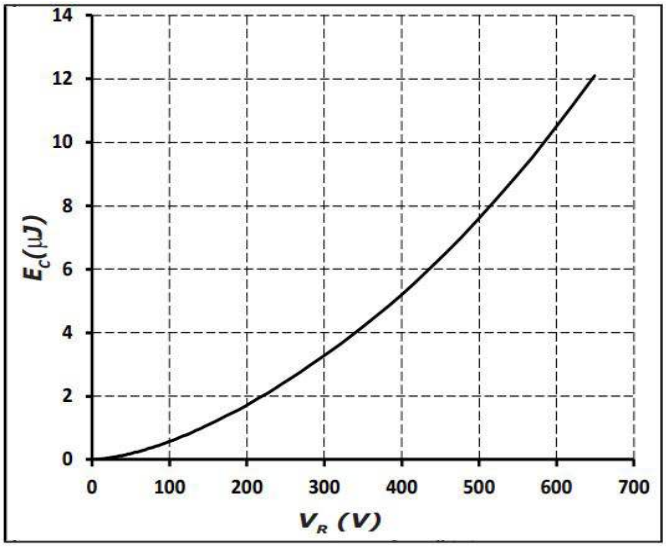


Figure 7
Capacitance Stored Energy

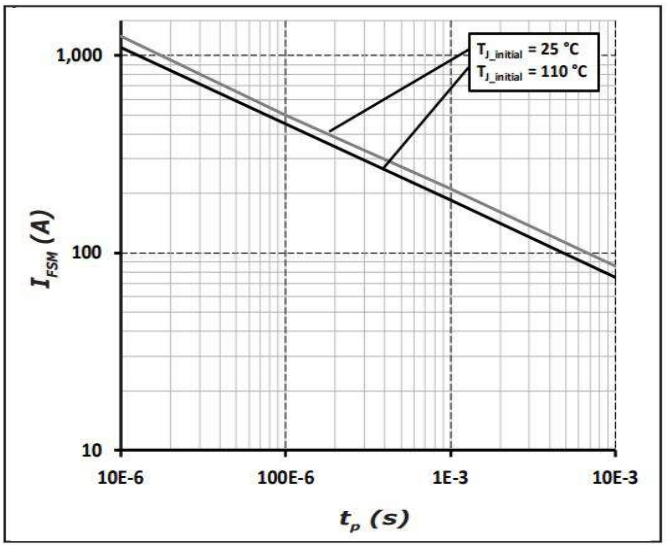


Figure 8
Non-Repetitive Peak Forward Surge Current versus Pulse Duration (sinusoidal waveform)

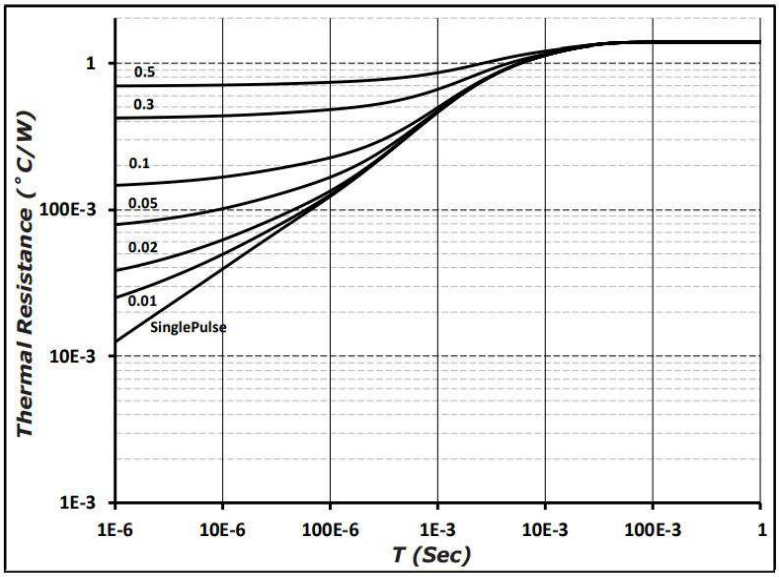
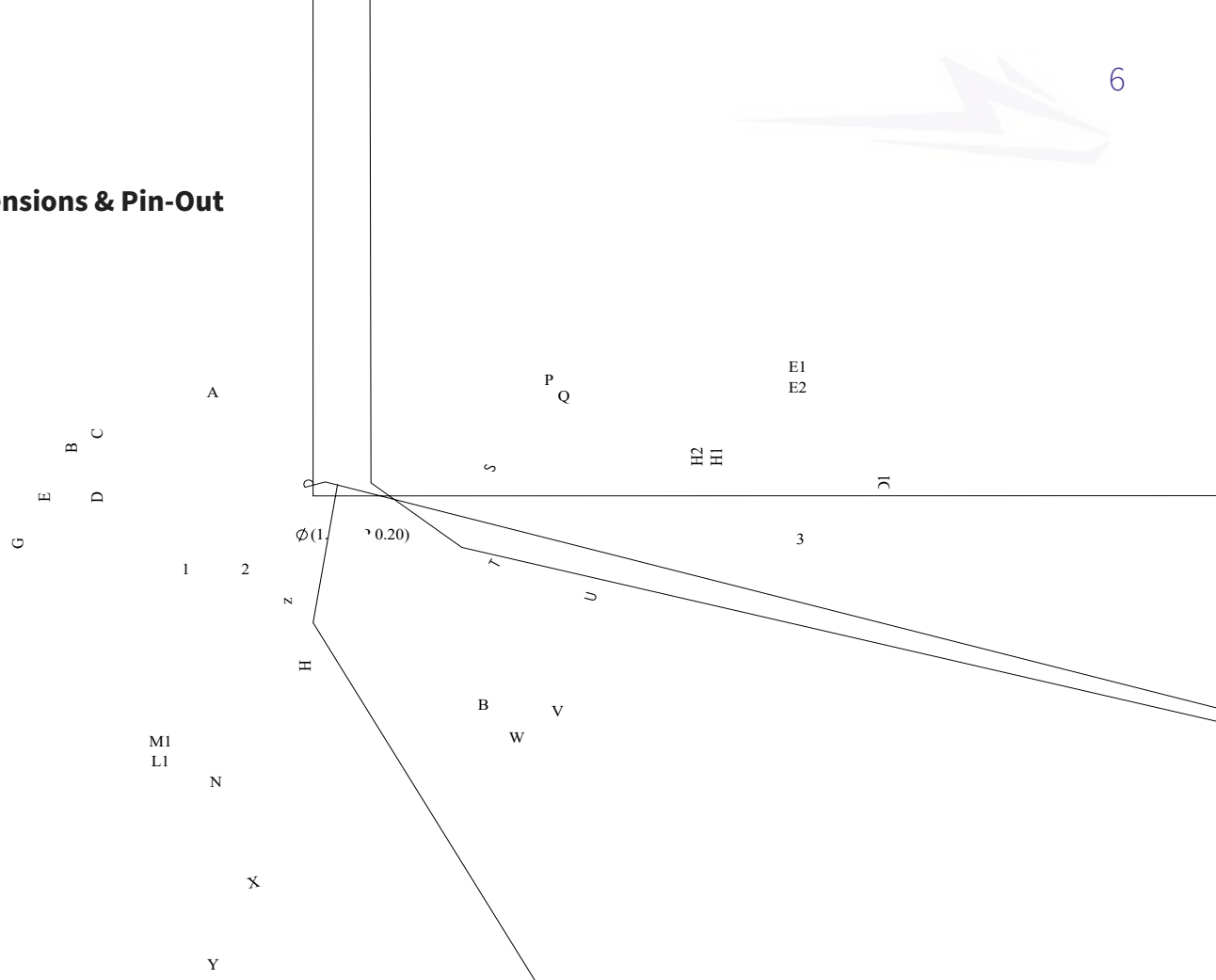


Figure 9
Transient Thermal Impedance

Package Dimensions & Pin-Out

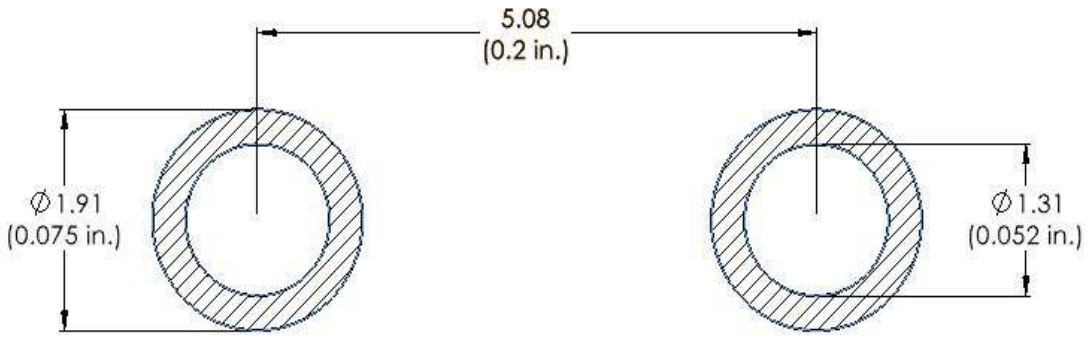
Package: TO-220-2



NOTE
1. ALL METAL SURFACES ARE TIN PLATE EXCEPT AREA OF CUT.
2. DIMENSIONING & TOLERANCING CONFOR ASME Y14.5M-1994.
3. ALL DIMENSIONS ARE LISTED IN MILLIMETE ANGLES ARE IN DEGREES.
4. PACKAGE BURR FLASH SIZE (0.5 mm) IS NOT INCLUDED IN THE DIMENSIONS

Recommended Solder Pad Layout

Primary dimensions shown in mm.



Product Ordering Information

Order Number	Packing Type
C6D20065A	Tube

Revision History

Document Version	Date of Release	Description of Changes
0	August-2022	Initial Release
1	April-2023	Update Package Drawing Update Landing Pad

Notes & Disclaimer

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