### 60V Nch+Nch Power MOSFET

V <sub>DSS</sub>	60V
R <sub>DS(on)</sub> (Max.)	120mΩ
I <sub>D</sub>	±3.5A
P <sub>D</sub>	2.0W

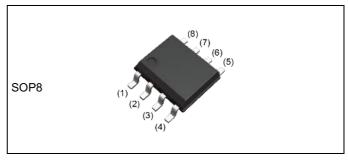
### Features

- 1) Low on resistance
- 2) Small Surface Mount Package (SOP8)
- 3) Pb-free plating ; RoHS compliant
- 4) Halogen free
- 5) Sn100% plating
- 6) AEC-Q101 Qualified

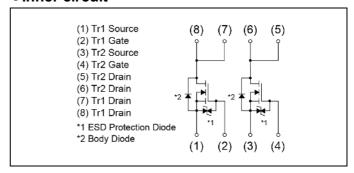
# Application

Switching

#### Outline



### •Inner circuit



# Packaging specifications

	Packing	Embossed Tape
	Reel size (mm)	330
Туре	Tape width (mm)	12
	Quantity (pcs)	2500
	Taping code	ТВ
	Marking	SP8K31

# ● **Absolute maximum ratings** (T<sub>a</sub> = 25°C ,unless otherwise specified) < Tr1 and Tr2>

Parameter	Symbol	Value	Unit	
Drain - Source voltage	$V_{DSS}$	60	V	
Continuous drain current	I <sub>D</sub>	±3.5	Α	
Pulsed drain current	I <sub>DP</sub> *1	±14	Α	
Gate - Source voltage	V <sub>GSS</sub>	±20	V	
Down discipation (total)	P <sub>D</sub> *2	2.0	۱۸/	
Power dissipation (total)	P <sub>D</sub> *3	1.4	W	
Junction temperature	Tj	150	°C	
Operating junction and storage temperature range	T <sub>stg</sub>	-55 to +150	°C	

## ●Thermal resistance

Downwater	Currele el	Values			1.1-:4
Parameter	Symbol	Min.	Тур.	Max.	Unit
Thermal registeres innetion, embient (total)	R <sub>thJA</sub> *2	-	-	62.5	°C/W
Thermal resistance, junction - ambient (total)	R <sub>thJA</sub> *3	-	-	89.2	C/VV

# ● Electrical characteristics (T<sub>a</sub> = 25°C) < Tr1 and Tr2>

Davanatas	Cy made al	Conditions	Values			Unit	
Parameter Symbol Co		Conditions	Min.	Тур.	Max.	Uffil	
Drain - Source breakdown voltage	V <sub>(BR)DSS</sub>	$V_{GS} = 0V$ , $I_D = 1mA$	60	-	-	V	
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_{j}}$	I <sub>D</sub> = 1A referenced to 25°C	-	63.7	-	mV/°C	
Zero gate voltage drain current	I <sub>DSS</sub>	V <sub>DS</sub> = 60V, V <sub>GS</sub> = 0V	-	-	1	μА	
Gate - Source leakage current	$I_{GSS}$ $V_{GS} = \pm 20V$ , $V_{DS} = 0V$		-	-	±10	μА	
Gate threshold voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = 10V, I <sub>D</sub> = 1mA		-	2.5	V	
Gate threshold voltage temperature coefficient	$\frac{\DeltaV_{GS(th)}}{\DeltaT_j}$	I <sub>D</sub> = 1A referenced to 25°C	-	-2.8	-	mV/°C	
		V <sub>GS</sub> = 10V, I <sub>D</sub> = 3.5A	-	85	120		
Static drain - source on - state resistance	R <sub>DS(on)</sub> *4	V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 3.5A	-	100	140	mΩ	
on state resistance		V <sub>GS</sub> = 4.0V, I <sub>D</sub> = 3.5A	-	105	150		
Gate resistance	$R_{G}$	f = 1MHz, open drain	-	6.9	-	Ω	
Forward Transfer Admittance	$ Y_{fs} ^{*4}$ $V_{DS} = 10V, I_D = 3.5A$		2.5	-	-	S	

<sup>\*1</sup> Pw  $\leq$  10µs, Duty cycle  $\leq$  1%

<sup>\*2</sup> Mounted on a ceramic board (30×30×0.8mm)

<sup>\*3</sup> Mounted on a Cu board (40×40×0.8mm)

<sup>\*4</sup> Pulsed

# ● Electrical characteristics (T<sub>a</sub> = 25°C) < Tr1 and Tr2>

Parameter	Cumbal	Conditions	Values			Lloit	
Parameter	Symbol Conditions —		Min.	Тур.	Max.	Unit	
Input capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0V	-	250	-		
Output capacitance	C <sub>oss</sub>	V <sub>DS</sub> = 10V	-	60	-	pF	
Reverse transfer capacitance	$C_{rss}$	f = 1MHz	-	30	-		
Turn - on delay time	$t_{d(on)}^{*4}$	V <sub>DD</sub> ≃ 30V,V <sub>GS</sub> = 10V	-	7	-		
Rise time	t <sub>r</sub> *4	I <sub>D</sub> = 1.8A	-	14	-	no	
Turn - off delay time	$t_{d(off)}^{*4}$	$R_L = 17\Omega$	_	25	-	ns	
Fall time	t <sub>f</sub> *4	$R_G = 10\Omega$	-	7	-		

# ullet Gate charge characteristics (T<sub>a</sub> = 25°C) <Tr1 and Tr2>

Parameter	Symbol	Conditions	Values			Unit
raianietei	Symbol	Conditions	Min.	Тур.	Max.	Offic
Total gate charge	$Q_g^{*4}$		-	3.7	5.2	
Gate - Source charge	Q <sub>gs</sub> *4	$V_{DD} \approx 30V, I_{D} = 3.5A$ $V_{GS} = 5V$	-	1.2	-	nC
Gate - Drain charge	Q <sub>gd</sub> *4	1.00	-	1.2	-	

# ●Body diode electrical characteristics (Source-Drain) (T<sub>a</sub> = 25°C)

## <Tr1 and Tr2>

Darameter	Cymahal	Conditions	Values			1 1:4	
Parameter	Symbol Conditions		Min.	Тур.	Max.	Unit	
Continuous forward current	I <sub>S</sub>	T - 25°C	-	-	1.0	^	
Pulse forward current	I <sub>SP</sub> *1	T <sub>a</sub> = 25°C	-	-	14	А	
Forward voltage	V <sub>SD</sub> *4	V <sub>GS</sub> = 0V, I <sub>S</sub> = 3.5A	-	-	1.2	V	

Fig.1 Power Dissipation Derating Curve

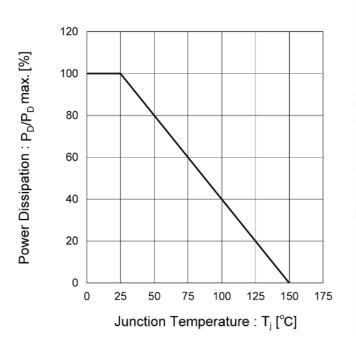


Fig.2 Maximum Safe Operating Area

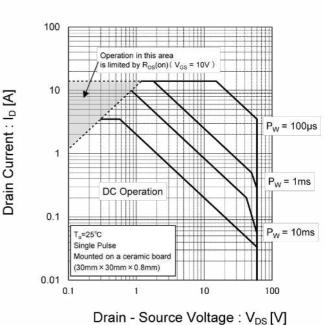


Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

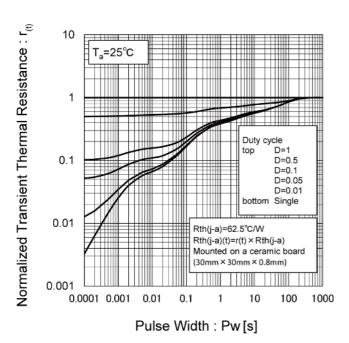
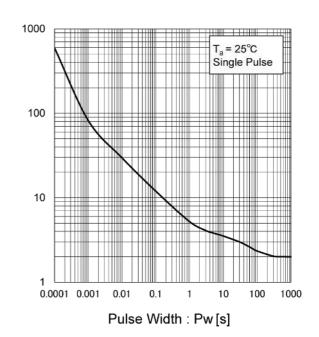


Fig.4 Single Pulse Maximum Power Dissipation



Peak Transient Power: P(W)

Fig.5 Typical Output Characteristics(I)

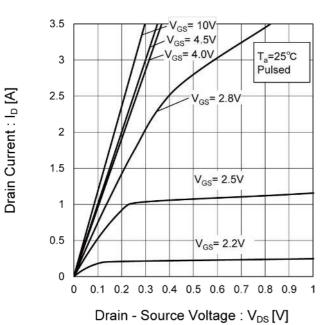
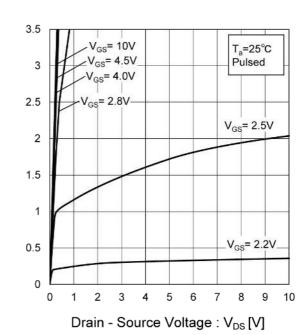


Fig.6 Typical Output Characteristics(II)



Drain Current: Ip [A]

Fig.7 Breakdown Voltage vs.
Junction Temperature

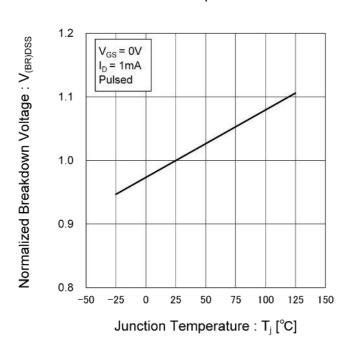


Fig.8 Typical Transfer Characteristics

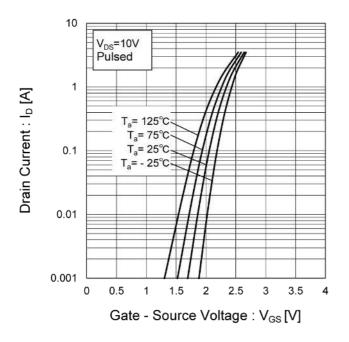


Fig.9 Gate Threshold Voltage vs.
Junction Temperature

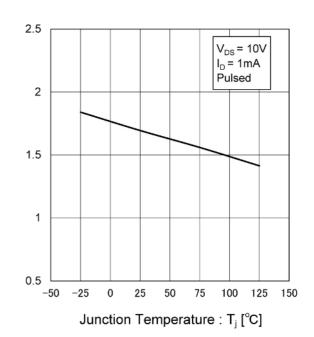
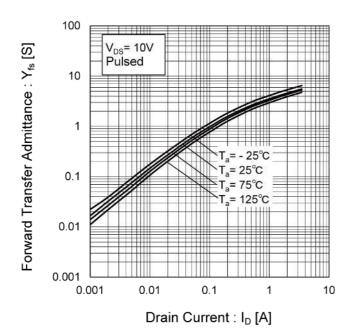


Fig.10 Forward Transfer Admittance vs.
Drain Current



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Gate Threshold Voltage: VGS(th) [V]

Fig.11 Drain Current Derating Curve

120 100 **Drain Current Dissipation** 80 : I<sub>D</sub>/I<sub>D</sub>max. [%] 60 40 20 0 -25 25 50 100 125 150 Junction Temperature :  $T_j$  [°C]

Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage

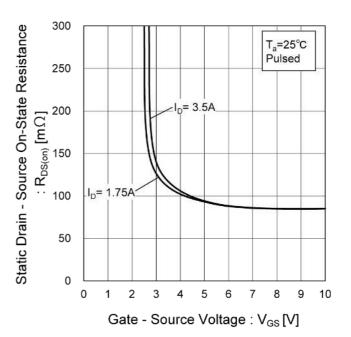


Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature

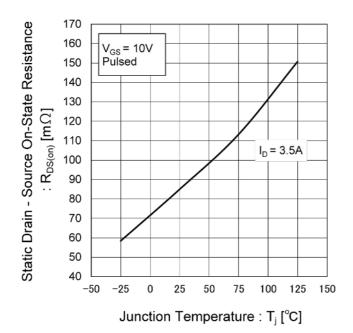


Fig.14 Static Drain - Source On - State
Resistance vs. Drain Current (I)

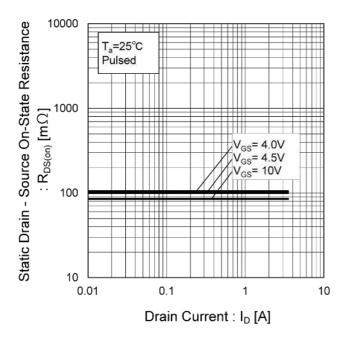


Fig.15 Static Drain - Source On - State Resistance vs. Drain Current (II)

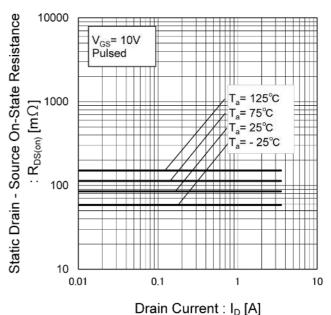


Fig.16 Static Drain - Source On - State Resistance vs. Drain Current (III)

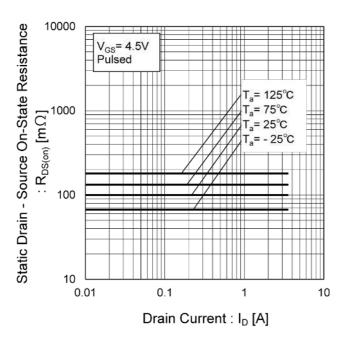


Fig.17 Static Drain - Source On - State
Resistance vs. Drain Current (IV)

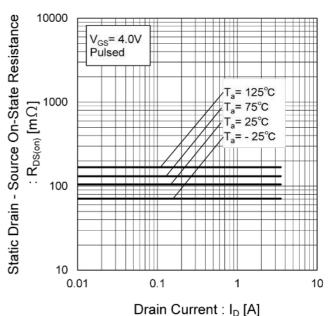


Fig.18 Typical Capacitances vs.

Drain - Source Voltage

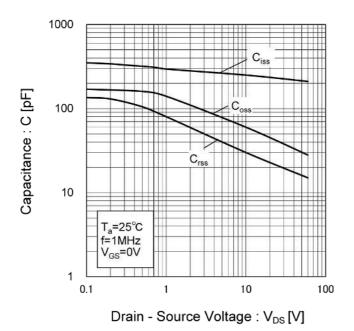


Fig.19 Switching Characteristics

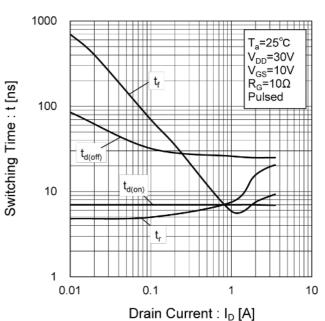


Fig.20 Typical Gate Charge

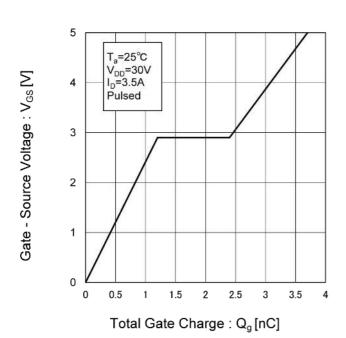
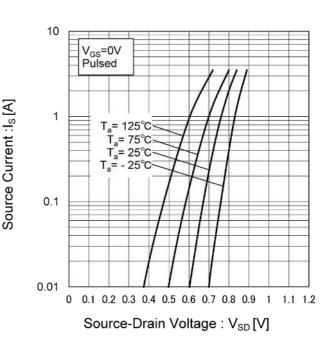


Fig.21 Source Current vs.

Source Drain Voltage



## • Measurement circuits < It is the same for the Tr1 and Tr2>

Fig.1-1 Switching Time Measurement Circuit

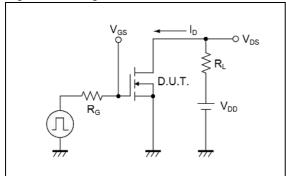


Fig.2-1 Gate Charge Measurement Circuit

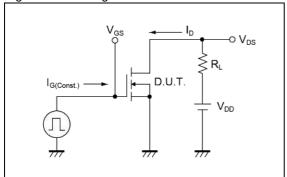


Fig.1-2 Switching Waveforms

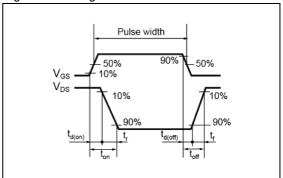
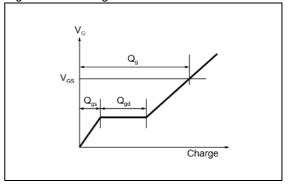
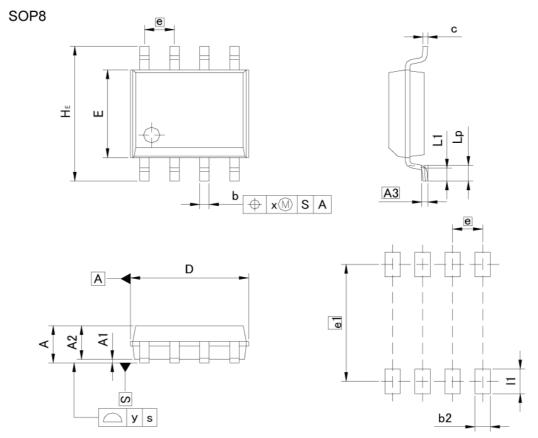


Fig.2-2 Gate Charge Waveform



## Dimensions



Pattern of terminal position areas [Not a pattern of soldering pads]

DIM	MILIM	ETERS	INC	HES
DIM	MIN	MAX	MIN	MAX
Α	=0	1.75	-	0.069
A1	0.	15	0.0	06
A2	1.40	1.60	0.055	0.063
A3	0.	25	0.0	10
b	0.30	0.50	0.012	0.020
С	0.10	0.30	0.004	0.012
D	4.80	5.20	0.189	0.205
E	3.75	4.05	0.148	0.159
е	1.	27	0.050	
HE	5.70	6.30	0.224	0.248
L1	0.40	0.60	0.016	0.024
Lp	0.65	0.85	0.026	0.033
x	0.	15	0.006 0.004	
У	0.	10		
DIM	MILIMETERS		INC	HES
DIM	MIN	MAX	MIN	MAX

0.65

1.15

5.15

Dimension in mm/inches

b2

e1



0.026

0.045

0.203

# **Notice**

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1. If you intend to use our Products in devices requiring extremely high reliability (such as medical equipment (Note 1), aircraft/spacecraft, nuclear power controllers, etc.) and whose malfunction or failure may cause loss of human life, bodily injury or serious damage to property ("Specific Applications"), please consult with the ROHM sales representative in advance. Unless otherwise agreed in writing by ROHM in advance, ROHM shall not be in any way responsible or liable for any damages, expenses or losses incurred by you or third parties arising from the use of any ROHM's Products for Specific Applications.

(Note1) Medical Equipment Classification of the Specific Applications

( )			
JAPAN	USA	EU	CHINA
CLASSⅢ	CLASSⅢ	CLASSIIb	CLASSⅢ
CLASSIV	CLASSIII	CLASSⅢ	CLASSIII

- 2. ROHM designs and manufactures its Products subject to strict quality control system. However, semiconductor products can fail or malfunction at a certain rate. Please be sure to implement, at your own responsibilities, adequate safety measures including but not limited to fail-safe design against the physical injury, damage to any property, which a failure or malfunction of our Products may cause. The following are examples of safety measures:
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  - [b] Installation of redundant circuits to reduce the impact of single or multiple circuit failure
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  - [b] Use of our Products outdoors or in places where the Products are exposed to direct sunlight or dust
  - [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl<sub>2</sub>, H<sub>2</sub>S, NH<sub>3</sub>, SO<sub>2</sub>, and NO<sub>2</sub>
  - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
  - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
  - [f] Sealing or coating our Products with resin or other coating materials
  - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
  - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

#### **Precaution for Mounting / Circuit board design**

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

#### **Precautions Regarding Application Examples and External Circuits**

- 1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
- 2. You agree that application notes, reference designs, and associated data and information contained in this document are presented only as guidance for Products use. Therefore, in case you use such information, you are solely responsible for it and you must exercise your own independent verification and judgment in the use of such information contained in this document. ROHM shall not be in any way responsible or liable for any damages, expenses or losses incurred by you or third parties arising from the use of such information.

#### **Precaution for Electrostatic**

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

#### **Precaution for Storage / Transportation**

- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
  - [a] the Products are exposed to sea winds or corrosive gases, including Cl2, H2S, NH3, SO2, and NO2
  - [b] the temperature or humidity exceeds those recommended by ROHM
  - [c] the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
  may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
  exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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