



DMN2075UDW

N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

| V(BR)DSS | R _{DS(on)} max | Ι _D Τ _A = 25°C |
|----------|---|--|
| 20V | 48mΩ @ V_{GS} = 4.5V | 2.8A |
| 201 | $59 { m m} \Omega$ @ V _{GS} = 2.5V | 2.6A |

Description and Applications

This new generation MOSFET has been designed to minimize the onstate resistance ($R_{DS(on)}$) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- DC-DC Converters
- Power management functions

Benefit and Features

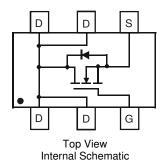
- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Lead Free By Design/RoHS Compliant (Note 1)
- "Green" Device (Note 2)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

- Case: SOT363
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin annealed over Alloy42 leadframe. Solderable per MIL-STD-202, Method 208
- Terminals Connections: See Diagram Below
- Weight: 0.006 grams (approximate)



Top View



Ordering Information (Note 3)

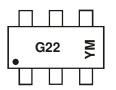
| Part Number | Case | Packaging |
|--------------|--------|------------------|
| DMN2075UDW-7 | SOT363 | 3000/Tape & Reel |

Notes: 1. No purposefully added lead.

2. Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com

3. For packaging details, go to our website at http://www.diodes.com

Marking Information



G22 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: Y = 2011) M = Month (ex: 9 = September)

Date Code Key

| Year | 201 | 1 | 2012 | | 2013 | 20 | 14 | 2015 | | 2016 | 2 | 2017 |
|-------|-----|-----|------|-----|------|-----|-----|------|-----|------|-----|------|
| Code | Y | | Z | | А | | 3 | С | | D | | E |
| Month | Jan | Feb | Mar | Apr | Мау | Jun | Jul | Aug | Sep | Oct | Nov | Dec |
| Code | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | 0 | N | D |



Maximum Ratings @T_A = 25°C unless otherwise specified

| Characteristic | Symbol | Value | Units | | |
|--|-----------------|--|------------------|------------|---|
| Drain-Source Voltage | | | V _{DSS} | 20 | V |
| Gate-Source Voltage | | | V _{GSS} | ±8V | V |
| | Steady State | T _A = 25°C T _A = 70°C | ID | 2.8 2.2 | А |
| Continuous Drain Current (Note 5) V_{GS} = 4.5V | t<5s | T _A = 25°C T _A = 70°C | ID | 3.1 2.5 | А |
| | Steady State | T _A = 25°C T _A = 70°C | ID | 2.6 2.1 | А |
| Continuous Drain Current (Note 5) $V_{GS} = 2.5V$ | t<5s | T _A = 25°C T _A = 70°C | ID | 2.8 2.2 | А |
| Pulsed Drain Current (10µs pulse, Duty cycle = 1%) | | I _{DM} | 20 | A | |
| Maximum Continuous Body Diode Current | | ls | 1.0 | А | |

Thermal Characteristics

| Characteristic | | Symbol | Value | Units |
|--|--------------|----------------------------------|-------------|-------|
| Total Power Dissipation (Note 4) | | PD | 0.5 | W |
| Thermal Desistance, Junction to Ambient (Note 4) | Steady state | D | 257 | °C/W |
| Thermal Resistance, Junction to Ambient (Note 4) | t<5s | $R_{	extsf{	heta}JA}$ | 213 | °C/W |
| Total Power Dissipation (Note 5) | | PD | 0.58 | W |
| Thermal Resistance, Junction to Ambient (Note 5) | Steady state | P | 221 | °C/W |
| Thermal Resistance, Junction to Ambient (Note 5) | t<5s | $R_{	extsf{	heta}JA}$ | 183 | °C/W |
| Thermal Resistance, Junction to Case (Note 5) | | $R_{\theta JC}$ | 65 | °C/W |
| Operating and Storage Temperature Range | | T _{J,} T _{STG} | -55 to +150 | °C |

Electrical Characteristics @TA = 25°C unless otherwise specified

| Obevestevistic | Cumphial | Min | T | Max | Unit | Test Osndition |
|---|----------------------|-----|----------|------|-------|--|
| | Symbol | Min | Тур | Max | Unit | Test Condition |
| OFF CHARACTERISTICS (Note 5) | | | r | | | |
| Drain-Source Breakdown Voltage | BV _{DSS} | 20 | - | - | V | $V_{GS} = 0V, I_D = 250 \mu A$ |
| Zero Gate Voltage Drain Current $T_J = 25^{\circ}C$ | I _{DSS} | - | - | 1.0 | μA | $V_{DS} = 20V, V_{GS} = 0V$ |
| Gate-Source Leakage | Igss | - | - | ±100 | nA | $V_{GS} = \pm 8V, V_{DS} = 0V$ |
| ON CHARACTERISTICS (Note 5) | | | | | | |
| Gate Threshold Voltage | V _{GS(th)} | 0.4 | - | 1.0 | V | $V_{DS} = V_{GS}, I_D = 250 \mu A$ |
| | | - | 40 | 48 | | $V_{GS} = 4.5V, I_D = 3A$ |
| Static Drain-Source On-Resistance | Б | - | 45 | 59 | mΩ | $V_{GS} = 2.5V, I_D = 2A$ |
| Static Drain-Source On-Resistance | R _{DS (ON)} | - | 51 | 70 | 11122 | $V_{GS} = 1.8V, I_D = 1A$ |
| | | - | 68 | 100 | | V _{GS} = 1.5V, I _D = 1A |
| Forward Transfer Admittance | Y _{fs} | - | 13 | - | S | $V_{DS} = 5V, I_D = 3A$ |
| Diode Forward Voltage | V _{SD} | - | 0.75 | 1.0 | V | $V_{GS} = 0V, I_{S} = 1A$ |
| DYNAMIC CHARACTERISTICS (Note 6) | | | | | | |
| Input Capacitance | Ciss | - | 594.3 | - | pF | |
| Output Capacitance | C _{oss} | - | 64.5 | - | pF | −V _{DS} = 10V, V _{GS} = 0V, −f = 1.0MHz |
| Reverse Transfer Capacitance | C _{rss} | - | 57.7 | - | pF | 1 = 1.00012 |
| Gate Resistance | Rg | - | 1.5 | - | Ω | $V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$ |
| Total Gate Charge | Qg | - | 7.0 | - | nC | |
| Gate-Source Charge | Q _{gs} | - | 0.9 | - | nC | $V_{GS} = 4.5V, V_{DS} = 10V,$ |
| Gate-Drain Charge | Q _{gd} | - | 1.4 | - | nC | $-I_{D} = 3.6A$ |
| Turn-On Delay Time | t _{D(on)} | - | 7.4 | - | ns | |
| Turn-On Rise Time | tr | - | 9.8 | - | ns | $V_{DD} = 10V, V_{GS} = 4.5V,$ |
| Turn-Off Delay Time | t _{D(off)} | - | 28.1 | - | ns | $R_L = 2.78\Omega, R_G = 1.0\Omega$ |
| Turn-Off Fall Time | t _f | - | 6.7 | - | ns | |

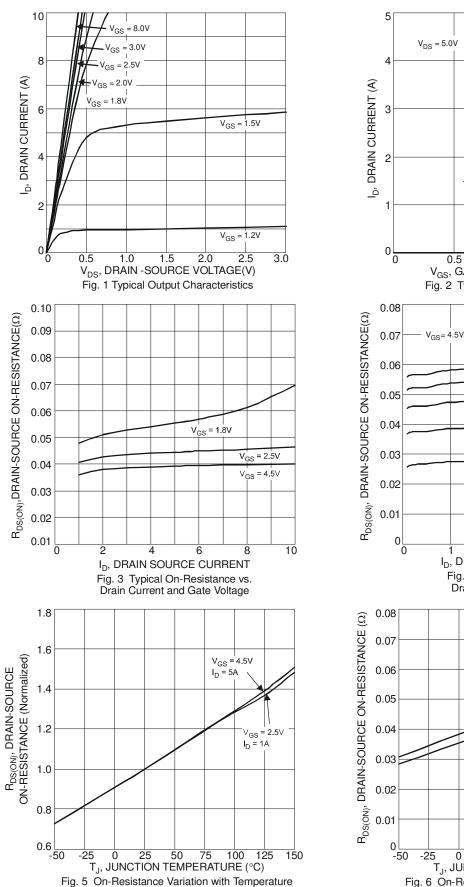
4. Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided. Notes:

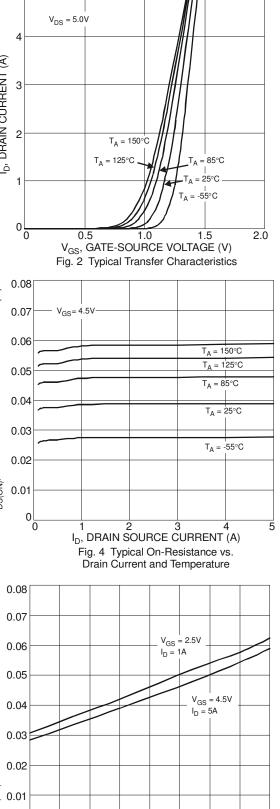
5. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper pad layout

Short duration pulse test used to minimize self-heating effect
 Guaranteed by design. Not subject to production testing.

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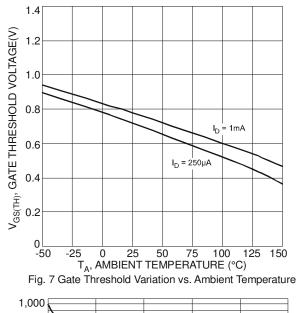


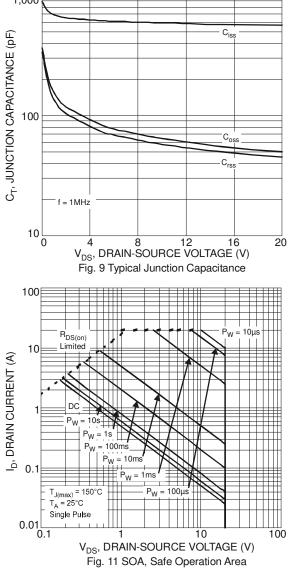
5 0 25 50 75 100 12 T_J, JUNCTION TEMPERATURE (°C) Fig. 6 On-Resistance Variation with Temperature

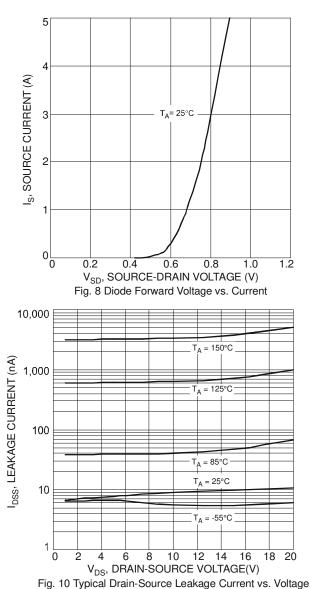
150

125

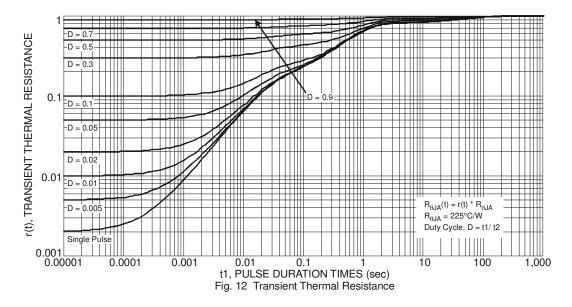




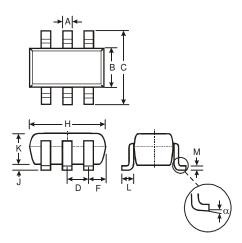






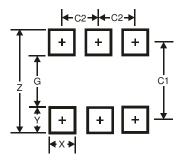


Package Outline Dimensions



| | SOT363 | | | | | | |
|--------|-----------|-------|--|--|--|--|--|
| Dim | Min | Max | | | | | |
| Α | 0.10 | 0.30 | | | | | |
| В | 1.15 | 1.35 | | | | | |
| С | 2.00 | 2.20 | | | | | |
| D | 0.65 | Тур | | | | | |
| F | 0.40 | 0.45 | | | | | |
| Н | 1.80 2.20 | | | | | | |
| J | 0 0.10 | | | | | | |
| К | 0.90 1.00 | | | | | | |
| L | 0.25 0.40 | | | | | | |
| М | 0.10 0.22 | | | | | | |
| α | 0° | 8° | | | | | |
| All Di | mensions | in mm | | | | | |

Suggested Pad Layout



| Dimensions | Value (in mm) |
|------------|---------------|
| Z | 2.5 |
| G | 1.3 |
| Х | 0.42 |
| Y | 0.6 |
| C1 | 1.9 |
| C2 | 0.65 |



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