

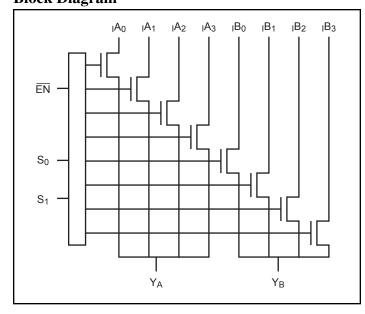
PI3DBV14

Low Voltage, High Bandwidth, Video, 4:1 Mux/DeMux, *NanoSwitch*™ with Single Enable

Features

- · Near-Zero propagation delay
- 5Ω switches connect inputs to outputs
- High signal passing bandwidth (300MHz)
- Beyond Rail-to-Rail switching
- 5V I/O tolerant with 3.3V supply
- 2.5V and 3.3V supply voltage operation
- Hot insertion capable
- Industrial operating temperature: -40°C to +85°C
- 2KV ESD Protection (human body model)
- Latch-up performance >250mA per JESD17
- Packaging (Pb-free & Green available):
 - 16-pin 150 mil wide plastic QSOP (Q)
 - 16-pin 173 mil wide plastic TSSOP (L)
 - 20-contact TQFN (ZH)

Block Diagram



Pin Description

Pin Name	Description
IAN, IBN	Data Inputs
S ₀₋₁	Select Inputs
ĒN	Enable
Y _A to Y _B	Data Outputs
GND	Ground
V_{CC}	Power

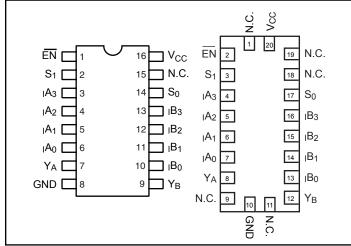
Description

Pericom Semiconductor's PI3DBV series of switch circuits are produced using the Company's advanced sub-micron CMOS technology, achieving industry leading performance.

The PI3DBV14 is a 2-channel, 4:1 Multiplexer/Demultiplexer with 3-state outputs. The switch introduces no additional ground bounce noise or propagation delay.

The PI3DBV1device is very useful in switching differential video signals and others that have high bandwidth (300MHz).

Pin Configuration



Notes:

1. N.C. = No internal connection

Truth Table⁽¹⁾

Enable	Sel	lect		
ĒΝ	S_1	S_0	Y	Function
Н	X	X	Hi-Z	Disable
L	L	L	10	S1-0 = 0
L	L	Н	I1	S1-0 = 1
L	Н	L	I2	S1-0=2
L	Н	Н	I3	S1-0=3

Notes:

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1. H=High Voltage Level; L=Low Voltage Level







Maximum Ratings

(Above which the useful life may be impaired. For user guidelines, not tested.)

Storage Temperature65°C to +150°C
Ambient Temperature with Power Applied –40°C to +85°C
Supply Voltage to Ground Potential0.5V to +4.6V
DC Input Voltage0.5V to +6.0V
DC Output Current
Power Dissipation

Note:

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

DC Electrical Characteristics, 3.3V Supply (Over the Operating Range, TA = -40° C to $+85^{\circ}$ C, VCC = $3.3V \pm 10\%$)

Parameters	Description	Test Conditions ⁽¹⁾	Min.	Typ ⁽²⁾	Max.	Units
V_{IH}	Input HIGH Voltage	Guaranteed Logic HIGH Level	2.0			
$V_{\rm IL}$	Input LOW Voltage	Guaranteed Logic LOW Level	-0.5		0.8	V
V_{IK}	Clamp Diode Voltage	V_{CC} = Min., I_{IN} = -18 mA		-1.3	-1.8	
I_{IH}	Input HIGH Current	$V_{CC} = Max., V_{IN} = V_{CC}$			±1	
${ m I}_{ m IL}$	Input LOW Current	$V_{CC} = Max., V_{IN} = GND$			±1	μΑ
I_{OZH}	High Impedance Output Current	$0 \le Y$, $In \le V_{CC}$			±1	
R _{ON}	Switch On Resistance ⁽⁴⁾	$V_{CC} = Min., V_{IN} = 0.0V,$ $I_{ON} = 48 \text{ mA or } -64\text{mA}$		4	6	Ω
014	2	$V_{CC} = Min., V_{IN} = 3.6V, I_{ON} = -15 \text{ mA}$		5	8	

DC Electrical Characteristics, 2.5V Supply (Over Operating Range, $T_A = -40^{\circ}\text{C}$ to $+85^{\circ}\text{C}$, $V_{CC} = 2.5\text{V} \pm 10\%$)

Parameters ⁽⁵⁾	Description	Test Conditions	Min.	Тур.	Max.	Units
V_{IH}	Input HIGH Voltage	Guaranteed Logic HIGH Level	1.8	_	$V_{CC} + 0.3$	
V_{IL}	Inout LOW Voltage	Guaranteed Logic LOW Level	-0.3	_	0.8	V
V_{IK}	Clamp Diode Voltage	$V_{CC} = Max., I_{IN} = -6mA$	_	-0.7	-1.8	
I_{IH}	Input HIGH Current	$V_{CC} = Max., V_{IN} = V_{CC}$	_	_	±1	
IIL	Input LOW Current	$V_{CC} = Max., V_{IN} = GND$	_	_	±1	μΑ
I _{OZH}	High Impedance Current ⁽³⁾	$0 \le Y$, In $\le V_{CC}$	_	_	±1	
Day	Switch On Resistance ⁽⁴⁾	$V_{CC} = Min., V_{IN} = 0.0V,$ $I_{ON} = -48mA$	-	6	8	Ω
R _{ON}	Switch On Resistance	$V_{CC} = Min., V_{IN} = 2.25V,$ $I_{ON} = -15mA$	_	7	14	22

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PI3DBV14 Low Voltage, High Bandwidth, Video, 4:1 Mux/DeMux, *NanoSwitch*™ with Single Enable

Capacitance $(T_A = 25^{\circ}C \text{ f} = 1 \text{ MHz})$

Parameters ⁽⁵⁾	Description	Test Conditions	Тур.	Units
C_{IN}	Input Capacitance		3.0	
$C_{OFF(IN)}$	In Capacitance, Switch Off		3.5	ъE
$C_{OFF(Y)}$	Y Capacitance, Switch Off	$V_{IN} = 0V$	12	pF
C _{ON}	Y/In Capacitance, Switch On		15.0	

Notes:

- 1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
- 2. Typical values are at $V_{CC} = 3.3V$, $T_A = 25$ °C ambient and maximum loading.
- 3. Not more than one output should be shorted at one time. Duration of the test should not exceed one second.
- 4. Measured by the voltage drop between Y and In pin at indicated current through the switch. On-Resistance is determined by the lower of the voltages on the two (Y, In) pins.
- 5. This parameter is determined by device characterization but is not production tested.

Power Supply Characteristics

Parameters	Description	Test Conditions ⁽¹⁾	Min.	Typ. ⁽²⁾	Max.	Units
I_{CC}	Quiescent Power Supply Current	$V_{CC} = 3.6V$, $V_{IN} = GND$ or V_{CC}			0.8	mA

Notes:

- 1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device.
- 2. Typical values are at $V_{CC} = 3.3V$, +25°C ambient.
- Per LVTTL driven input (control inputs only); Y and In pins do not contribute to I_{CC}. This parameter is guaranteed, but not production tested.

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Low Voltage, High Bandwidth, Video, 4:1 Mux/DeMux, *NanoSwitch*™ with Single Enable

Dynamic Electrical Characteristics Over the Operating Range ($T_A = -40^{\circ}$ to $+85^{\circ}$, $V_{CC} = 3.3 \text{V} \pm 10\%$)

Parameter	Description	Test Condition	Min.	Тур.	Max.	Units
X _{TALK}	Crosstalk	See Test Diagram		-60		dB
O_{IRR}	Off-Isolation	See Test Diagram		-60		uБ
BW	-3dB Bandwidth	See Test Diagram	200	300		MHz

Switching Characteristics over 3.3V Operating Range

Parameters Description		Conditions ⁽¹⁾	Com.			Units
Farameters	rarameters Description		Min.	Тур.	Max.	Units
t _{PLH} t _{PHL}	Propogation Delay ^(2,3) Y to In, In to Y	See Test Diagram			0.3	
t _{PZH} t _{PZL}	Enable Time S or \overline{EN} to Y or In	See Test Diagram	1.5		9.0	ns
$t_{ m PHZ} \ t_{ m PLZ}$	Disable Time S or EN to Y or In	See Test Diagram	1.5		9.0	

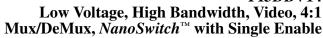
Switching Characteristics over 2.5V Operating Range

Danamatana	Description	Conditions ⁽¹⁾	Co	I Inita	
Parameters	Description	Conditions	Min.	Max.	Units
t _{PLH} t _{PHL}	Propogation Delay ^(2,3) Y to In, In to Y	See Test Diagram		0.3	
t _{PZH} t _{PZL}	Enable Time S or \overline{EN} to Y or In	Con Toot Dispuss	1.5	15.0	ns
t _{PHZ} t _{PLZ}	Disable Time S or \overline{EN} to Y or In	See Test Diagram	1.5	12.0	

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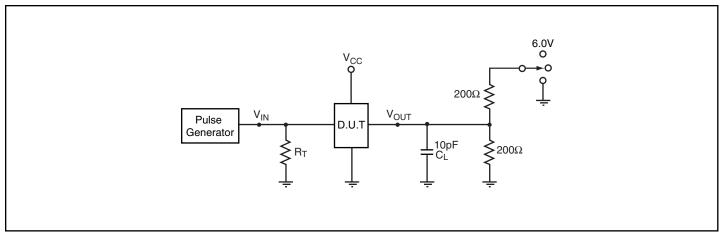
Notes:

- See test circuit and waveforms.
- 2. This parameter is guaranteed but not tested on Propagation Delays.
- 3. The switch contributes no propagational delay other than the RC delay of the On-Resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.30ns for 10pF load. Since this time constant is much smaller than the rise/fall times of typical driving signals, it adds very little propagational delay to the system. Propagational delay of the switch when used in a system is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.





Test Circuit for Electrical Characteristics⁽¹⁾



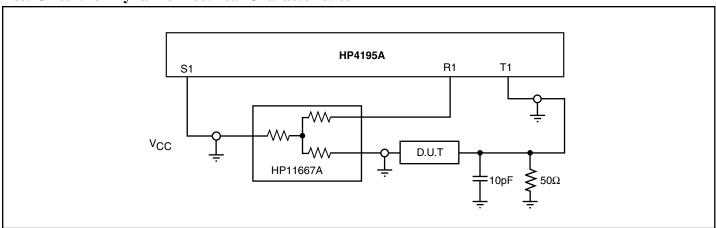
Notes:

- C_L = Load capacitance: includes jig and probe capacitance.
- 2. R_T = Termination resistance: should be equal to Z_{OUT} of the Pulse Generator
- 3. Waveform 1 is for an output with internal conditions such that the output is low except when disabled by the output control. Waveform 2 is for an output with internal conditions such that the output is high except when disabled by the output control.
- 4. All input impulses are supplied by generators having the following characteristics: $PRR \le MHz$, $Z_O = 50\Omega$, $t_R \le 2.5 ns$, $t_F \le 2.5 ns$.
- 5. The outputs are measured one at a time with one transition per measurement.

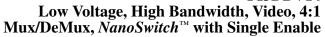
Switch Positions

Test	Switch
t _{PLZ} , t _{PZL}	6.0V
t _{PHZ} , t _{PZH}	GND
Prop Delay	Open

Test Circuit for Dynamic Electrical Characteristics

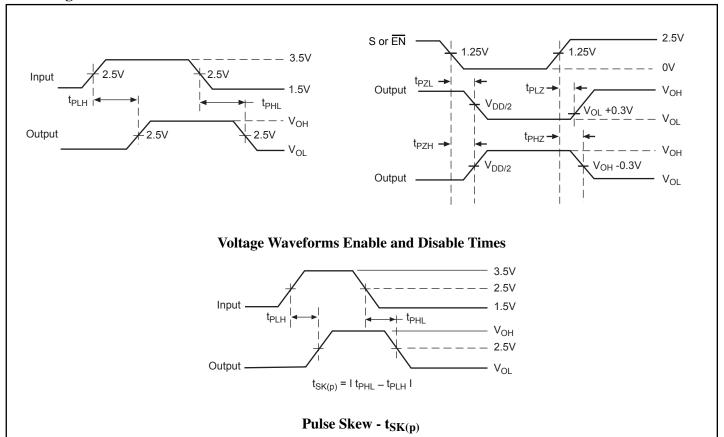


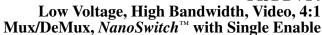
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Switching Waveforms







Applications Information

Logic Inputs

The logic control inputs can be driven upto 3.6Vregardless of the supply voltage. For example, given a +3.3V supply, $\overline{\text{EN}}$ maybe driven LOW to 0V and HIGH to 3.6V. Driving $\overline{\text{EN}}$ Rail-to-Rail[®] minimizes power consumption.

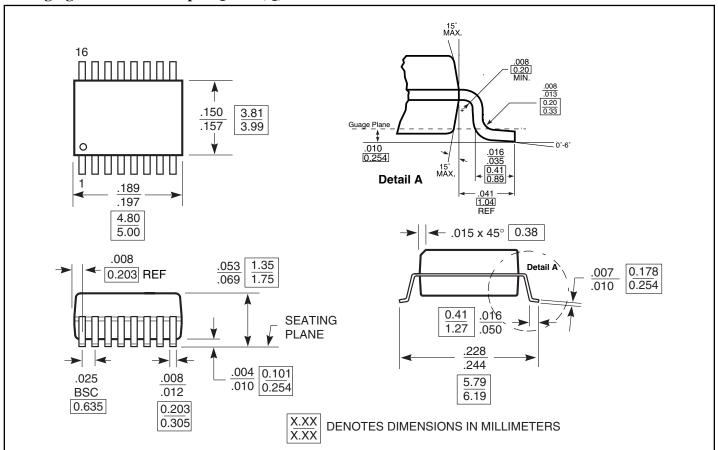
Power Supply-Sequencing

Proper power supply sequencing is recommended for all CMOS devices. Always apply V_{CC} before applying signals to the input/output or control pins.

Hot Insertion

For Datacom and Telecom applications that have ten or more volts passing through the backplane, a high voltage from the power supply may be seen at the device input pins during hot insertion. The PI3CHxx devices have maximum limits of 6V and 120mA for 20ns. If the power is higher or applied for a longer time or repeatedly reaches the maximum limits, the devices can be damaged.

Packaging Mechanical: 16-pin QSOP (Q)

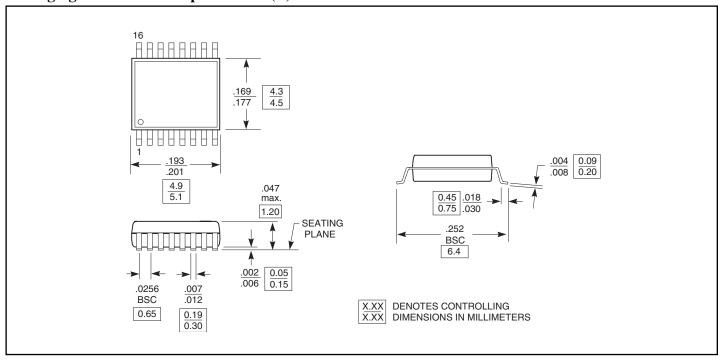


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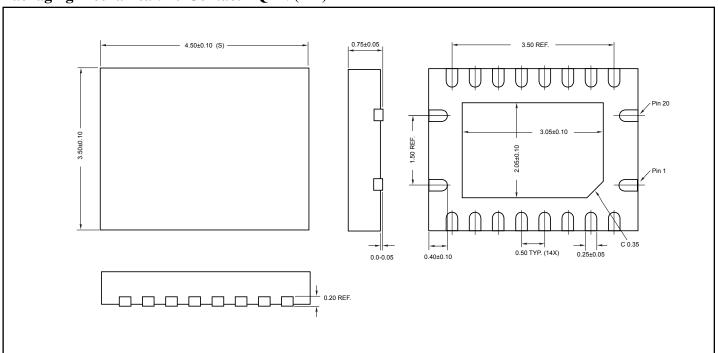




Packaging Mechanical: 16-pin TSSOP (L)



Packaging Mechanical: 20-Contact TQFN (ZH)



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PI3DBV14 Low Voltage, High Bandwidth, Video, 4:1 Mux/DeMux, $NanoSwitch^{\text{TM}}$ with Single Enable

Ordering Information

Ordering Code	Package Code	Package Description
PI3DBV14Q	Q	150-mil, 16-pin QSOP
PI3DBV14QE	Q	Pb-free & Green, 150-mil, 16-pin QSOP
PI3DBV141L	L	173-mil, 16-pin TSSOP
PI3DBV14LE	L	Pb-free & Green, 173-mil, 16-pin TSSOP
PI3DBV14ZHE	E	Pb-free & Green, 20-Contact TQFN

Notes:

- Thermal characteristics can be found on the company web site at www.pericom.com/packaging/
- Number of Transistors = TBD

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