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April 1st, 2010 Renesas Electronics Corporation

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MOS FIELD EFFECT TRANSISTOR

2SK2515

SWITCHING N-CHANNEL POWER MOS FET

DESCRIPTION

The 2SK2515 is N-Channel MOS Field Effect Transistors designed for high current switching applications.

FEATURES

ABSOLUTE MAXIMUM RATINGS (TA = 25 °C)

 FEATURES Super Low on-state resistance R_{DS} (on)1 = 9.0 mΩ MAX. (V_{GS} = 10 V, I_D = 25 A) R_{DS} (on)2 = 14 mΩ MAX. (V_{GS} = 4 V, I_D = 25 A) Low input capacitance C_{iss} = 3 400 pF TYP. Built-in G-S Protection Diode 								
ABSOLUTE MAXIMUM RATINGS (TA = 25 °C)								
Drain to Source Voltage (Vgs = 0 V)	VDSS	60	V					
Gate to Source Voltage (VDS = 0 V)	Vgss	<u>±</u> 20	V					
Drain Current (DC)	ID (DC)	₩ ±50	Α					
Drain Current (pulse)*	D (pulse)	±200	Α					
Total Power Dissipation (Tc = 25 °C)	PT1	150	W					
Total Power Dissipation (T _A = 25 °C)	PT2	3.0	W					
Channel Temperature	Tch	150	°C					
Storage Temperature	Tstg	-55 to +150	°C					
Single Avalanche Current**	I _{AS}	50	Α					
Single Avalanche Energy**	Eas	250	mJ					

PW \leq 10 μ s, Duty Cycle \leq 1 %

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Starting T_{ch} = 25 °C, R_G = 25 Ω , V_{GS} = 20 V \rightarrow 0

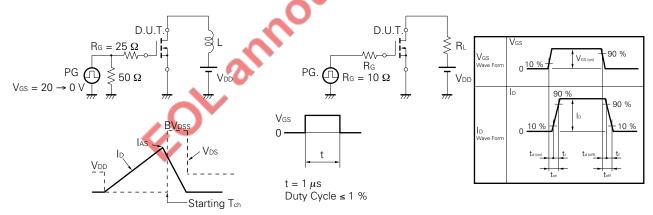


ELECTRICAL CHARACTERISTICS (TA = 25 °C)

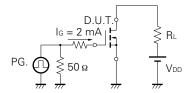
CHARACTERISTIC	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITIONS
Drain to Source On-Resistance	RDS (on)1		7.3	9.0	mΩ	Vgs = 10 V, ID = 25 A
Drain to Source On-Resistance	RDS (on)2		11	14	mΩ	Vgs = 4 V, ID = 25 A
Gate to Source Cutoff Voltage	VGS (off)	1.0	1.5	2.0	V	V _{DS} = 10 V, I _D = 1 mA
Forward Transfer Admittance	yfs	20	58		S	V _{DS} = 10 V, I _D = 25 A
Drain Leakage Current	IDSS			10	μΑ	VDS = VDSS, VGS = 0
Gate to Source Leakage Current	Igss			±10	μΑ	Vgs = ±20 V, Vps = 0
Input Capacitance	Ciss		3 400		pF	V _{DS} = 10 V
Output Capacitance	Coss		1 600		pF	Vgs = 0
Reverse Transfer Capacitance	Crss		770		pF	f = 1 MHz
Turn-On Delay Time	td (on)		55		ns	ID = 25 A
Rise Time	tr		360		ns	VGS(on) = 10 V
Turn-Off Delay Time	td (off)		480		ns	V _{DD} = 30 V
Fall Time	tf		360		ns	$R_G = 10 \Omega$
Total Gate Charge	QG		152		nC	lo = 50 A
Gate to Source Charge	Qgs		11		nC	V _{DD} = 48 V
Gate to Drain Charge	Q _{GD}		60		nC	Vgs = 10 V
Body Diode Forward Voltage	VF (S-D)		0.92	Y	V	IF = 50 A, VGS = 0
Reverse Recovery Time	trr		105	2	ns	IF = 50 A, VGS = 0
Reverse Recovery Charge	Qrr		265		nC	di/dt = 100 A/μs

Test Circuit 1 Avalanche Capability

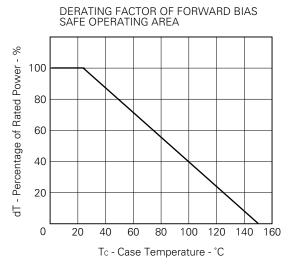
Test Circuit 2 Switching Time

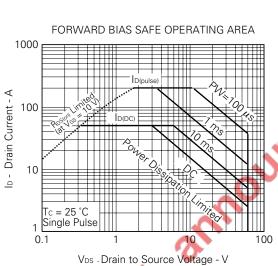


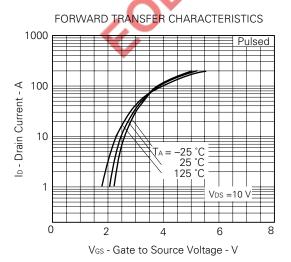
Test Circuit 3 Gate Charge

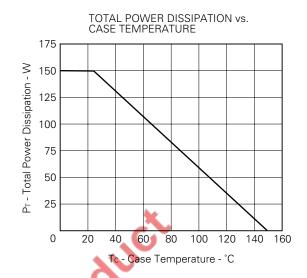


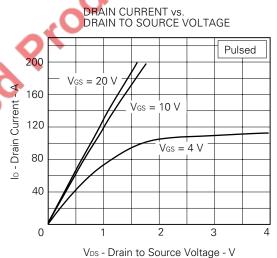
TYPICAL CHARACTERISTICS (TA = 25 °C)



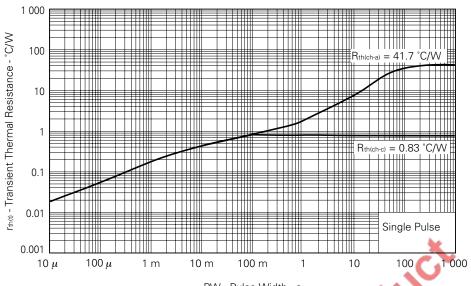






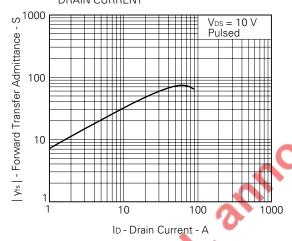


TRANSIENT THERMAL RESISTANCE vs. PULSE WIDTH

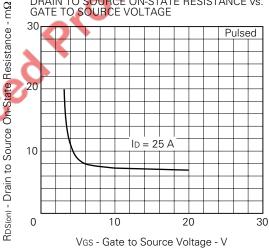


PW - Pulse Width - s

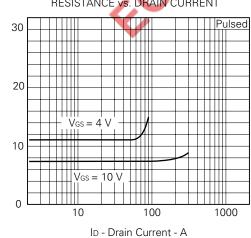
FORWARD TRANSFER ADMITTANCE vs. DRAIN CURRENT



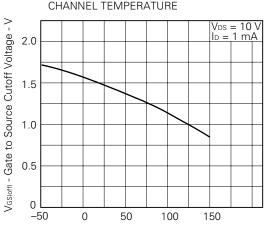




DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT

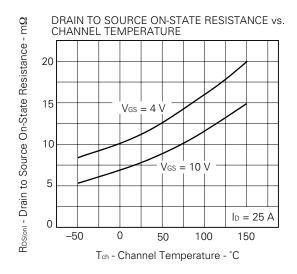


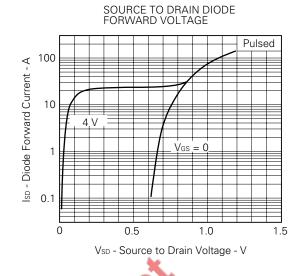
GATE TO SOURCE CUTOFF VOLTAGE vs. CHANNEL TEMPERATURE

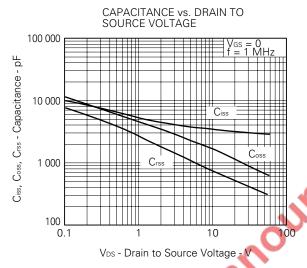


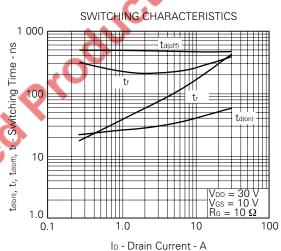
Tch - Channel Temperature - °C

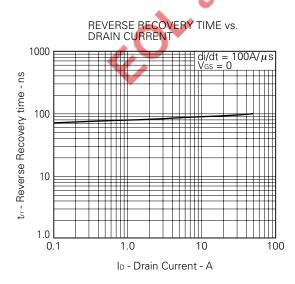
RDS(on) - Drain to Source On-State Resistance - m\Omega

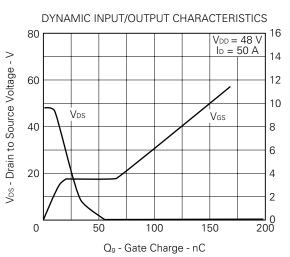


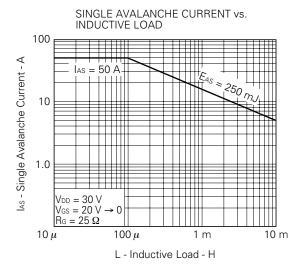


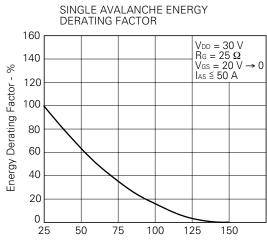












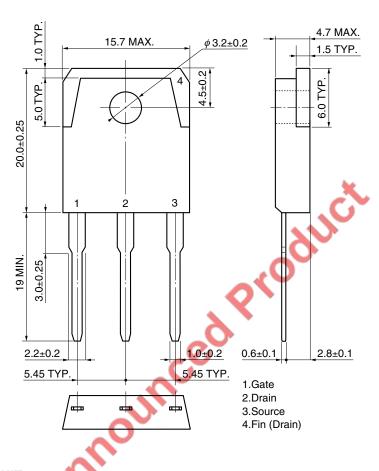
Starting T_{ch} - Starting Channel Temperature - $^{\circ}$ C

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..ng Channe
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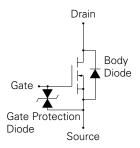
6

PACKAGE DRAWING (Unit: mm)

<R> TO-3P (MP-88)



EQUIVALENT CIRCUIT



Remark The diode connected between the gate and source of the transistor serves as a protector against ESD.

When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

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