BLF6G22-45

Power LDMOS transistor

Rev. 5 — 1 September 2015



1. Product profile

1.1 General description

45 W LDMOS power transistor for base station applications at frequencies from 2000 MHz to 2200 MHz.

Table 1. Typical performance

RF performance at $T_{case} = 25 \ ^{\circ}C$ in a common source class-AB production test circuit.

Mode of operation	f	V _{DS}	P _{L(AV)}	G _p	η _D	ACPR
	(MHz)	(V)	(W)	(dB)	(%)	(dBc)
2-carrier W-CDMA	2110 to 2170	28	2.5	18.5	13	-49 <mark>[1]</mark>

[1] Test signal: 3GPP; test model 1; 64 DPCH; PAR = 7.5 dB at 0.01 % probability on CCDF per carrier; carrier spacing 5 MHz.

1.2 Features and benefits

- Typical 2-carrier W-CDMA performance at frequencies of 2110 MHz and 2170 MHz, a supply voltage of 28 V and an I_{Dq} of 405 mA:
 - Average output power = 2.5 W
 - Power gain = 18.5 dB (typ)
 - Efficiency = 13 %
 - ◆ ACPR = -49 dBc
- Easy power control
- Integrated ESD protection
- Excellent ruggedness
- High efficiency
- Excellent thermal stability
- Designed for broadband operation (2000 MHz to 2200 MHz)
- Internally matched for ease of use
- Compliant to Directive 2002/95/EC, regarding Restriction of Hazardous Substances (RoHS)

1.3 Applications

 RF power amplifiers for W-CDMA base stations and multicarrier applications in the 2000 MHz to 2200 MHz frequency range

2. Pinning information

Pin	Description	S	Simplified outline	Graphic symbol
1	drain			
2	gate			1 لــــا
3	source	<u>[1]</u>		

[1] Connected to flange.

3. Ordering information

Table 3. Ordering information

Type number	Package	Package			
	Name	Description	Version		
BLF6G22-45	-	flanged ceramic package; 2 mounting holes; 2 leads	SOT608A		

4. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V _{DS}	drain-source voltage		-	65	V
V _{GS}	gate-source voltage		-0.5	+13	V
T _{stg}	storage temperature		-65	+150	°C
Tj	junction temperature		-	225	°C

5. Thermal characteristics

Table 5.Thermal characteristics

Symbol	Parameter	Conditions	Тур	Unit
R _{th(j-case)}	thermal resistance from junction to case	T _{case} = 80 °C; P _L = 12.5 W (CW)	1.7	K/W

6. Characteristics

Table 6. Characteristics

 $T_i = 25 \ ^{\circ}C$ per section; unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Мах	Unit
V _{(BR)DSS}	drain-source breakdown voltage	V _{GS} = 0 V; I _D = 0.5 mA	65	-	-	V
V _{GS(th)}	gate-source threshold voltage	V _{DS} = 10 V; I _D = 72 mA	1.4	1.9	2.4	V
V _{GSq}	gate-source quiescent voltage	V _{DS} = 28 V; I _D = 300 mA	1.65	2.15	2.65	V
I _{DSS}	drain leakage current	V _{GS} = 0 V; V _{DS} = 28 V	-	-	1.5	μA
I _{DSX}	drain cut-off current	$V_{GS} = V_{GS(th)} + 3.75 V;$ $V_{DS} = 10 V$	-	12.5	-	A
I _{GSS}	gate leakage current	V _{GS} = 11 V; V _{DS} = 0 V	-	-	150	nA
9 _{fs}	forward transconductance	V _{DS} = 10 V; I _D = 3.5 A	-	5	-	S
R _{DS(on)}	drain-source on-state resistance	$V_{GS} = V_{GS(th)} + 3.75 V;$ I _D = 2.5 A	-	0.2	-	Ω

7. Application information

Table 7. Application information

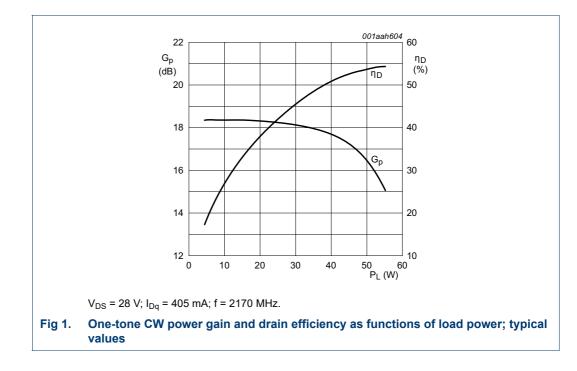
Mode of operation: 2-carrier W-CDMA; PAR 7.5 dB at 0.01 % probability on CCDF; 3GPP test model 1; 1 to 64 PDPCH; $f_1 = 2112.5$ MHz; $f_2 = 2117.5$ MHz; $f_3 = 2162.5$ MHz; $f_4 = 2167.5$ MHz; RF performance at $V_{DS} = 28$ V; $I_{Dq} = 405$ mA; $T_{case} = 25$ °C; unless otherwise specified; in a class-AB production test circuit.

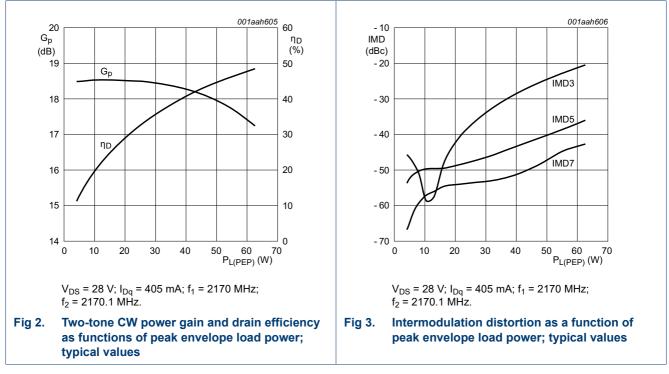
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
P _{L(AV)}	average output power		-	2.5	-	W
G _p	power gain	P _{L(AV)} = 2.5 W	17.3	18.5	19.7	dB
η _D	drain efficiency	P _{L(AV)} = 2.5 W	10.5	13	-	%
ACPR	adjacent channel power ratio	P _{L(AV)} = 2.5 W	-	-49	-45	dBc

7.1 Ruggedness in class-AB operation

The BLF6G22-45 is capable of withstanding a load mismatch corresponding to VSWR = 10 : 1 through all phases under the following conditions: V_{DS} = 28 V; I_{Dq} = 405 mA; P_L = 45 W (CW); f = 2170 MHz.

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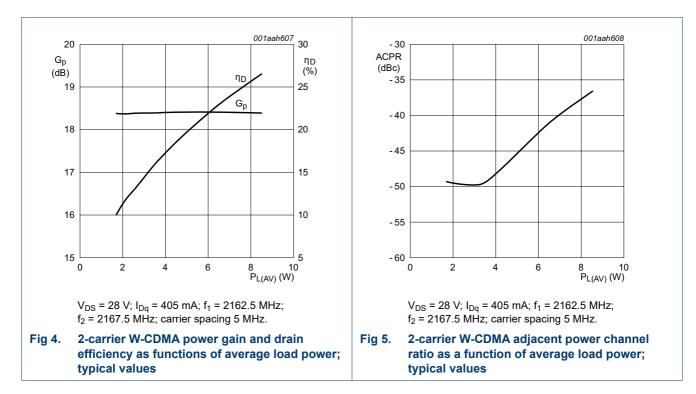




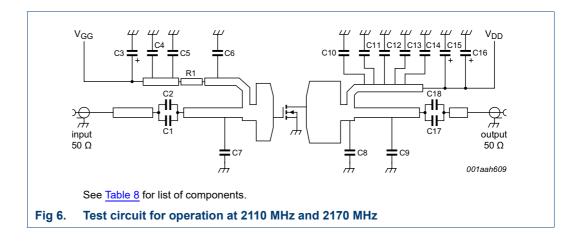
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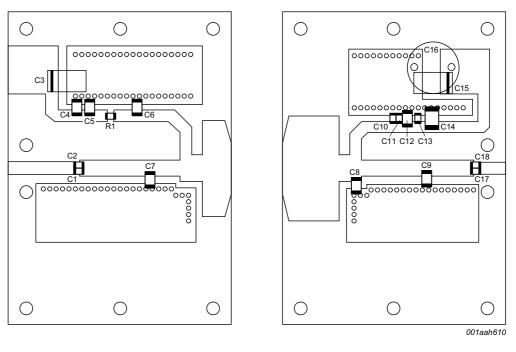
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8. Test information





Striplines are on a double copper-clad Rogers Duroid 5880 Printed-Circuit Board (PCB) with ϵ_r = 2.2 and thickness = 0.79 mm.

See Table 8 for list of components.

Fig 7. Component layout for 2110 MHz and 2170 MHz test circuit

Table 8.List of components

For test circuit, see <u>Figure 6</u> and <u>Figure 7</u>.

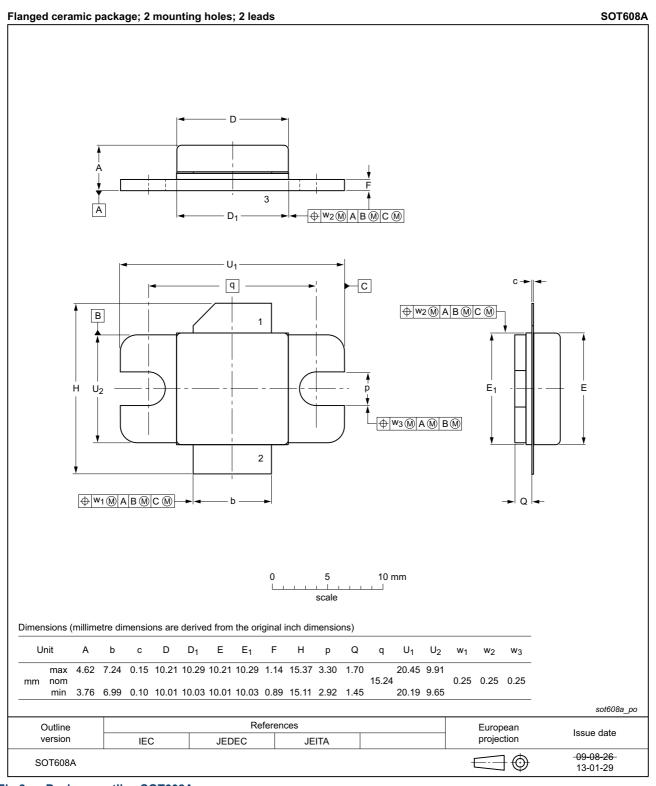
Component	Description	Value	Remarks
C1, C2, C17, C18	multilayer ceramic chip capacitor	6.8 pF	[1]
C3, C15	tantalum capacitor	10 μF	
C4, C5	multilayer ceramic chip capacitor	1.5 μF	
C6, C12	multilayer ceramic chip capacitor	10 pF	[2]
C7	multilayer ceramic chip capacitor	0.5 pF	[2]
C8	multilayer ceramic chip capacitor	1.2 pF	[2]
C9	multilayer ceramic chip capacitor	1.0 pF	[2]
C10, C11	multilayer ceramic chip capacitor	100 nF	
C13	multilayer ceramic chip capacitor	220 nF	
C14	multilayer ceramic chip capacitor	4.7 μF	
C16	electrolytic capacitor	220 μF, 63 V	
R1	chip resistor	5.6 Ω	

[1] American technical ceramics type 100A or capacitor of same quality.

[2] American technical ceramics type 100B or capacitor of same quality.

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Package outline 9.



Package outline SOT608A Fig 8.

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10. Handling information

CAUTION



This device is sensitive to ElectroStatic Discharge (ESD). Observe precautions for handling electrostatic sensitive devices. Such precautions are described in the *ANSI/ESD S20.20*, *IEC/ST 61340-5*, *JESD625-A* or

equivalent standards.

11. Abbreviations

Table 9. Abbreviations				
Acronym	Description			
3GPP	3rd Generation Partnership Project			
CCDF	Complementary Cumulative Distribution Function			
CW	Continuous Waveform			
DPCH	Dedicated Physical CHannel			
ESD	ElectroStatic Discharge			
IMD	InterModulation Distortion			
LDMOS	Laterally Diffused Metal-Oxide Semiconductor			
PAR	Peak-to-Average power Ratio			
PDPCH	transmission Power of the Dedicated Physical CHannel			
RF	Radio Frequency			
VSWR	Voltage Standing-Wave Ratio			
W-CDMA	Wideband Code Division Multiple Access			

12. Revision history

Table 10. Revision history **Document ID** Release date Data sheet status Change notice Supersedes BLF6G22-45#5 20150901 Product data sheet BLF6G22-45 v.4 Modifications: · The format of this document has been redesigned to comply with the new identity guidelines of Ampleon. · Legal texts have been adapted to the new company name where appropriate. BLF6G22-45 v.4 20150326 Product data sheet BLF6G22-45 v.3 BLF6G22-45 v.3 BLF6G22-45 BLF6G22S-45 2 20130311 Product data sheet BLF6G22-45_BLF6G22S-45_2 BLF6G22-45 BLF6G22S-45 1 20080421 Product data sheet BLF6G22-45_BLF6G22S-45_1 20080219 Preliminary data sheet

13. Legal information

13.1 Data sheet status

Document status[1][2]	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

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