



# BTA206X-800CT

## 3Q Hi-Com Triac

Rev. 2 — 19 December 2011

Product data sheet

## 1. Product profile

### 1.1 General description

Planar passivated high commutation three quadrant triac in a SOT186A (TO-220F) "full pack" plastic package intended for use in circuits where high static and dynamic  $dV/dt$  and high  $dI/dt$  can occur. This "series CT" triac will commute the full RMS current at the maximum rated junction temperature ( $T_j = 150\text{ °C}$ ) without the aid of a snubber. It is used where "high junction operating temperature capability" is required.

### 1.2 Features and benefits

- 3Q technology for improved noise immunity
- High commutation capability with maximum false trigger immunity
- High immunity to false turn-on by  $dV/dt$
- High junction operating temperature capability
- High voltage capability
- Isolated mounting base package
- Planar passivated for voltage ruggedness and reliability
- Triggering in three quadrants only

### 1.3 Applications

- Applications subject to high temperature
- Electronic thermostats (heating and cooling)
- Motor controls for home appliances
- Rectifier-fed DC inductive loads e.g. DC motors and solenoids

### 1.4 Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$V_{\text{DRM}}$	repetitive peak off-state voltage		-	-	800	V
$I_{\text{TSM}}$	non-repetitive peak on-state current	full sine wave; $T_{j(\text{init})} = 25\text{ °C}$ ; $t_p = 20\text{ ms}$ ; see <a href="#">Figure 4</a> ; see <a href="#">Figure 5</a>	-	-	60	A
$T_j$	junction temperature		-	-	150	°C
$I_{\text{T(RMS)}}$	RMS on-state current	full sine wave; $T_h \leq 114\text{ °C}$ ; see <a href="#">Figure 1</a> ; see <a href="#">Figure 2</a> ; see <a href="#">Figure 3</a>	-	-	6	A

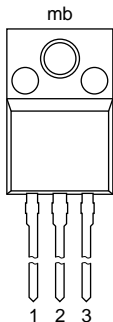
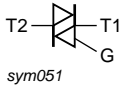


**Table 1. Quick reference data ...continued**

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
<b>Static characteristics</b>						
I <sub>GT</sub>	gate trigger current	V <sub>D</sub> = 12 V; I <sub>T</sub> = 0.1 A; T2+ G+; T <sub>j</sub> = 25 °C; see <a href="#">Figure 7</a>	4	-	35	mA
		V <sub>D</sub> = 12 V; I <sub>T</sub> = 0.1 A; T2+ G-; T <sub>j</sub> = 25 °C; see <a href="#">Figure 7</a>	4	-	35	mA
		V <sub>D</sub> = 12 V; I <sub>T</sub> = 0.1 A; T2- G-; T <sub>j</sub> = 25 °C; see <a href="#">Figure 7</a>	4	-	35	mA
<b>Dynamic characteristics</b>						
dV <sub>D</sub> /dt	rate of rise of off-state voltage	V <sub>DM</sub> = 536 V; T <sub>j</sub> = 150 °C; (V <sub>DM</sub> = 67% of V <sub>DRM</sub> ); exponential waveform; gate open circuit	500	-	-	V/μs
dI <sub>com</sub> /dt	rate of change of commutating current	V <sub>D</sub> = 400 V; T <sub>j</sub> = 150 °C; I <sub>T(RMS)</sub> = 6 A; dV <sub>com</sub> /dt = 20 V/μs; (snubberless condition); gate open circuit	10	-	-	A/ms

## 2. Pinning information

**Table 2. Pinning information**

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	T1	main terminal 1		 sym051
2	T2	main terminal 2		
3	G	gate		
mb	n.c.	mounting base; isolated		

**SOT186A (TO-220F)**

## 3. Ordering information

**Table 3. Ordering information**

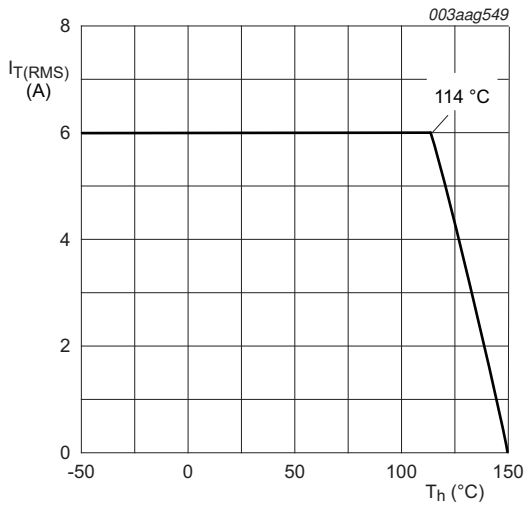
Type number	Package		
	Name	Description	Version
BTA206X-800CT	TO-220F	plastic single-ended package; isolated heatsink mounted; 1 mounting hole; 3-lead TO-220 "full pack"	SOT186A
BTA206X-800CT/L01	TO-220F	plastic single-ended package; isolated heatsink mounted; 1 mounting hole; 3-lead TO-220 "full pack"	SOT186A

### 4. Limiting values

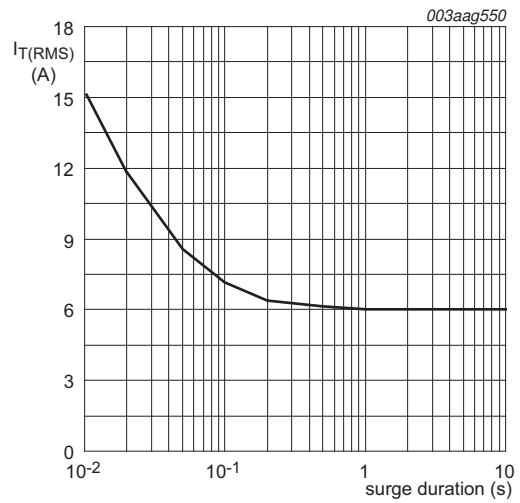
**Table 4. Limiting values**

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
$V_{DRM}$	repetitive peak off-state voltage		-	800	V
$I_{T(RMS)}$	RMS on-state current	full sine wave; $T_h \leq 114\text{ °C}$ ; see <a href="#">Figure 1</a> ; see <a href="#">Figure 2</a> ; see <a href="#">Figure 3</a>	-	6	A
$I_{TSM}$	non-repetitive peak on-state current	full sine wave; $T_{j(\text{init})} = 25\text{ °C}$ ; $t_p = 20\text{ ms}$ ; see <a href="#">Figure 4</a> ; see <a href="#">Figure 5</a>	-	60	A
		full sine wave; $T_{j(\text{init})} = 25\text{ °C}$ ; $t_p = 16.7\text{ ms}$	-	66	A
$I^2t$	$I^2t$ for fusing	$t_p = 10\text{ ms}$ ; sine-wave pulse	-	18	A <sup>2</sup> s
$dl_T/dt$	rate of rise of on-state current	$I_T = 10\text{ A}$ ; $I_G = 0.2\text{ A}$ ; $dl_G/dt = 0.2\text{ A}/\mu\text{s}$	-	100	A/ $\mu\text{s}$
$I_{GM}$	peak gate current		-	2	A
$P_{GM}$	peak gate power		-	5	W
$P_{G(AV)}$	average gate power	over any 20 ms period	-	0.5	W
$T_{stg}$	storage temperature		-40	150	°C
$T_j$	junction temperature		-	150	°C



**Fig 1. RMS on-state current as a function of heatsink temperature; maximum values**



$f = 50\text{ Hz}$ ;  $T_h = 114\text{ °C}$

**Fig 2. RMS on-state current as a function of surge duration; maximum values**

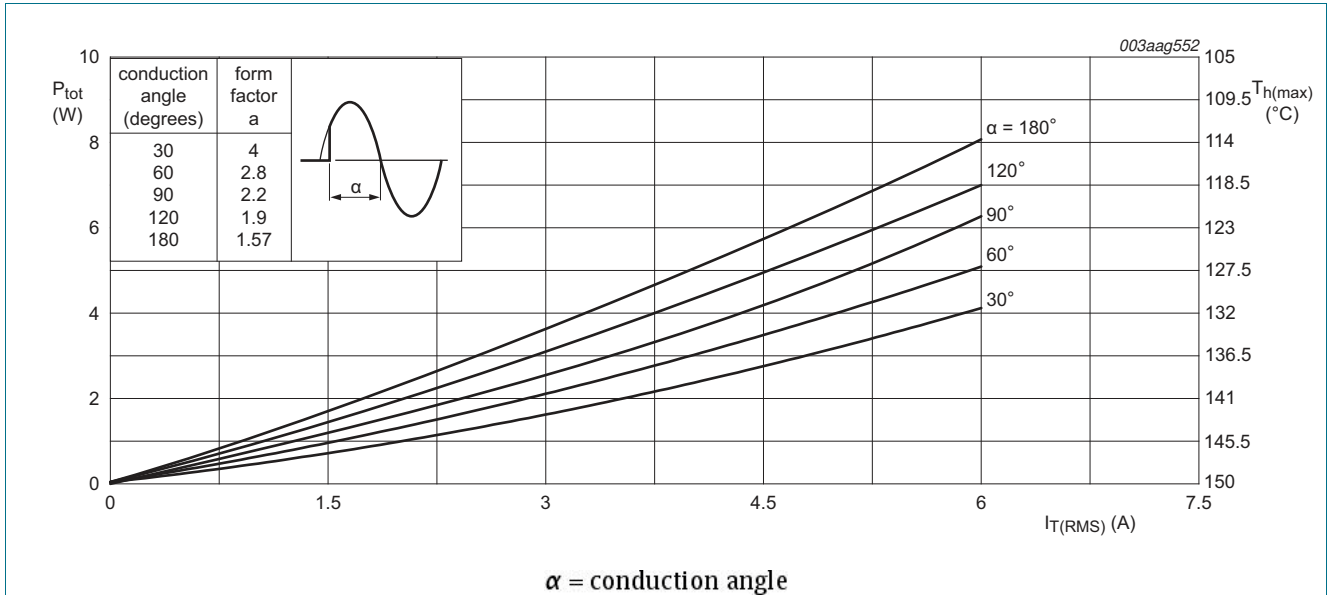


Fig 3. Total power dissipation as a function of RMS on-state current; maximum values

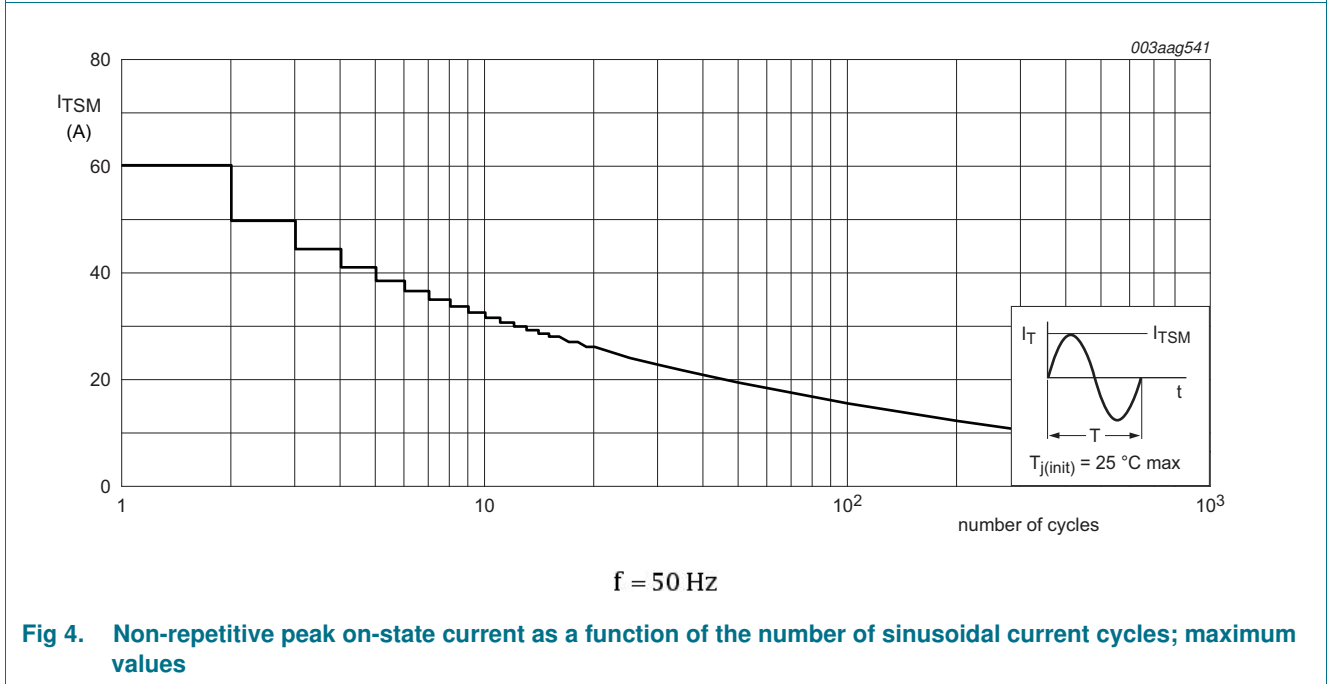
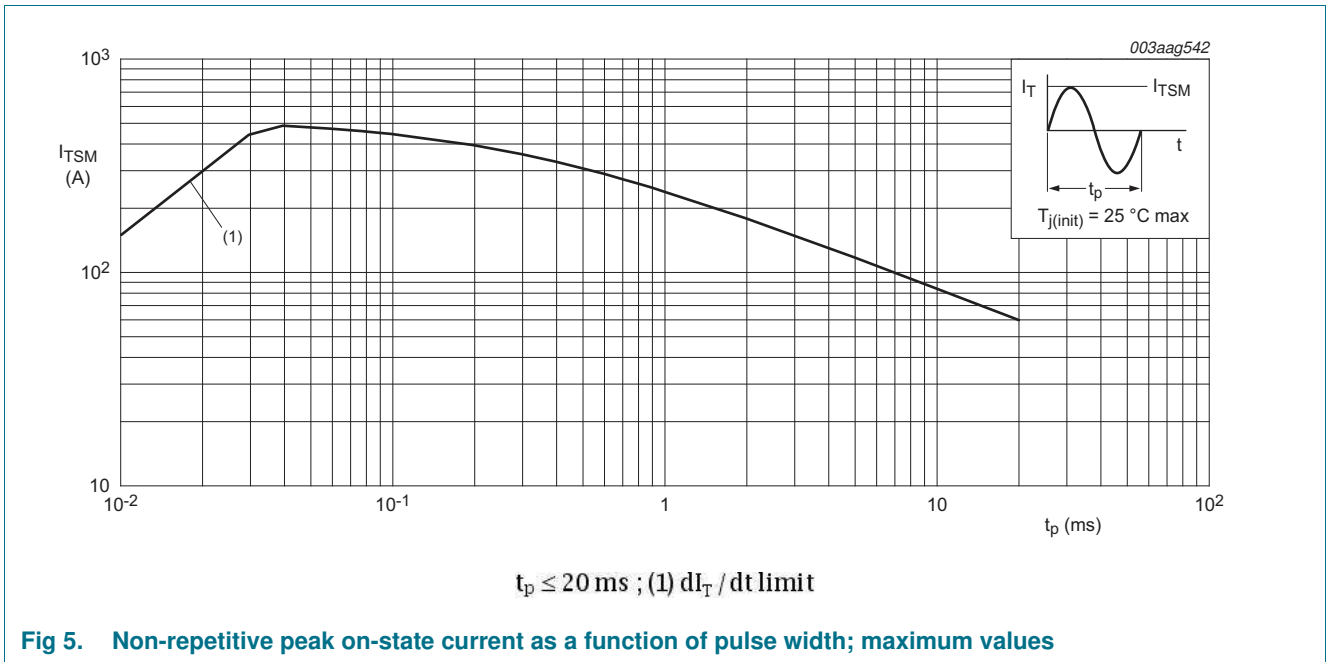


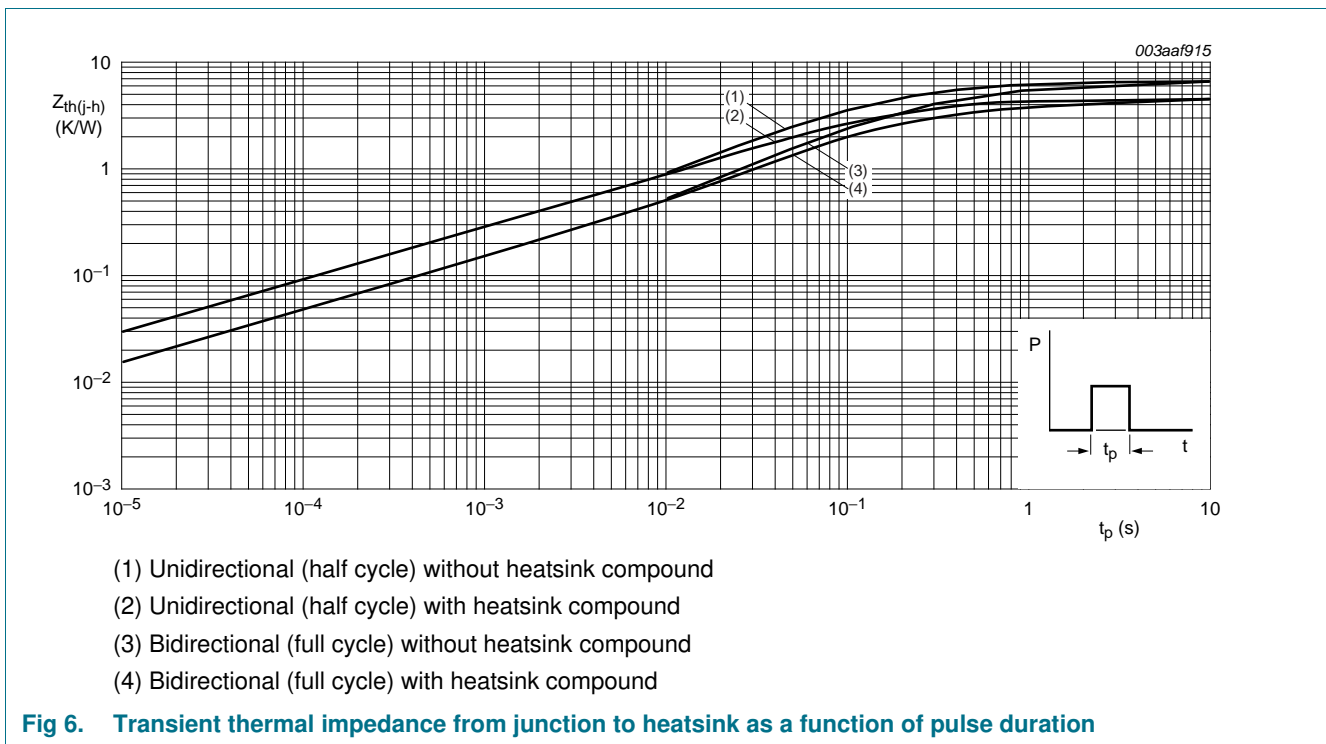
Fig 4. Non-repetitive peak on-state current as a function of the number of sinusoidal current cycles; maximum values



## 5. Thermal characteristics

**Table 5. Thermal characteristics**

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$R_{th(j-h)}$	thermal resistance from junction to heatsink	full cycle or half cycle; with heatsink compound; see <a href="#">Figure 6</a>	-	-	4.5	K/W
		full cycle or half cycle; without heatsink compound; see <a href="#">Figure 6</a>	-	-	6.5	K/W
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	-	55	-	K/W



## 6. Isolation characteristics

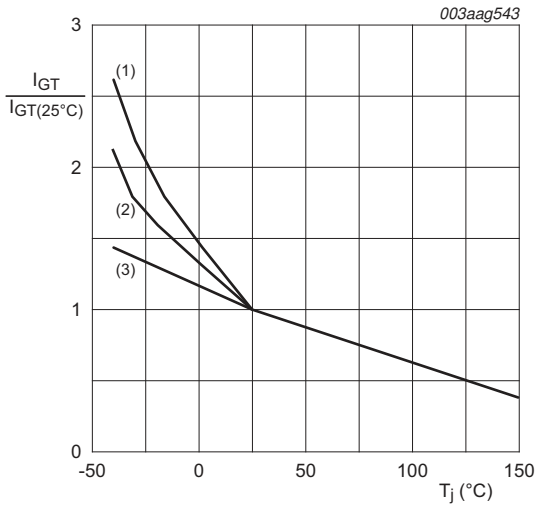
**Table 6. Isolation characteristics**

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$V_{isol(RMS)}$	RMS isolation voltage	from all terminals to external heatsink; sinusoidal waveform; clean and dust free ; 50 Hz ≤ f ≤ 60 Hz; RH ≤ 65 %; $T_h = 25\text{ °C}$	-	-	2500	V
$C_{isol}$	isolation capacitance	from main terminal 2 to external heatsink ; f = 1 MHz; $T_h = 25\text{ °C}$	-	10	-	pF

## 7. Characteristics

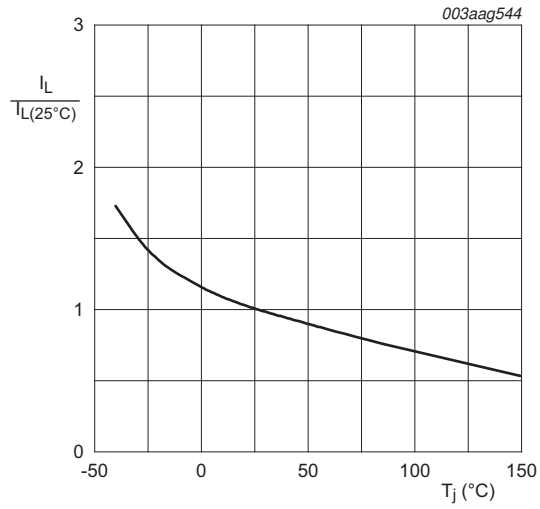
**Table 7. Characteristics**

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
<b>Static characteristics</b>						
$I_{GT}$	gate trigger current	$V_D = 12\text{ V}$ ; $I_T = 0.1\text{ A}$ ; T2+ G+; $T_j = 25\text{ °C}$ ; see <a href="#">Figure 7</a>	4	-	35	mA
		$V_D = 12\text{ V}$ ; $I_T = 0.1\text{ A}$ ; T2+ G-; $T_j = 25\text{ °C}$ ; see <a href="#">Figure 7</a>	4	-	35	mA
		$V_D = 12\text{ V}$ ; $I_T = 0.1\text{ A}$ ; T2- G-; $T_j = 25\text{ °C}$ ; see <a href="#">Figure 7</a>	4	-	35	mA
$I_L$	latching current	$V_D = 12\text{ V}$ ; $I_G = 0.1\text{ A}$ ; T2+ G+; $T_j = 25\text{ °C}$ ; see <a href="#">Figure 8</a>	-	-	50	mA
		$V_D = 12\text{ V}$ ; $I_G = 0.1\text{ A}$ ; T2+ G-; $T_j = 25\text{ °C}$ ; see <a href="#">Figure 8</a>	-	-	60	mA
		$V_D = 12\text{ V}$ ; $I_G = 0.1\text{ A}$ ; T2- G-; $T_j = 25\text{ °C}$ ; see <a href="#">Figure 8</a>	-	-	50	mA
$I_H$	holding current	$V_D = 12\text{ V}$ ; $T_j = 25\text{ °C}$ ; see <a href="#">Figure 9</a>	-	-	35	mA
$V_T$	on-state voltage	$I_T = 7\text{ A}$ ; see <a href="#">Figure 10</a>	-	1.3	1.6	V
$V_{GT}$	gate trigger voltage	$V_D = 12\text{ V}$ ; $I_T = 0.1\text{ A}$ ; $T_j = 25\text{ °C}$ ; see <a href="#">Figure 11</a>	-	0.8	1.5	V
		$V_D = 400\text{ V}$ ; $I_T = 0.1\text{ A}$ ; $T_j = 150\text{ °C}$	0.25	-	-	V
$I_D$	off-state current	$V_D = 800\text{ V}$ ; $T_j = 150\text{ °C}$	-	0.4	2	mA
<b>Dynamic characteristics</b>						
$dV_D/dt$	rate of rise of off-state voltage	$V_{DM} = 536\text{ V}$ ; $T_j = 150\text{ °C}$ ; ( $V_{DM} = 67\%$ of $V_{DRM}$ ); exponential waveform; gate open circuit	500	-	-	V/ $\mu$ s
$dI_{com}/dt$	rate of change of commutating current	$V_D = 400\text{ V}$ ; $T_j = 150\text{ °C}$ ; $I_{T(RMS)} = 6\text{ A}$ ; $dV_{com}/dt = 20\text{ V}/\mu\text{s}$ ; (snubberless condition); gate open circuit	10	-	-	A/ms
		$V_D = 400\text{ V}$ ; $T_j = 150\text{ °C}$ ; $I_{T(RMS)} = 6\text{ A}$ ; $dV_{com}/dt = 10\text{ V}/\mu\text{s}$ ; gate open circuit	12	-	-	A/ms
		$V_D = 400\text{ V}$ ; $T_j = 150\text{ °C}$ ; $I_{T(RMS)} = 6\text{ A}$ ; $dV_{com}/dt = 1\text{ V}/\mu\text{s}$ ; gate open circuit	20	-	-	A/ms

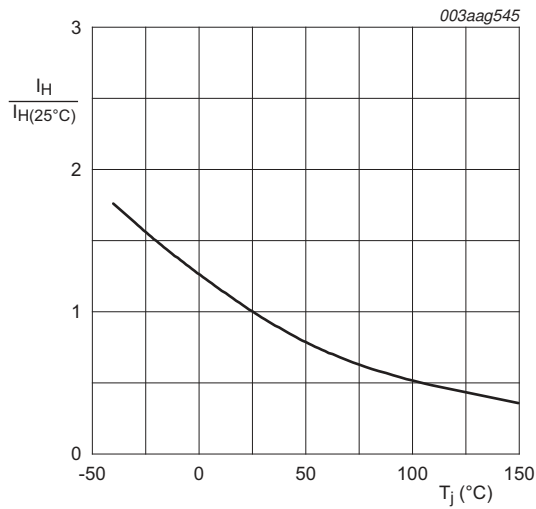


- (1) T2- G-
- (2) T2+ G-
- (3) T2+ G+

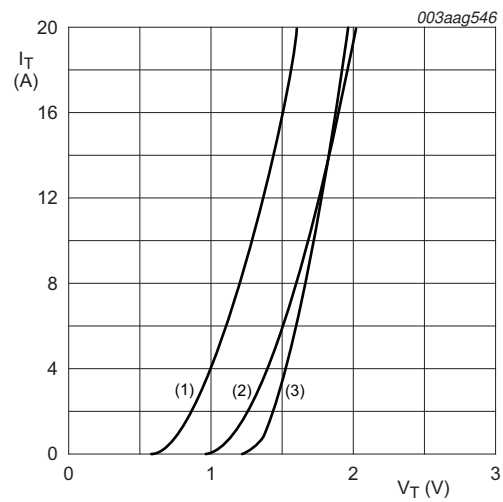
**Fig 7. Normalized gate trigger current as a function of junction temperature**



**Fig 8. Normalized latching current as a function of junction temperature**



**Fig 9. Normalized holding current as a function of junction temperature**



$V_o = 1.184 \text{ V}; R_s = 0.047 \Omega$

- (1)  $T_j = 150 \text{ }^\circ\text{C}$ ; typical values
- (2)  $T_j = 150 \text{ }^\circ\text{C}$ ; maximum values
- (3)  $T_j = 25 \text{ }^\circ\text{C}$ ; maximum values

**Fig 10. On-state current as a function of on-state voltage**



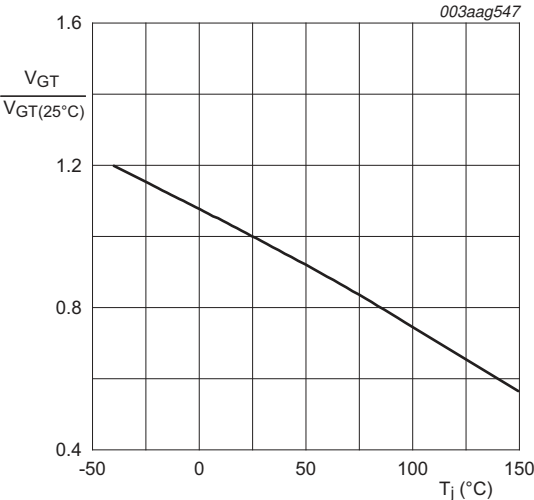


Fig 11. Normalized gate trigger voltage as a function of junction temperature

8. Package outline

Plastic single-ended package; isolated heatsink mounted;  
1 mounting hole; 3-lead TO-220 'full pack'

SOT186A

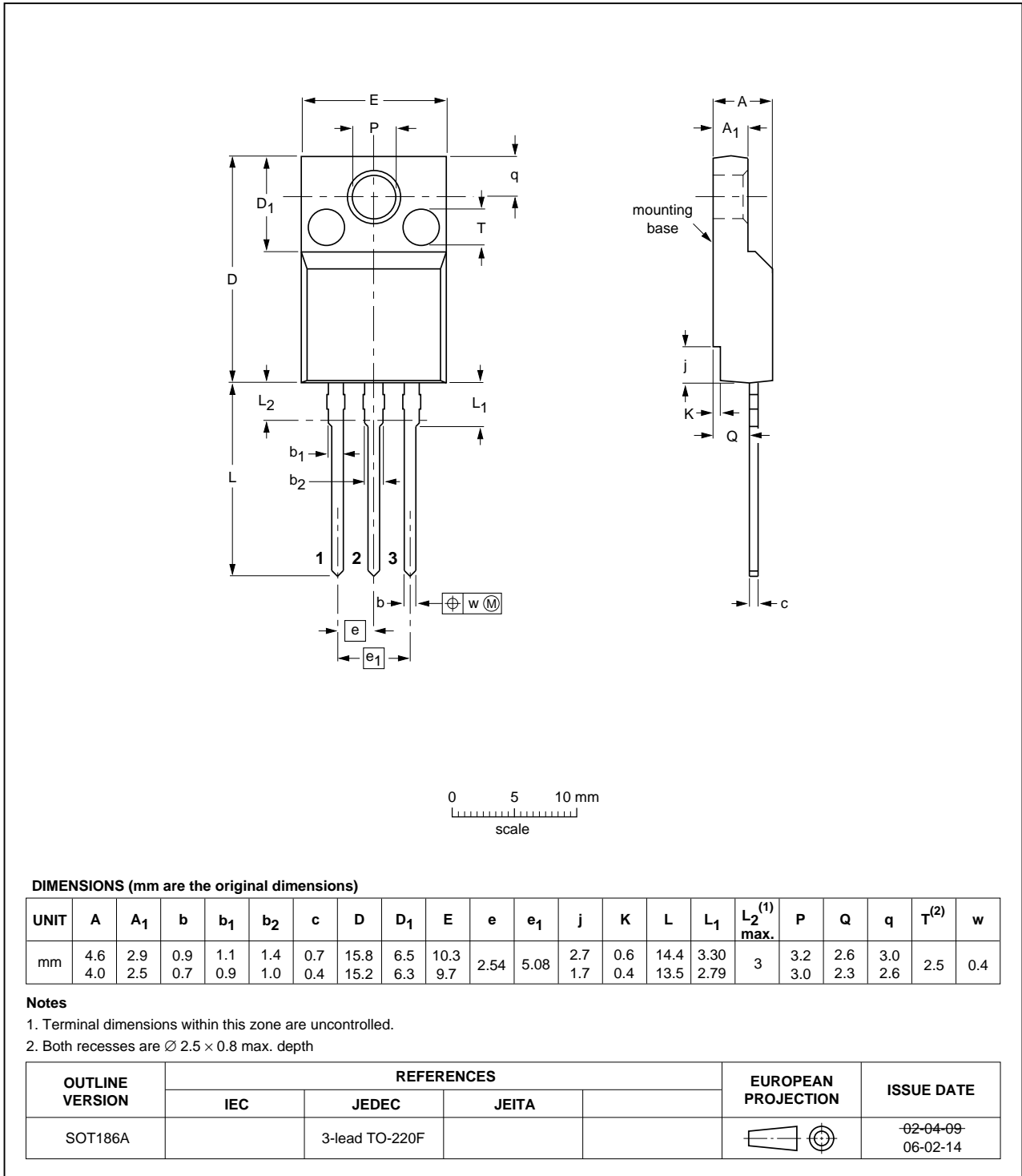


Fig 12. Package outline SOT186A (TO-220F)

## 9. Revision history

Table 8. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
BTA206X-800CT v.2	20111219	Product data sheet	-	BTA206X-800CT v.1
Modifications:	• Various changes to content.			
BTA206X-800CT v.1	20110823	Product data sheet	-	-

## 10. Legal information

### 10.1 Data sheet status

Document status <a href="#">[1]</a> <a href="#">[2]</a>	Product status <a href="#">[3]</a>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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