

C6D10170H

6th Generation 1700 V, 10 A Silicon Carbide Schottky Diode

Description

With the performance advantages of a Silicon Carbide (SiC) Schottky Barrier diode, power electronics systems can expect to meet higher efficiency standards than Si-based solutions, while also reaching higher frequencies and power densities. SiC diodes can be easily paralleled to meet various application demands, without concern of thermal runaway. In combination with the reduced cooling requirements and improved thermal performance of SiC products, SiC diodes are able to provide lower overall system costs in a variety of diverse applications.

Features

- Low Forward Voltage (V_F) Drop with Positive Temperature Coefficient
- Zero Reverse Recovery Current / Forward Recovery Voltage
- Temperature-Independent Switching Behavior
- Low Profile Package with Low Inductance

PIN 10 PIN 20 Package Types: T0-247-2 Marking: C6D10170H

Applications

- Industrial Switched Mode Power Supplies
- Uninterruptible & AUX Power Supplies
- Boost for PFC & DC-DC Stages
- Solar Inverters

Maximum Ratings ($T_c = 25^{\circ}C$ Unless Otherwise Specified)

| Parameter | Symbol | Value | Unit | Test Conditions | Notes | |
|--|------------------|-------|------------------|--|--------|--|
| Repetitive Peak Reverse Voltage | V _{RRM} | 1700 | v | | | |
| DC Blocking Voltage | V _{DC} | 1700 | V | | | |
| | | 40 | | $T_c = 25 \text{ °C}$ | | |
| Continuous Forward Current | I _F | 21 | A | T _c = 125 °C | Fig. 3 | |
| | | 10 | | T _c = 160 °C | | |
| Repetitive Peak Forward Surge Current | I _{FRM} | 58 | | $T_c = 25 \text{ °C}, t_p = 10 \text{ ms}, \text{Half Sine Wave}$ | | |
| | | 32 | | $T_c = 110 \text{ °C}, t_p = 10 \text{ ms}, \text{Half Sine Wave}$ | | |
| Non-Repetitive Peak Forward Surge Current | I _{FSM} | 148 | | $T_c = 25 \text{ °C}, t_p = 10 \text{ ms}, \text{Half Sine Wave}$ | Fig. 8 | |
| | | 93 | | $T_c = 110 \text{ °C}, t_p = 10 \text{ ms}, \text{Half Sine Wave}$ | | |
| Power Dissipation | P _{tot} | 204 | W | $T_c = 25 \text{ °C}$ | Fig. 4 | |
| | | 88 | | $T_c = 110 \text{ °C}$ | | |
| i²t Value | ∫i²t | 109 | A ² s | $T_{c} = 25 \text{ °C}, t_{p} = 10 \text{ ms}$ | | |
| | | 43 | | $T_{c} = 110 \text{ °C}, t_{p} = 10 \text{ ms}$ | | |

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Electrical Characteristics

| Parameter | Symbol | Тур. | Max. | Unit | Test Conditions | Notes | |
|---------------------------|----------------|------|------|------|--|----------|--|
| Forward Voltage | V | 1.45 | 1.7 | V | I _F = 10 A, T _j = 25 °C | | |
| | V _F | 2.0 | 2.8 | | I _F = 10 A, T _j = 175 °C | - Fig. 1 | |
| Reverse Current | | 4 | 18 | μA | V _R = 1700 V, T _j = 25 °C | | |
| | R R | 24 | 90 | | V _R = 1700 V, T _j = 175 °C | Fig. 2 | |
| Total Capacitive Charge | Q _c | 126 | | nC | V _R = 1700 V, T _j = 25 °C | Fig. 5 | |
| Total Capacitance | | 1227 | | pF | $V_{R} = 0 V, T_{j} = 25 °C, f = 1 MHz$ | | |
| | С | 53 | | | $V_{R} = 800 \text{ V}, \text{ T}_{j} = 25 \text{ °C}, \text{ f} = 1 \text{ MHz}$ | Fig. 6 | |
| | | 52 | | | $V_{R} = 1700 \text{ V}, \text{ T}_{j} = 25 \text{ °C}, \text{ f} = 1 \text{ MHz}$ | | |
| Capacitance Stored Energy | E _c | 79 | | μJ | V _R = 1700 V | Fig. 7 | |

Notes:

SiC Schottky Diodes are majority carrier devices, so there is no reverse recovery charge.

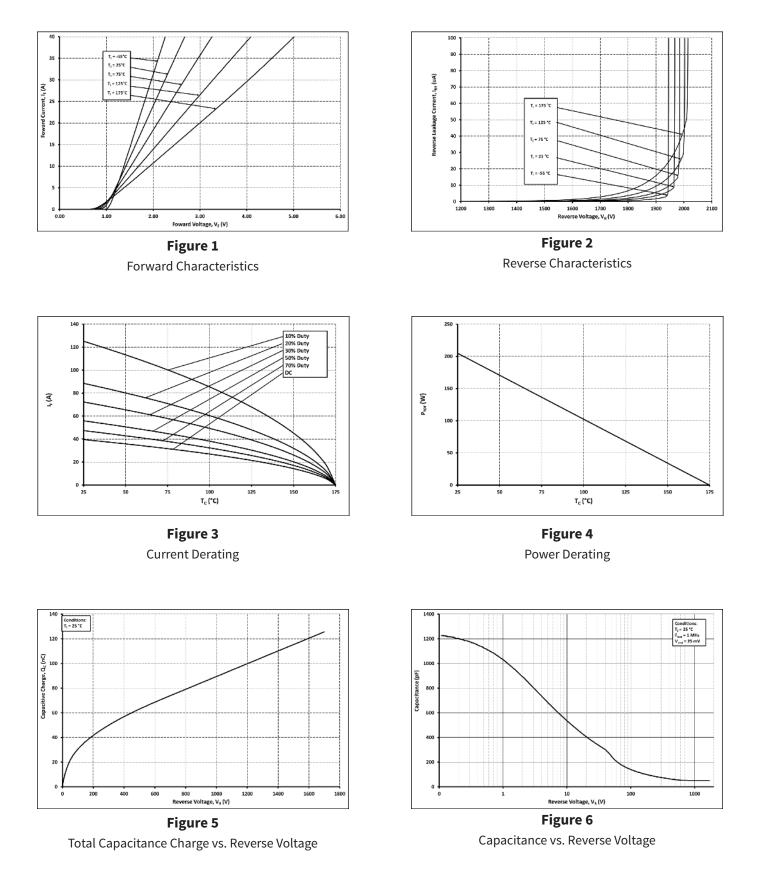
Thermal & Mechanical Characteristics

| Parameter | Symbol | Value | Unit | Notes |
|--|--------------------------|-------------|----------|------------|
| Thermal Resistance, Junction to Case (Typical) | R _{0, JC (TYP)} | 0.62 | 8C (M) | |
| Thermal Resistance, Junction to Case (Maximum) | R _{0, JC (MAX)} | 0.73 | − °C / W | |
| Junction Temperature | T _j | -55 to +175 | - °C | |
| Case & Storage Temperature | T _c | -55 to +150 | | |
| TO 247 Mounting Toursus | | 1 | Nm | M3 Screw |
| TO-247 Mounting Torque | | 8.8 | lbf-in | 6-32 Screw |

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Typical Performance



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Typical Performance

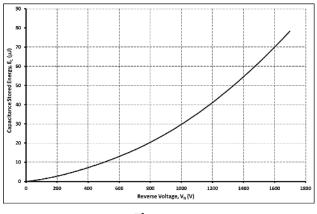


Figure 7 Capacitance Stored Energy

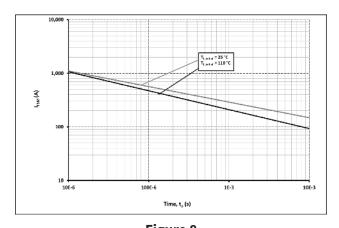


Figure 8 Non-Repetitive Peak Forward Surge Current vs. Pulse Duraion (Sinusouidal Waveform)

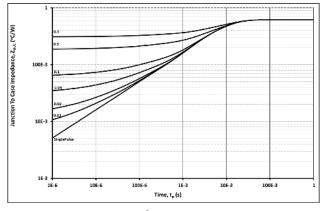


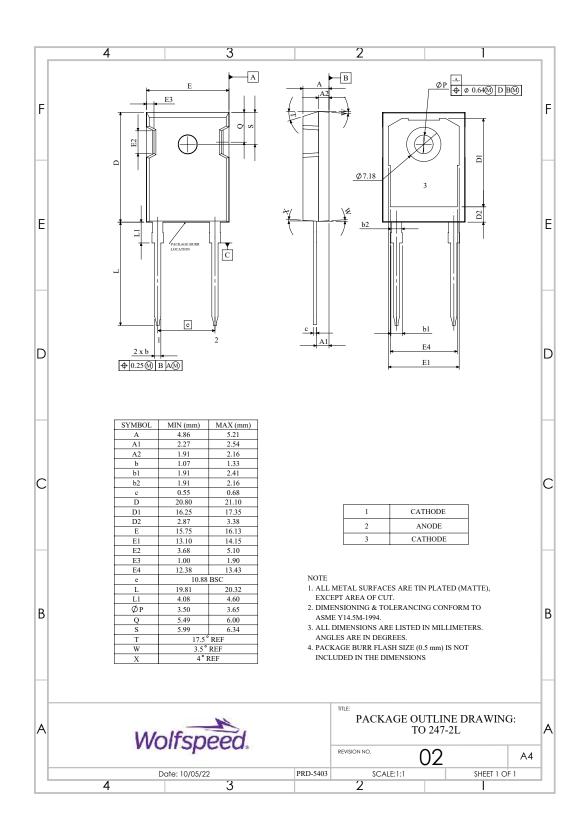
Figure 9 Transient Thermal Impedance

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Package Dimensions & Pin-Out

Package: TO-247-2 (All dimensions are in mm)



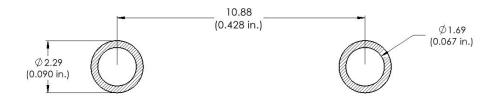
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Recommended Solder Pad Layout

Package: TO-247-2 (All dimensions are in mm)



Product Ordering Information

| Order Number | Packing Type |
|--------------|--------------|
| C6D10170H | Tube |

REACh, RoHS, and Halogen-Free compliance documentation available for this product.

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Revision History

| Document Version | Date of Release | Description of changes |
|------------------|-----------------|------------------------|
| 0 | December-2022 | Initial datasheet |

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Contact info:

4600 Silicon Drive Durham, NC 27703 USA Tel: +1.919.313.5300 www.wolfspeed.com/power

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