CryptoAuthentication™ SOIC XPRO Starter Kit

Part Number: DM320109

Summary

The CryptoAuthentication™ SOIC Xplained Pro Starter (CryptoAuth-XSTK) kit consists of a SAMD21-XPRO and an AT88CKSCKTSOIC-XPRO socket board along with Crypto Authentication sample devices. The board works with Microchip's Crypto Evaluation Studio (ACES) out of the box and can be used for evaluation and development wih CryptoAuthentication Devices. The kit supports all Crypto Authentication devices including the ATECC608A, ATECC508A, ATECC108A, ATSHA204A and the ATAES132A. The kit can support I²C Interface devices, Single-Wire-Interface (SWI) devices, and devices with a SPI interface by setting the appropriate switches on the socket board. If a different package other than SOIC is required then a different socket board may be ordered and used with the kit.

The firmware in the kit will also work with ATCRYPOTAUTH-XPRO and ATCRYPTOAUTH-XPRO-B boards.   A maximum of 8 CryptoAuthentication devices can be detected with the firmware.

Package Contents

- 1 - Information card containing Quick Start information.
- 1 - SAMD21-XPRO Board Preprogrammed with Microchip Secure Products Group Kit Prototocl
- 1 - AT88CKSCKTSOIC-XPRO Socket
- 1 - USB Cable
- 1 - Sample Pack of 3 ATECC608A I^2C Devices
- 1 - Sample Pack of 3 ATECC508A I^2C Devices
- 1 - Sample Pack of 3 ATSHA204A I^2C Devices
- 1 - Sample Pack of 3 ATAES132A I^2C Devices

System Requirements

- Windows 7 or WIndows 10 Operating System for use with Microchip ACES
- Linux or Windows for use with Python Tools
- ACES 6.0.3 or Later

Device Features

- ATSAMD21-XPRO Develoopment Board pre-programmed with Kit Protocol FIrmware
- AT88CKSCKTSOIC-XPRO Socket Board
- Compatible with ACES software and other Microchip Development Tools
- Kit name identified by software as CryptoAuth-XSTK
- Crypto Device Detection on EXT1 and/or EXT2 XPRO ports.
- Debugger output using USB EDBG port
- I^2C 3-Device SOIC samples packs for the ATECC608A, ATECC508A, ATSHA204A, ATAES132A.
- Support for I^2C, Single Wire (SWI) and SPI Interface devices