

N-Channel 60-V (D-S) MOSFET

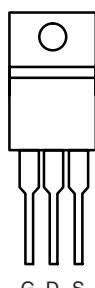
PRODUCT SUMMARY			
V _{(BR)DSS} (V)	r _{D(on)} (Ω)	I _D (A)	Q _g (Typ)
60	0.005 at V _{GS} = 10 V	90 ^d	105

FEATURES

- TrenchFET® Power MOSFET
- 175 °C Junction Temperature
- 100 % R_g and UIS Tested



TO-220AB

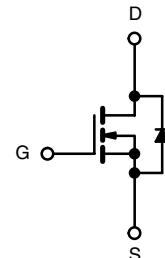


Top View

Ordering Information: SUP90N06-5m0P-E3 (Lead (Pb)-free)

APPLICATIONS

- Power Supply
 - Secondary Synchronous Rectification
- Industrial
- OR-ing



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS

 $T_C = 25^\circ\text{C}$, unless otherwise noted

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	60	V
Gate-Source Voltage	V _{GS}	± 20	
Continuous Drain Current ($T_J = 175^\circ\text{C}$)	I _D	90 ^d	A
		90 ^d	
Pulsed Drain Current	I _{DM}	240	
Avalanche Current	I _{AS}	70	
Single Avalanche Energy ^a	E _{AS}	245	mJ
Maximum Power Dissipation ^a	P _D	300 ^b	W
		3.75	
Operating Junction and Storage Temperature Range	T _J , T _{stg}	- 55 to 175	°C

THERMAL RESISTANCE RATINGS

Parameter	Symbol	Limit	Unit
Junction-to-Ambient (PCB Mount) ^c	R _{thJA}	40	°C/W
Junction-to-Case (Drain)	R _{thJC}	0.5	

Notes:

- Duty cycle ≤ 1 %.
- See SOA curve for voltage derating.
- When Mounted on 1" square PCB (FR-4 material).
- Package limited.

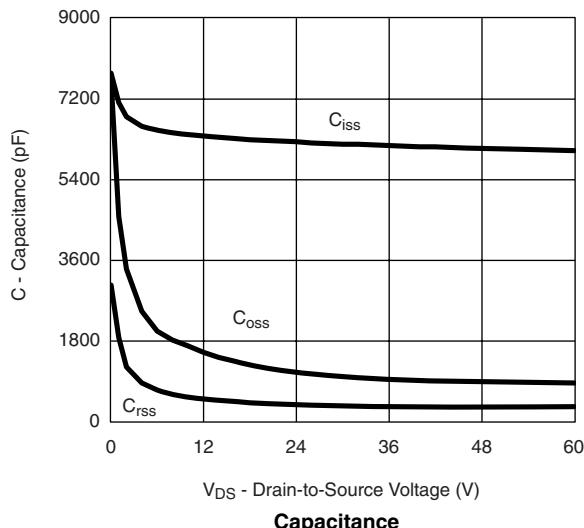
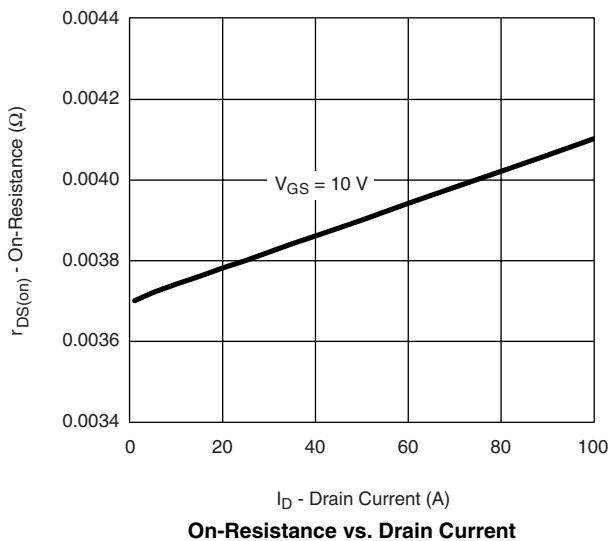
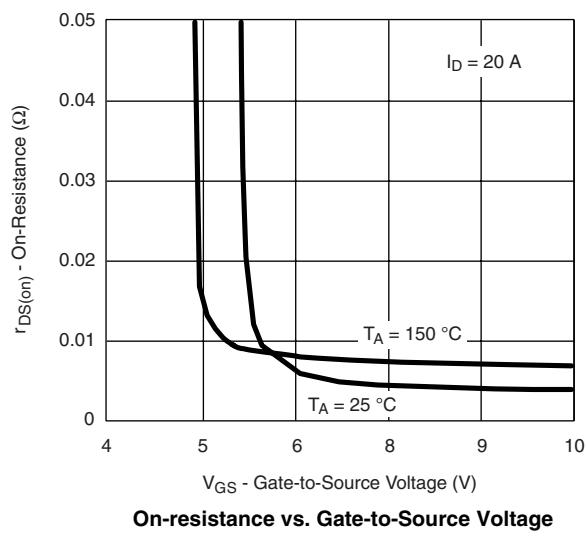
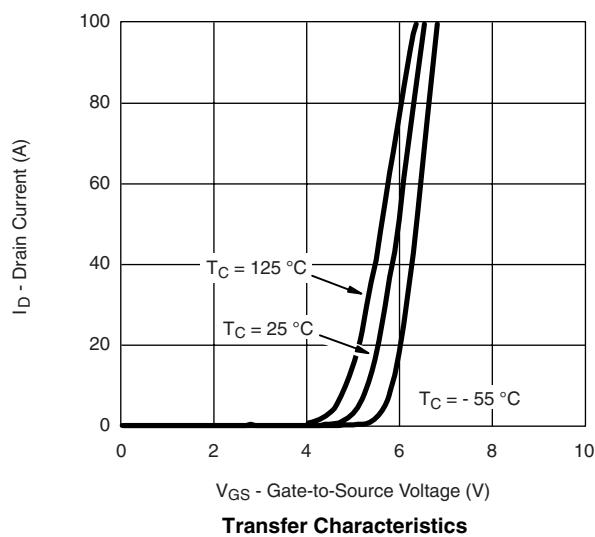
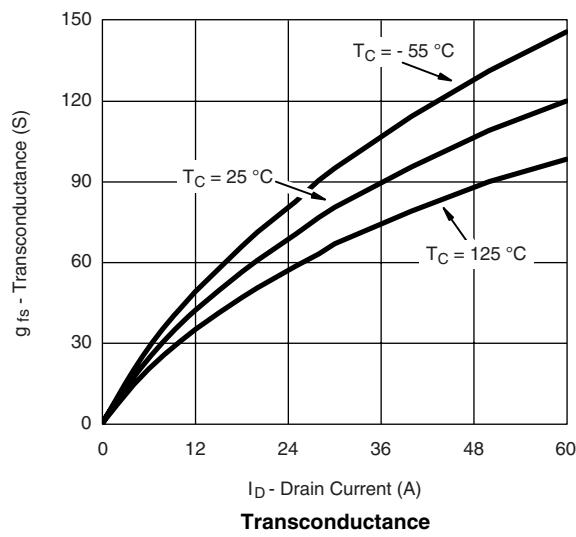
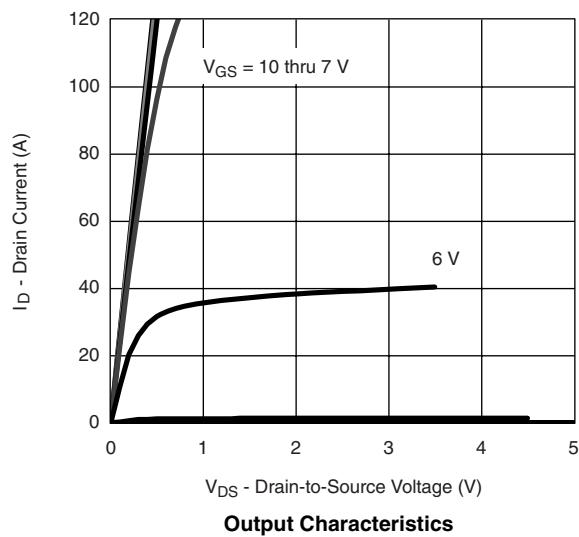
SPECIFICATIONS $T_J = 25^\circ\text{C}$, unless otherwise noted

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Static						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{DS}} = 0 \text{ V}, I_D = 250 \mu\text{A}$	60			V
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = 250 \mu\text{A}$	2.5		4.5	
Gate-Body Leakage	I_{GSS}	$V_{\text{DS}} = 0 \text{ V}, V_{\text{GS}} = \pm 20 \text{ V}$			± 250	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{\text{DS}} = 60 \text{ V}, V_{\text{GS}} = 0 \text{ V}$			1	μA
		$V_{\text{DS}} = 60 \text{ V}, V_{\text{GS}} = 0 \text{ V}, T_J = 125^\circ\text{C}$			50	
		$V_{\text{DS}} = 60 \text{ V}, V_{\text{GS}} = 0 \text{ V}, T_J = 150^\circ\text{C}$			250	
On-State Drain Current ^a	$I_{\text{D}(\text{on})}$	$V_{\text{DS}} \geq 10 \text{ V}, V_{\text{GS}} = 10 \text{ V}$	70			A
Drain-Source On-State Resistance ^a	$r_{\text{DS}(\text{on})}$	$V_{\text{GS}} = 10 \text{ V}, I_D = 20 \text{ A}$		0.0041	0.005	Ω
		$V_{\text{GS}} = 10 \text{ V}, I_D = 20 \text{ A}, T_J = 125^\circ\text{C}$		0.0068	0.0087	
Forward Transconductance ^a	g_{fs}	$V_{\text{DS}} = 15 \text{ V}, I_D = 20 \text{ A}$		60		S
Dynamic^b						
Input Capacitance	C_{iss}	$V_{\text{GS}} = 0 \text{ V}, V_{\text{DS}} = 30 \text{ V}, f = 1 \text{ MHz}$		6190		pF
Output Capacitance	C_{oss}			990		
Reverse Transfer Capacitance	C_{rss}			340		
Total Gate Charge ^c	Q_g	$V_{\text{DS}} = 30 \text{ V}, V_{\text{GS}} = 10 \text{ V}, I_D = 85 \text{ A}$		105	160	nC
Gate-Source Charge ^c	Q_{gs}			29		
Gate-Drain Charge ^c	Q_{gd}			28		
Gate Resistance	R_g	$f = 1 \text{ MHz}$		1.4	2.8	Ω
Turn-On Delay Time ^c	$t_{\text{d}(\text{on})}$	$V_{\text{DD}} = 30 \text{ V}, R_L = 0.4 \Omega$ $I_D \geq 85 \text{ A}, V_{\text{GEN}} = 10 \text{ V}, R_g = 1 \Omega$		23	35	ns
Rise Time ^c	t_r			15	25	
Turn-Off Delay Time ^c	$t_{\text{d}(\text{off})}$			36	55	
Fall Time ^c	t_f			8	15	
Source-Drain Diode Ratings and Characteristics ($T_C = 25^\circ\text{C}$) ^b						
Continuous Current	I_S				85	A
Pulsed Current	I_{SM}				240	
Forward Voltage ^a	V_{SD}	$I_F = 30 \text{ A}, V_{\text{GS}} = 0 \text{ V}$		0.84	1.5	V
Reverse Recovery Time	t_{rr}	$I_F = 75 \text{ A}, \text{di/dt} = 100 \text{ A}/\mu\text{s}$		61	100	ns
Peak Reverse Recovery Current	$I_{\text{PRM}(\text{REC})}$			3.0	4.5	A
Reverse Recovery Charge	Q_{rr}			91	140	μC

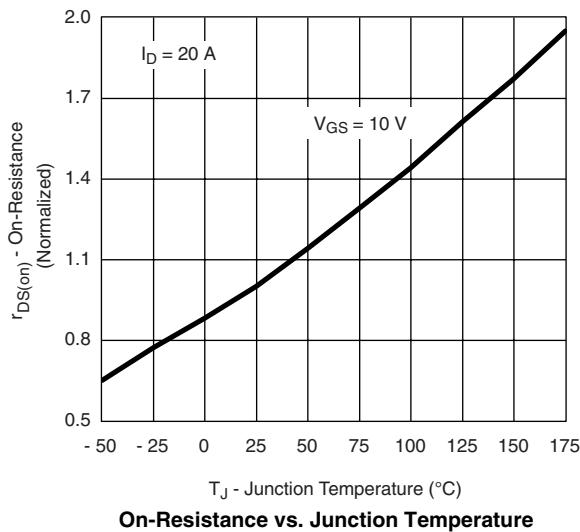
Notes:

- a. Pulse test; pulse width $\leq 300 \mu\text{s}$, duty cycle $\leq 2\%$.
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

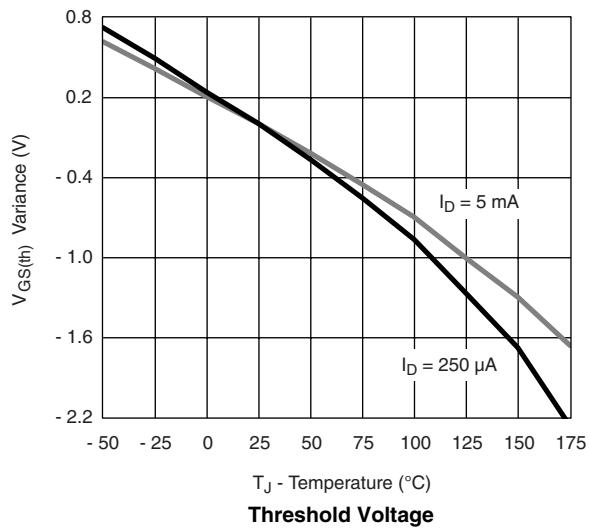
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted


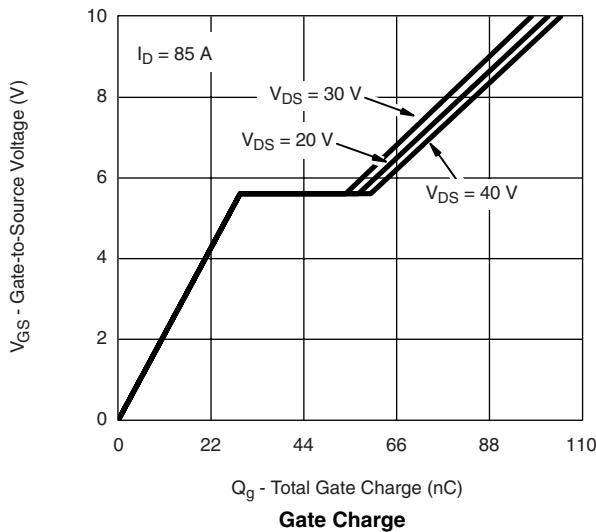
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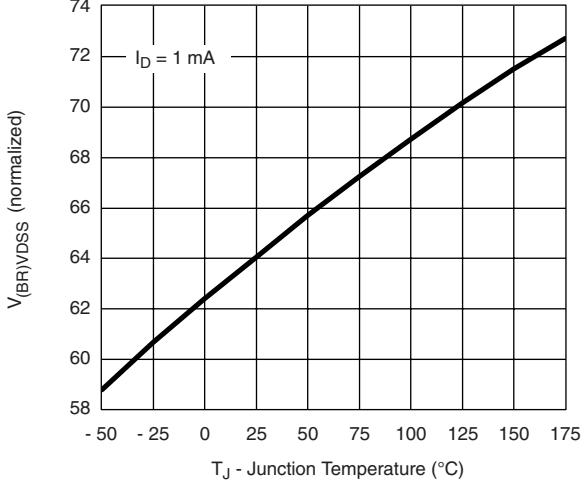
On-Resistance vs. Junction Temperature



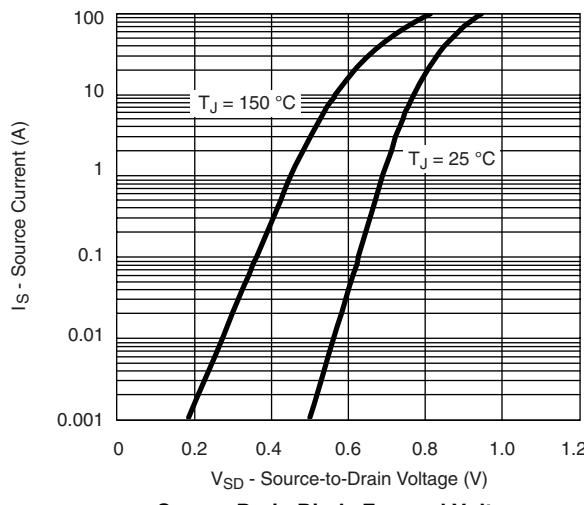
Threshold Voltage



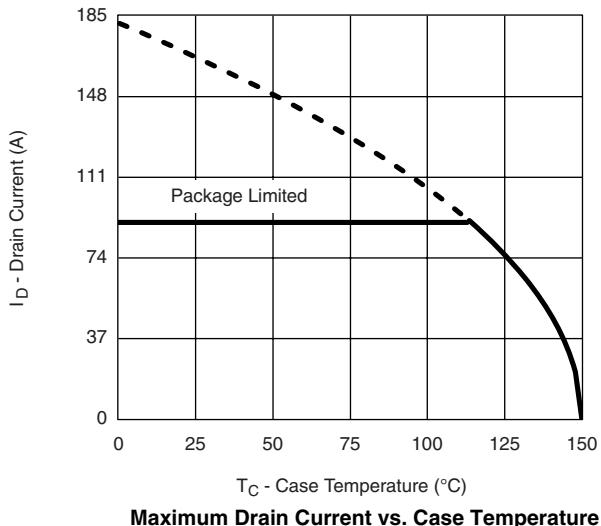
Gate Charge



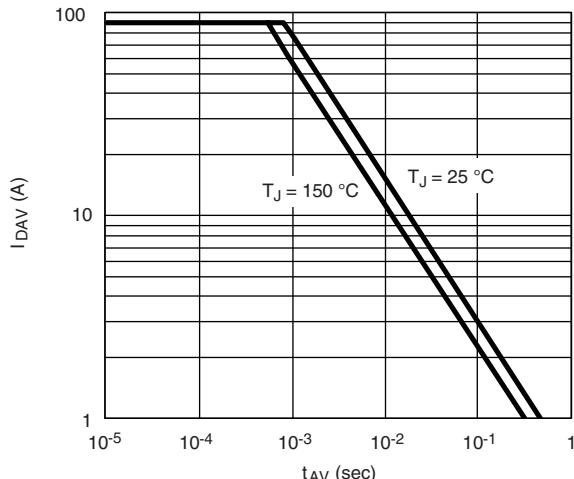
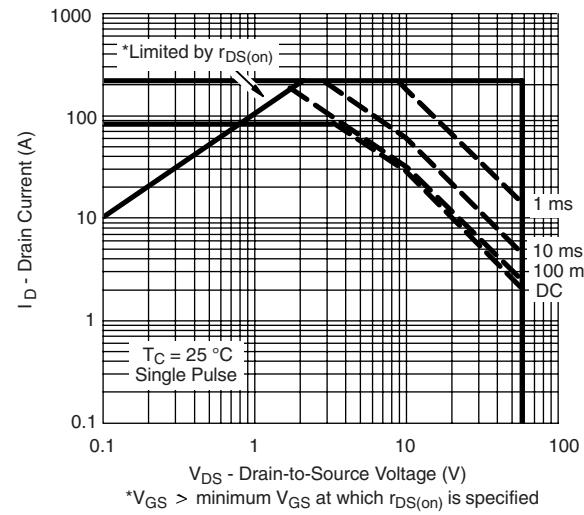
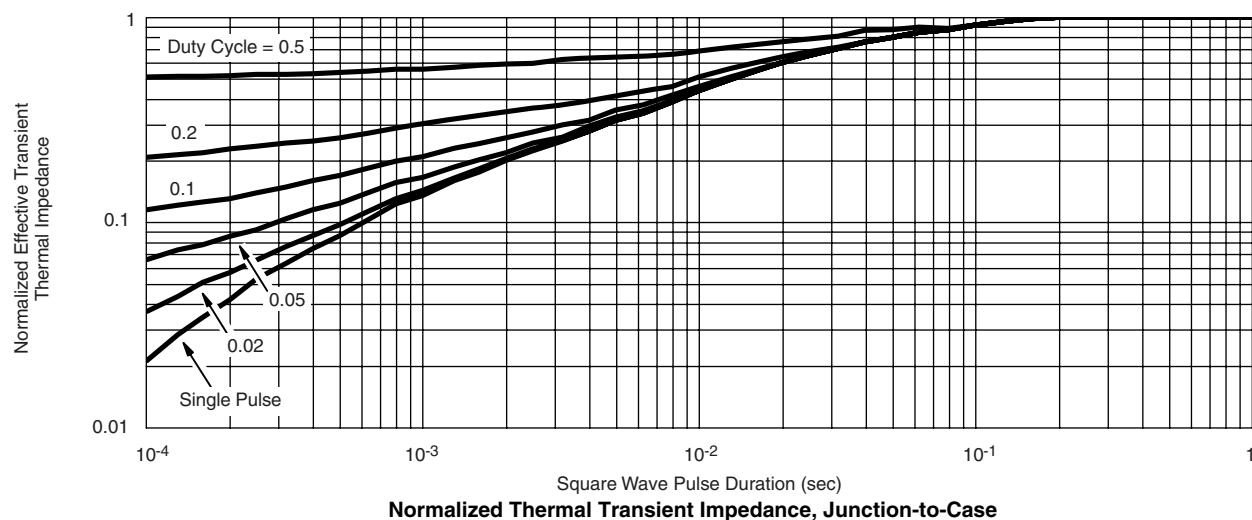
On-Resistance vs. Junction Temperature



Source-Drain Diode Forward Voltage



Maximum Drain Current vs. Case Temperature

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

Single Pulse Avalanche Current Capability vs. Time

Safe Operating Area

Normalized Thermal Transient Impedance, Junction-to-Case

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