

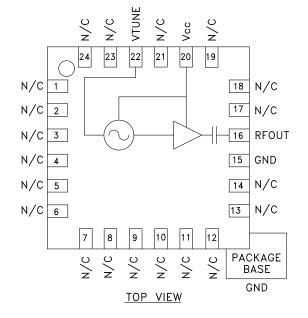


Typical Applications

Low noise MMIC VCO w/Buffer Amplifier for:

- Wireless Infrastructure
- Industrial Controls
- Test Equipment
- Military

Functional Diagram



HMC416LP4 / 416LP4E

MMIC VCO w/ BUFFER AMPLIFIER, 2.75 - 3.0 GHz

Features

Pout: +4.5 dBm Phase Noise: -114 dBc/Hz @100 k Hz No External Resonator Needed Single Supply: 3V @ 37 mA QFN Leadless SMT Package, 16 mm²

General Description

The HMC416LP4 & HMC416LP4E are GaAs InGaP Heterojunction Bipolar Transistor (HBT) MMIC VCOs with integrated resonators, negative resistance devices, varactor diodes, and buffer amplifiers. Covering 2.75 to 3.0 GHz, the VCO's phase noise performance is excellent over temperature, shock, vibration and process due to the oscillator's monolithic structure. Power output is 4.5 dBm typical from a single supply of 3V @ 37 mA. The voltage controlled oscillator is packaged in a low cost leadless QFN 4 x 4 mm surface mount package.

Electrical Specifications, $T_{a} = +25^{\circ}$ C, Vcc = +3V

Parameter	Min.	Тур.	Max.	Units
Frequency Range		2.75 - 3.0		
Power Output	1.5	4.5		dBm
SSB Phase Noise @ 100 kHz Offset, Vtune= +5V @ RF Output		-114		dBc/Hz
Tune Voltage (Vtune)	0		10	V
Supply Current (Icc) (Vcc = +3.0V)		37		mA
Tune Port Leakage Current			10	μA
Output Return Loss		9		dB
Harmonics 2nd 3rd		-5 -16		dBc dBc
Pulling (into a 2.0:1 VSWR)		3		MHz pp
Pushing @ Vtune= +5V		-1		MHz/V
Frequency Drift Rate		0.3		MHz/°C

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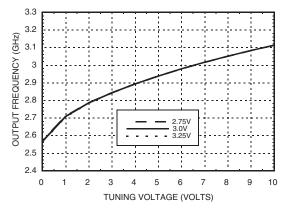
For price, delivery, and to place orders: Analog Devices, Inc., One Technology Way, P.O. Box 9106, Norwood, MA 02062-9106 Phone: 781-329-4700 • Order online at www.analog.com Application Support: Phone: 1-800-ANALOG-D

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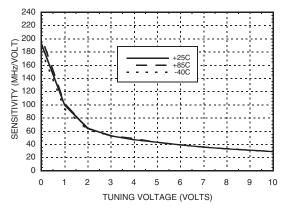




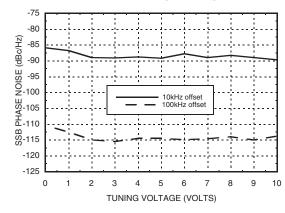
Frequency vs. Tuning Voltage, T= 25°C



Sensitivity vs. Tuning Voltage, Vcc= +3V

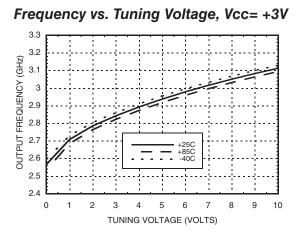


Phase Noise vs. Tuning Voltage

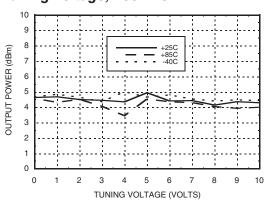




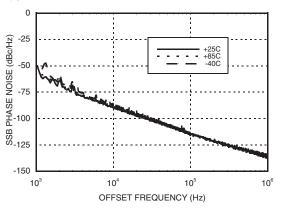
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Output Power vs. Tuning Voltage, Vcc= +3V



Typical SSB Phase Noise @ Vtune= +5V



VCOs & PLOs - SMT

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Absolute Maximum Ratings

Vcc	+3.5 Vdc
Vtune	0 to +11V
Channel Temperature	135 °C
Continuous Pdiss (T = 85°C) (derate 3.6 mW/°C above 85°C)	180 mW
Thermal Resistance (R _{TH}) (junction to package base)	277 °C/W
Storage Temperature	-65 to +150 °C
Operating Temperature	-40 to +85 °C

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Typical Supply Current vs. Vcc

Vcc (V)	lcc (mA)
2.75	32
3.0	37
3.25	42

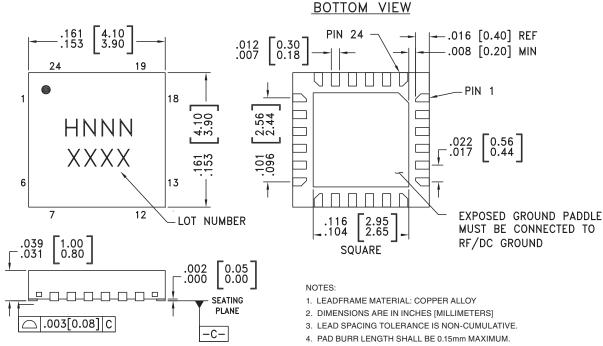
Note: VCO will operate over full voltage range shown above.



ELECTROSTATIC SENSITIVE DEVICE OBSERVE HANDLING PRECAUTIONS

Outline Drawing

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- PAD BURR HEIGHT SHALL BE 0.05mm MAXIMUM. 5. PACKAGE WARP SHALL NOT EXCEED 0.05mm.
- ALL GROUND LEADS AND GROUND PADDLE MUST BE SOLDERED TO PCB RF GROUND.
- 7. REFER TO HITTITE APPLICATION NOT FOR SUGGESTED LAND PATTERN.

Package Information

Part Number	Package Body Material	Lead Finish	MSL Rating	Package Marking [3]
HMC416LP4	Low Stress Injection Molded Plastic	Sn/Pb Solder	MSL1 ^[1]	H416 XXXX
HMC416LP4E	RoHS-compliant Low Stress Injection Molded Plastic	100% matte Sn	MSL1 ^[2]	<u>H416</u> XXXX

[1] Max peak reflow temperature of 235 °C

[2] Max peak reflow temperature of 260 °C

[3] 4-Digit lot number XXXX

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Pin Descriptions

Pin Number	Function	Description	Interface Schematic
1- 14, 17 - 19, 21, 23, 24	N/C	No Connection. These pins may be connected to RF ground. Performance will not be affected.	
15	GND	This pin must be connected to RF & DC ground.	GND =
16	RFOUT	RF output (AC coupled)	○ RFOUT
20	Vcc	Supply Voltage Vcc= 3V	VccO
22	VTUNE	Control Voltage Input. Modulation port bandwidth dependent on drive source impedance.	$\begin{array}{c} 10nH \\ 50 \\ 4.6pF \\ \hline \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ $
	GND	Package bottom has an exposed metal paddle that must be RF & DC grounded.	GND =

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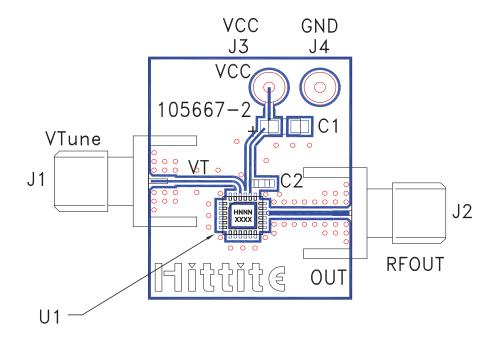
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v02.0805

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Evaluation PCB



List of Materials for Evaluation PCB 105706^[1]

Item	Description	
J1 - J2	PCB Mount SMA RF Connector	
J3 - J4	DC Pin	
C1	4.7 µF Tantalum Capacitor	
C2	10,000 pF Capacitor, 0603 Pkg.	
U1	HMC416LP4 / HMC416LP4E VCO	
PCB [2]	105667 Eval Board	

Reference this number when ordering complete evaluation PCB
Circuit Board Material: Rogers 4350

The circuit board used in the final application should use RF circuit design techniques. Signal lines should have 50 ohm impedance while the package ground leads and exposed paddle should be connected directly to the ground plane similar to that shown. A sufficient number of via holes should be used to connect the top and bottom ground planes. The evaluation circuit board shown is available from Hittite upon request.

VCOs & PLOs - SMT

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Notes:

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