

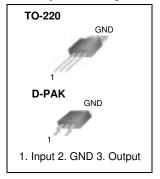


### Features

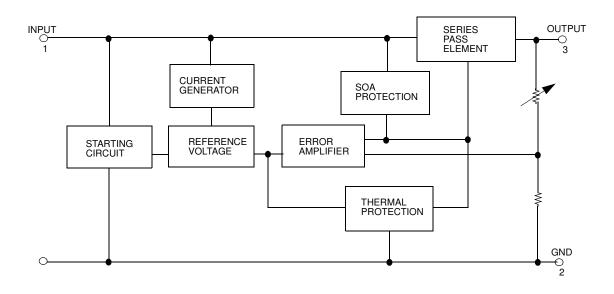
- Output Current up to 1A
- Output Voltages of 5, 6, 8, 9, 12, 15, 18, 24V
- Thermal Overload Protection
- Short Circuit Protection
- Output Transistor Safe Operating Area Protection

### Description

The MC78XXE/LM78XXE/MC78XXAE series of three terminal positive regulators are available in the TO-220/D-PAK package and with several fixed output voltages, making them useful in a wide range of applications. Each type employs internal current limiting, thermal shut down and safe operating area protection, making it essentially indestructible. If adequate heat sinking is provided, they can deliver over 1A output current. Although designed primarily as fixed voltage regulators, these devices can be used with external components to obtain adjustable voltages and currents.



# Internal Block Digram



# **Absolute Maximum Ratings**

Parameter	Symbol	Value	Unit
Input Voltage (for $V_O = 5V$ to $18V$ ) (for $V_O = 24V$ )	VI VI	35 40	V V
Thermal Resistance Junction-Cases (TO-220)	Rejc	5	°C/W
Thermal Resistance Junction-Air (TO-220)	R <sub>0</sub> JA	65	°C/W
Operating Temperature Range	TOPR	0 ~ +125	°C
Storage Temperature Range	TSTG	-65 ~ +150	°C

# **Electrical Characteristics (MC7805E/LM7805E)**

(Refer to test circuit ,0°C < TJ < 125°C, IO = 500mA, VI = 10V, CI =  $0.33\mu$ F, CO=  $0.1\mu$ F, unless otherwise specified)

Devenueter	Cumhal	0	u diti a u a	MC78	05E/LM	7805E	Unit
Parameter	Symbol		onditions	Min.	Тур.	Max.	Unit
		TJ = +25°C		4.8	5.0	5.2	
Output Voltage	Vo	$5.0 \text{mA} \le \text{lo} \le 1.0 \text{A}, \text{P}_{O} \le 15 \text{W}$ VI = 7V to 20V		4.75	5.0	5.25	V
Line Regulation (Note1)	Doglino	TJ = +25°C	Vo = 7V to 25V	-	4.0	100	mV
Line Regulation (Note1)	Regline	1J = +25 C	VI = 8V to 12V	-	1.6	50	IIIV
		$T_J = +25^{\circ}C$	IO = 5.0mA to1.5A	-	9	100	
Load Regulation (Note1)	-		I <sub>O</sub> =250mA to 750mA	-	4	50	mV
Quiescent Current	lq	TJ = +25°C		-	5.0	8.0	mA
Quieseent Current Change	410	I <sub>O</sub> = 5mA to 1.	0A	-	0.03	0.5	mA
Quiescent Current Change	ΔlQ	VI = 7V to 25V		-	0.3	1.3	ШA
Output Voltage Drift (Note2)	$\Delta V_O / \Delta T$	$I_{O} = 5mA$		-	-0.8	-	mV/°C
Output Noise Voltage	VN	f = 10Hz to 10	0kHz, TA = +25°C	-	42	-	μV/Vo
Ripple Rejection (Note2)	RR	f = 120Hz V <sub>O</sub> = 8V to 18	f = 120Hz V <sub>O</sub> = 8V to 18V		73	-	dB
Dropout Voltage	VDrop	I <sub>O</sub> = 1A, T <sub>J</sub> =+25°C		-	2	-	V
Output Resistance (Note2)	rO	f = 1kHz		-	15	-	mΩ
Short Circuit Current	Isc	VI = 35V, TA =	+25°C	-	230	-	mA
Peak Current (Note2)	Iрк	TJ = +25°C		-	2.2	-	А

### Note:

1. Load and line regulation are specified at constant junction temperature. Changes in  $V_0$  due to heating effects must be taken into account separately. Pulse testing with low duty is used.

# Electrical Characteristics (MC7806E) (Continued)

(Refer to test circuit ,0°C < TJ < 125°C, IO = 500mA, VI =11V, CI =  $0.33\mu$ F, CO=  $0.1\mu$ F, unless otherwise specified)

Parameter	Cumbal	0	onditions	N	IC7806	E	Unit
Parameter	Symbol		onations	Min.	Тур.	Max.	Unit
		TJ = +25°C		5.75	6.0	6.25	
Output Voltage	Vo	$\begin{array}{l} \text{5.0mA} \leq \text{IO} \leq \\ \text{VI} = 8.0 \text{V to 21} \end{array}$	1.0A, P_O $\leq$ 15W V	5.7	6.0	6.3	V
Line Regulation (Nate1)	Poglino	TJ =+25°C	VI = 8V to 25V	-	5	120	mV
Line Regulation (Note1)	Regline	1J = +25  C	VI = 9V to 13V	-	1.5	60	ΠV
Load Population (Noto1)	Poglaad	TL	IO =5mA to 1.5A	-	9	120	mV
Load Regulation (Note1)	Regload	TJ =+25°C	IO =250mA to750A	-	3	60	IIIV
Quiescent Current	lQ	TJ =+25°C		-	5.0	8.0	mA
Quiescent Current Change	Alo	$\Delta I_Q$ IO = 5mA to 1A		-	-	0.5	mA
Quiescent Current Change	ΔIQ	VI = 8V to 25V		-	-	1.3	IIIA
Output Voltage Drift (Note2)	$\Delta V_{O}/\Delta T$	IO = 5mA		-	-0.8	-	mV/°C
Output Noise Voltage	VN	f = 10Hz to 100k	Hz, TA = +25°C	-	45	-	μV/Vo
Ripple Rejection (Note2)	RR	f = 120Hz VI = 9V to 19V			75	-	dB
Dropout Voltage	VDrop	IO = 1A, TJ = +	25°C	-	2	-	V
Output Resistance (Note2)	rO	f = 1kHz		-	19	-	mΩ
Short Circuit Current	ISC	VI = 35V, TA= -	+25°C	-	250	-	mA
Peak Current (Note2)	Iрк	TJ =+25°C		-	2.2	-	A

### Note:

1. Load and line regulation are specified at constant junction temperature. Changes in Vo due to heating effects must be taken into account separately. Pulse testing with low duty is used.

# Electrical Characteristics (MC7808E) (Continued)

(Refer to test circuit ,0°C < TJ < 125°C, IO = 500mA, VI =14V, CI =  $0.33\mu$ F, CO=  $0.1\mu$ F, unless otherwise specified)

Dexemptor	Symbol		onditions	M	IC7808	E	Limit
Parameter	Symbol		onations	Min.	Тур.	Max.	Unit
		TJ =+25°C		7.7	8.0	8.3	
Output Voltage	Vo	$\begin{array}{l} \text{5.0mA} \leq \text{IO} \leq 1\\ \text{VI} = 10.5\text{V to }23 \end{array}$	, -	7.6	8.0	8.4	V
Line Regulation (Note1)	Poglino	TJ =+25°C	VI = 10.5V to 25V	-	5.0	160	mV
Line Regulation (Note1)	Regline	1J = +25  C	VI = 11.5V to 17V	-	2.0	80	111V
Load Regulation (Nato1)	Poglaad	TJ =+25°C	IO = 5.0mA to 1.5A	-	10	160	
Load Regulation (Note1)	Regload	1J = +25  C	I <sub>O</sub> = 250mA to 750mA	-	5.0	80	mV
Quiescent Current	lQ	TJ =+25°C	·	-	5.0	8.0	mA
Quissoont Current Change	Ale	IO = 5mA to 1.0A	ł	-	0.05	0.5	mA
Quiescent Current Change	ΔlQ	VI = 10.5A to 25	V	-	0.5	1.0	ША
Output Voltage Drift (Note2)	$\Delta V_{O}/\Delta T$	IO = 5mA		-	-0.8	-	mV/°C
Output Noise Voltage	VN	f = 10Hz to 100kH	lz, TA = +25°C	-	52	-	μV/Vo
Ripple Rejection (Note2)	RR	f = 120Hz, VI = 1	1.5V to 21.5V	56	73	-	dB
Dropout Voltage	VDrop	IO = 1A, TJ = +2	IO = 1A, TJ = +25°C		2	-	V
Output Resistance (Note2)	rO	f = 1kHz		-	17	-	mΩ
Short Circuit Current	Isc	VI = 35V, TA = +	25°C	-	230	-	mA
Peak Current (Note2)	lрк	TJ =+25°C		-	2.2	-	Α

### Note:

1. Load and line regulation are specified at constant junction temperature. Changes in V<sub>O</sub> due to heating effects must be taken into account separately. Pulse testing with low duty is used.

# Electrical Characteristics (MC7809E) (Continued)

(Refer to test circuit ,0°C < TJ < 125°C, IO = 500mA, VI =15V, CI =  $0.33\mu$ F, CO=  $0.1\mu$ F, unless otherwise specified)

Deverator	Cumhal	0	nditiono	Ν	IC7809	E	Unit
Parameter	Symbol		onditions	Min.	Тур.	Max.	Unit
		TJ = +25°C		8.65	9	9.35	
Output Voltage	Vo	$5.0 \text{mA} \le \text{IO} \le 1.0$ VI = 11.5V to 24V	, -	8.6	9	9.4	V
Line Regulation (Note1)	Poglino	TJ = +25°C	VI = 11.5V to 25V	-	6	180	mV
Line Regulation (Note1)	Regline	1J = +25 C	VI = 12V to 17V	-	2	90	IIIV
Load Population (Noto1)	Pogload	TJ = +25°C	IO = 5mA to 1.5A	-	12	180	mV
Load Regulation (Note1)	Regload	1J = +25 C	I <sub>O</sub> = 250mA to 750mA	-	4	90	IIIV
Quiescent Current	lq	TJ = +25°C	·	-	5.0	8.0	mA
Quiessont Current Change	Alo	IO = 5mA to 1.0A	ł	-	-	0.5	mA
Quiescent Current Change	ΔlQ	VI = 11.5V to 26	V	-	-	1.3	ШA
Output Voltage Drift (Note2)	$\Delta V_{O}/\Delta T$	IO = 5mA		-	-1	-	mV/°C
Output Noise Voltage	VN	f = 10Hz to 100kH	lz, TA = +25°C	-	58	-	μV/Vo
Ripple Rejection (Note2)	RR	f = 120Hz VI = 13V to 23V	f = 120Hz		71	-	dB
Dropout Voltage	VDrop	IO = 1A, TJ = +2	5°C	-	2	-	V
Output Resistance (Note2)	rO	f = 1kHz		-	17	-	mΩ
Short Circuit Current	ISC	VI = 35V, TA = +	25°C	-	250	-	mA
Peak Current (Note2)	lрк	TJ = +25°C		-	2.2	-	А

### Note:

1. Load and line regulation are specified at constant junction temperature. Changes in Vo due to heating effects must be taken into account separately. Pulse testing with low duty is used.

# Electrical Characteristics (MC7812E) (Continued)

(Refer to test circuit ,0°C < TJ < 125°C, IO = 500mA, VI =19V, CI =  $0.33\mu$ F, CO= $0.1\mu$ F, unless otherwise specified)

Parameter	Symbol	6	onditions	M	C7812	E	Unit
Farameter	Symbol		bilations	Min.	Тур.	Max.	Unit
		$T_J = +25^{\circ}C$		11.5	12	12.5	
Output Voltage	Vo	$5.0 \text{mA} \le \text{IO} \le 1.0$ VI = 14.5V to 27V		11.4	12	12.6	V
Line Regulation (Note1)	Regline	TJ = +25°C	VI = 14.5V to 30V	-	10	240	mV
	negime	1J = +25 C	VI = 16V to 22V	-	3.0	120	III V
Load Regulation (Note1)	Regload	TJ = +25°C	IO = 5mA to 1.5A	-	11	240	mV
Load Regulation (Noter)	negioau	1J = +25 C	IO = 250mA to 750mA	-	5.0	120	III V
Quiescent Current	lq	$T_J = +25^{\circ}C$	·	-	5.1	8.0	mA
Quiescent Current Change	410	$\Delta I_Q$ IO = 5mA to 1.0A		-	0.1	0.5	mA
Quiescent Guirent Ghange	ΔIQ	VI = 14.5V to 30\	/	-	0.5	1.0	ША
Output Voltage Drift (Note2)	$\Delta V_{O}/\Delta T$	IO = 5mA		-	-1	-	mV/°C
Output Noise Voltage	VN	f = 10Hz to 100kHz	z, TA = +25°C	-	76	-	μV/Vo
Ripple Rejection (Note2)	RR	f = 120Hz VI = 15V to 25V	-		71	-	dB
Dropout Voltage	VDrop	IO = 1A, TJ = +28	5°C	-	2	-	V
Output Resistance (Note2)	rO	f = 1kHz		-	18	-	mΩ
Short Circuit Current	ISC	VI = 35V, TA= +2	25°C	-	230	-	mA
Peak Current (Note2)	lрк	$T_J = +25^{\circ}C$		-	2.2	-	А

### Note:

1. Load and line regulation are specified at constant junction temperature. Changes in V<sub>O</sub> due to heating effects must be taken into account separately. Pulse testing with low duty is used.

# Electrical Characteristics (MC7815E) (Continued)

(Refer to test circuit ,0°C < TJ < 125°C, IO = 500mA, VI =23V, CI =  $0.33\mu$ F, CO= $0.1\mu$ F, unless otherwise specified)

Deremeter	Cumhal	0	onditions	N	IC7815	E	Unit
Parameter	Symbol		onaltions	Min.	Тур.	Max.	Unit
		TJ =+25°C		14.4	15	15.6	
Output Voltage	Vo	$5.0 \text{mA} \le \text{IO} \le 7$ VI = 17.5V to 3	1.0A, P <sub>O</sub> ≤ 15W 30V	14.25	15	15.75	V
Line Regulation (Note1)	Poglino	TL . 25°C	VI = 17.5V to 30V	-	11	300	mV
Line Regulation (Note1)	Regline	TJ = +25°C	VI = 20V to 26V	-	3	150	шv
			IO = 5mA to 1.5A	-	12	300	
Load Regulation (Note1)	Regload	-	I <sub>O</sub> = 250mA to 750mA	-	4	150	mV
Quiescent Current	lQ	TJ =+25°C		-	5.2	8.0	mA
Quiagoant Current Change	410	IO = 5mA  to  1	.0A	-	-	0.5	mA
Quiescent Current Change	ΔlQ	VI = 17.5V to 3	30V	-	-	1.0	ША
Output Voltage Drift (Note2)	$\Delta V_O / \Delta T$	$I_{O} = 5mA$		-	-1	-	mV/°C
Output Noise Voltage	VN	f = 10Hz to 100	kHz, TA = +25°C	-	90	-	μV/Vo
Ripple Rejection (Note2)	RR	f = 120Hz VI = 18.5V to 2	f = 120Hz VI = 18.5V to 28.5V		70	-	dB
Dropout Voltage	VDrop	IO = 1A, TJ=+25°C		-	2	-	V
Output Resistance (Note2)	rO	f = 1kHz		-	19	-	mΩ
Short Circuit Current	ISC	VI = 35V, TA =	= +25°C	-	250	-	mA
Peak Current (Note2)	lрк	TJ =+25°C		-	2.2	-	А

### Note:

1. Load and line regulation are specified at constant junction temperature. Changes in V<sub>O</sub> due to heating effects must be taken into account separately. Pulse testing with low duty is used.

# Electrical Characteristics (MC7818E) (Continued)

(Refer to test circuit ,0°C < TJ < 125°C, IO = 500mA, VI = 27V, CI =  $0.33\mu$ F, CO =  $0.1\mu$ F, unless otherwise specified)

Parameter	Symbol	6	onditions	Μ	IC7818	E	Unit
Falameter	Symbol		bhailions	Min.	Тур.	Max.	Unit
		TJ =+25°C		17.3	18	18.7	
Output Voltage	Vo	$5.0 \text{mA} \le \text{IO} \le 1.0 \text{A}$ VI = 21V to 33V	A, PO ≤15W	17.1	18	18.9	V
Line Regulation (Note1)	Poglino	TJ =+25°C	VI = 21V to 33V	-	15	360	mV
	Regline	15=+25 0	VI = 24V to 30V	-	5	180	шv
Load Regulation (Note1)	Regload	T I – 125°C	$I_{O} = 5mA$ to 1.5A	-	15	360	mV
Load Regulation (Note I)	negioau	TJ =+25°C	I <sub>O</sub> = 250mA to 750mA	-	5.0	180	шv
Quiescent Current	lq	$T_J = +25^{\circ}C$	·	-	5.2	8.0	mA
Quiescent Current Change	ΔlQ	IO = 5mA to 1.0A	l l	-	-	0.5	mA
Quiescent Guirent Ghange	ΔIQ	$V_I = 21V$ to $33V$		-	-	1	1114
Output Voltage Drift (Note2)	$\Delta V_{O} / \Delta T$	IO = 5mA		-	-1	-	mV/°C
Output Noise Voltage	VN	f = 10Hz to 100kHz	z, TA = +25°C	-	110	-	μV/Vo
Ripple Rejection (Note2)	RR	f = 120Hz VI = 22V to 32V	-		69	-	dB
Dropout Voltage	VDrop	$I_{O} = 1A, T_{J} = +28$	5°C	-	2	-	V
Output Resistance (Note2)	rO	f = 1kHz		-	22	-	mΩ
Short Circuit Current	ISC	VI = 35V, TA = +2	25°C	-	250	-	mA
Peak Current (Note2)	IPK	TJ = +25°C		-	2.2	-	А

#### Note:

1. Load and line regulation are specified at constant junction temperature. Changes in V<sub>O</sub> due to heating effects must be taken into account separately. Pulse testing with low duty is used.

# Electrical Characteristics (MC7824E) (Continued)

(Refer to test circuit ,0°C < TJ < 125°C, IO = 500mA, VI =33V, CI =  $0.33\mu$ F, CO= $0.1\mu$ F, unless otherwise specified)

Parameter	Cumhal	6	onditions	Μ	C7824	E	Unit
Parameter	Symbol		onations	Min.	Тур.	Max.	Unit
		$T_J = +25^{\circ}C$		23	24	25	
Output Voltage	Vo	$5.0\text{mA} \le \text{IO} \le 1.0$ VI = 27V to 38V	A, P <sub>O</sub> ≤ 15W	22.8	24	25.25	V
Line Regulation (Note1)	Regline	TJ = +25°C	VI = 27V to 38V	-	17	480	mV
	negime	1J = +25 C	VI = 30V to 36V	-	6	240	III V
Load Regulation (Note1)	Regload	TJ = +25°C	$I_{O} = 5mA$ to 1.5A	-	15	480	mV
Load Regulation (Note1)	negioau	1J = +25 C	I <sub>O</sub> = 250mA to 750mA	-	5.0	240	III V
Quiescent Current	lQ	TJ = +25°C	·	-	5.2	8.0	mA
Quiescent Current Change	ΔΙο	IO = 5mA to 1.0A	l .	-	0.1	0.5	mA
Quiescent Current Change	ΔIQ	VI = 27V to 38V		-	0.5	1	MA
Output Voltage Drift (Note2)	$\Delta VO/\Delta T$	IO = 5mA		-	-1.5	-	mV/°C
Output Noise Voltage	VN	f = 10Hz to 100kH	z, TA = +25°C	-	60	-	μV/Vo
Ripple Rejection (Note2)	RR	f = 120Hz VI = 28V to 38V	-		67	-	dB
Dropout Voltage	VDrop	IO = 1A, TJ= +25	°℃	-	2	-	V
Output Resistance (Note2)	rO	f = 1kHz		-	28	-	mΩ
Short Circuit Current	ISC	VI = 35V, TA= +2	25°C	-	230	-	mA
Peak Current (Note2)	Iрк	$T_J = +25^{\circ}C$		-	2.2	-	А

### Note:

1. Load and line regulation are specified at constant junction temperature. Changes in V<sub>O</sub> due to heating effects must be taken into account separately. Pulse testing with low duty is used.

# Electrical Characteristics (MC7805AE) (Continued)

(Refer to the test circuits.  $0^{\circ}C < T_J < 125^{\circ}C$ ,  $I_0 = 1A$ ,  $V_I = 10V$ ,  $C_I = 0.33\mu$ F,  $C_O = 0.1\mu$ F, unless otherwise specified)

Parameter	Symbol	Co	onditions	Min.	Тур.	Max.	Unit
		$T_J = +25^{\circ}C$		4.9	5	5.1	
Output Voltage	Vo	IO = 5mA  to  1 $V_I = 7.5V \text{ to } 2$		4.8	5	5.2	V
		VI = 7.5V to 2	5V, IO = 500mA	-	5	50	
Line Regulation (Note1)	Pagling	VI = 8V to 12\	/	-	3	50	mV
	Regline	T.J = +25°C	VI= 7.3V to 20V	-	5	50	IIIV
		1J = +25  C	VI= 8V to 12V	-	1.5	25	
		$T_{J} = +25^{\circ}C, I_{C}$	) = 5mA to 1.5A	-	9	100	
Load Regulation (Note1)	Regload	$I_{O} = 5mA$ to 1	A	-	9	100	mV
		$I_{O} = 250 \text{mA to}$	I <sub>O</sub> = 250mA to 750mA		4	50	
Quiescent Current	lq	TJ = +25°C		-	5.0	6	mA
		IO = 5mA to 1	A	-	-	0.5	
Quiescent Current Change	ΔlQ	VI = 8 V to 25	V, IO = 500mA	-	-	0.8	mA
		VI = 7.5V to 2	0V, TJ = +25°C	-	-	0.8	
Output Voltage Drift (Note2)	$\Delta V / \Delta T$	lo = 5mA		-	-0.8	-	mV/°C
Output Noise Voltage	VN	f = 10Hz to 10 TA =+25°C	0kHz	-	10	-	μV/Vo
Ripple Rejection (Note2)	RR		f = 120Hz, IO = 500mA VI = 8V to 18V		68	-	dB
Dropout Voltage	VDrop	IO = 1A, TJ =+25°C		-	2	-	V
Output Resistance (Note2)	rO	f = 1kHz		-	17	-	mΩ
Short Circuit Current	Isc	VI = 35V, TA =	=+25°C	-	250	-	mA
Peak Current (Note2)	lрк	TJ = +25°C		-	2.2	-	A

### Note:

1. Load and line regulation are specified at constant junction temperature. Change in V<sub>O</sub> due to heating effects must be taken into account separately. Pulse testing with low duty is used.

# Electrical Characteristics (MC7806AE) (Continued)

(Refer to the test circuits.  $0^{\circ}C < T_J < 125^{\circ}C$ ,  $I_0 = 1A$ ,  $V_I = 11V$ ,  $C_I = 0.33\mu$ F,  $C_O = 0.1\mu$ F, unless otherwise specified)

Parameter	Symbol	Co	onditions	Min.	Тур.	Max.	Unit
		TJ =+25°C		5.58	6	6.12	
Output Voltage	Vo	IO = 5mA to 1 VI = 8.6V to 2		5.76	6	6.24	V
		VI = 8.6V to 2	5V, I <u>O</u> = 500mA	-	5	60	
Line Regulation (Note1)	Regline	VI = 9V to 13	/	-	3	60	mV
	negime	T.I =+25°C	VI = 8.3V to 21V	-	5	60	IIIV
		15=+25 0	VI = 9V to 13V	-	1.5	30	
		TJ =+25°C, IC	) = 5mA to 1.5A	-	9	100	
Load Regulation (Note1)	Regload	I <sub>O</sub> = 5mA to 1	A	-	4	100	mV
		$I_{O} = 250 \text{mA to}$	o 750mA	-	5.0	50	
Quiescent Current	lQ	TJ =+25°C		-	4.3	6	mA
		IO = 5mA to 1	A	-	-	0.5	
Quiescent Current Change	ΔlQ	VI = 9V to 25V	√, IO = 500mA	-	-	0.8	mA
		VI = 8.5V to 2	1V, TJ = +25°C	-	-	0.8	
Output Voltage Drift (Note2)	$\Delta V / \Delta T$	IO = 5mA		-	-0.8	-	mV/°C
Output Noise Voltage	VN	f = 10Hz to 10 TA = +25°C	00kHz	-	10	-	μV/Vo
Ripple Rejection (Note2)	RR		f = 120Hz, IO = 500mA VI = 9V to 19V		65	-	dB
Dropout Voltage	VDrop	$I_{O} = 1A, T_{J} = +25^{\circ}C$		-	2	-	V
Output Resistance (Note2)	rO	f = 1kHz		-	17	-	mΩ
Short Circuit Current	ISC	VI = 35V, TA =	=+25°C	-	250	-	mA
Peak Current (Note2)	IPK	$T_J = +25^{\circ}C$		-	2.2	-	А

### Note:

1. Load and line regulation are specified at constant junction temperature. Change in V<sub>O</sub> due to heating effects must be taken into account separately. Pulse testing with low duty is used.

# Electrical Characteristics (MC7808AE) (Continued)

(Refer to the test circuits.  $0^{\circ}C < T_J < 125^{\circ}C$ ,  $I_0 = 1A$ ,  $V_I = 14V$ ,  $C_I = 0.33\mu$ F,  $C_O = 0.1\mu$ F, unless otherwise specified)

Parameter	Symbol	Co	onditions	Min.	Тур.	Max.	Unit
		TJ =+25°C		7.84	8	8.16	
Output Voltage	Vo	$I_{O} = 5mA \text{ to } 1$ VI = 10.6V to		7.7	8	8.3	V
		VI= 10.6V to 2	25V, IO = 500mA	-	6	80	
Line Regulation (Note1)	Regline	VI= 11V to 17	V	-	3	80	mV
	negiine	T.J =+25°C	VI= 10.4V to 23V	-	6	80	111 V
		1J =+25 C	V <sub>I</sub> = 11V to 17V	-	2	40	
		TJ =+25°C, IC	) = 5mA to 1.5A	-	12	100	
Load Regulation (Note1)	Regload	I <sub>O</sub> = 5mA to 1	A	-	12	100	mV
		IO = 250mA to	o 750mA	-	5	50	
Quiescent Current	lQ	TJ =+25°C		-	5.0	6	mA
		IO = 5mA to 1A		-	-	0.5	
Quiescent Current Change	ΔlQ	VI = 11V to 25V, IO = 500mA		-	-	0.8	mA
		VI= 10.6V to 2	23V, TJ =+25°C	-	-	0.8	
Output Voltage Drift (Note2)	$\Delta V / \Delta T$	$I_{O} = 5mA$		-	-0.8	-	mV/°C
Output Noise Voltage	V <sub>N</sub>	f = 10Hz to 10 TA =+25°C	0kHz	-	10	-	μV/Vo
Ripple Rejection (Note2)	RR		f = 120Hz, IO = 500mA VI = 11.5V to 21.5V		62	-	dB
Dropout Voltage	VDrop	IO = 1A, TJ =+25°C		-	2	-	V
Output Resistance (Note2)	rO	f = 1kHz		-	18	-	mΩ
Short Circuit Current	ISC	VI = 35V, TA =	=+25°C	-	250	-	mA
Peak Current (Note2)	Iрк	$T_J = +25^{\circ}C$		-	2.2	-	А

### Note:

1. Load and line regulation are specified at constant junction temperature. Change in V<sub>O</sub> due to heating effects must be taken into account separately. Pulse testing with low duty is used.

# Electrical Characteristics (MC7809AE) (Continued)

(Refer to the test circuits.  $0^{\circ}C < T_J < 125^{\circ}C$ ,  $I_0 = 1A$ ,  $V_I = 15V$ ,  $C_I = 0.33\mu$ F,  $C_O = 0.1\mu$ F, unless otherwise specified)

Parameter	Symbol	Conditions		Min.	Тур.	Max.	Unit
TJ =		$T_J = +25^{\circ}C$		8.82	9.0	9.18	v
Output Voltage	Vo	$I_{O} = 5mA \text{ to } 1A, P_{O} \le 15W$ VI = 11.2V to 24V		8.65	9.0	9.35	
		VI = 11.7V to 2	25V, IO = 500mA	-	6	90	
Line Regulation (Note1)	Poglino	VI= 12.5V to 1	19V	-	4	45	mV
	Regline	T.I =+25°C	VI = 11.5V to 24V	-	6	90	111V
		1J =+25 C	VI = 12.5V to 19V	-	2	45	1
		TJ =+25°C, IC	) = 5mA to 1.0A	-	12	100	
Load Regulation (Note1)	Regload	I <sub>O</sub> = 5mA to 1.0A		-	12	100	mV
		I <sub>O</sub> = 250mA to 750mA		-	5	50	
Quiescent Current	lQ	TJ = +25°C		-	5.0	6.0	mA
	ΔlQ	VI = 11.7V to 25V, TJ = +25°C		-	-	0.8	
Quiescent Current Change		VI = 12V to 25V, IO = 500mA		-	-	0.8	mA
		IO = 5mA to 1.0A		-	-	0.5	
Output Voltage Drift (Note2)	$\Delta V / \Delta T$	IO = 5mA		-	-1.0	-	mV/°C
Output Noise Voltage	VN	f = 10Hz to 100kHz TA = +25°C		-	10	-	μV/Vo
Ripple Rejection (Note2)	RR	f = 120Hz, IO = 500mA VI = 12V to 22V		-	62	-	dB
Dropout Voltage	VDrop	IO = 1A, TJ =+25°C		-	2.0	-	V
Output Resistance (Note2)	rO	f = 1kHz		-	17	-	mΩ
Short Circuit Current	ISC	VI = 35V, TA = +25°C		-	250	-	mA
Peak Current (Note2)	IPK	TJ = +25°C		-	2.2	-	Α

### Note:

1. Load and line regulation are specified at constant, junction temperature. Change in V<sub>O</sub> due to heating effects must be taken into account separately. Pulse testing with low duty is used.

# Electrical Characteristics (MC7812AE) (Continued)

(Refer to the test circuits.  $0^{\circ}C < T_J < 125^{\circ}C$ ,  $I_0 = 1A$ ,  $V_I = 19V$ ,  $C_I = 0.33\mu$ F,  $C_O = 0.1\mu$ F, unless otherwise specified)

Parameter	Symbol	Conditions		Min.	Тур.	Max.	Unit
	TJ =+25°C		11.75	12	12.25		
Output Voltage	Vo	IO = 5mA to 1A, PO ≤15W VI = 14.8V to 27V		11.5	12	12.5	V
		VI= 14.8V to 3	VI= 14.8V to 30V, IO = 500mA		10	120	
Line Regulation (Note1)	Poglino	VI= 16V to 22	V	-	4	120	.,
	Regline	TL	VI= 14.5V to 27V	-	10	120	mV
		TJ =+25°C	VI= 16V to 22V	-	3	60	1
		TJ =+25°C, IC	) = 5mA to 1.5A	-	12	100	
Load Regulation (Note1)	Regload	I <sub>O</sub> = 5mA to 1.0A		-	12	100	mV
		I <sub>O</sub> = 250mA to 750mA		-	5	50	-
Quiescent Current	lq	TJ =+25°C		-	5.1	6.0	mA
	ΔlQ	VI = 15V to 30V, TJ=+25°C		-		0.8	
Quiescent Current Change		VI = 14V to 27V, IO = 500mA		-		0.8	mA
		IO = 5mA to 1.0A		-		0.5	]
Output Voltage Drift (Note2)	$\Delta V / \Delta T$	IO = 5mA		-	-1.0	-	mV/°C
Output Noise Voltage	VN	f = 10Hz to 100kHz TA =+25°C		-	10	-	μV/Vo
Ripple Rejection (Note2)	RR	f = 120Hz, IO = 500mA VI = 14V to 24V		-	60	-	dB
Dropout Voltage	VDrop	IO = 1A, TJ =+25°C		-	2.0	-	V
Output Resistance (Note2)	rO	f = 1kHz		-	18	-	mΩ
Short Circuit Current	Isc	VI= 35V, TA =+25°C		-	250	-	mA
Peak Current (Note2)	lрк	TJ=+25°C		-	2.2	-	Α

### Note:

1. Load and line regulation are specified at constant junction temperature. Change in V<sub>O</sub> due to heating effects must be taken into account separately. Pulse testing with low duty is used.

# Electrical Characteristics (MC7815AE) (Continued)

(Refer to the test circuits.  $0^{\circ}C < T_J < 125^{\circ}C$ ,  $I_0 = 1A$ ,  $V_I = 23V$ ,  $C_I = 0.33\mu$ F,  $C_O = 0.1\mu$ F, unless otherwise specified)

Parameter	Symbol	Conditions		Min.	Тур.	Max.	Unit
		$T_J =+25^{\circ}C$ $IO = 5mA \text{ to } 1A, PO \le 15W$ $V_I = 17.7V \text{ to } 30V$		14.7	15	15.3	V
Output Voltage	Vo			14.4	15	15.6	
		VI = 17.9V to 3	30V, IO = 500mA	-	10	150	
Line Regulation (Note1)	Regline	VI = 20V to 26	SV	-	5	150	mV
	negime	TL	VI = 17.5V to 30V	-	11	150	IIIV
		TJ =+25°C	VI = 20V to 26V	-	3	75	1
		TJ =+25°C, IC	) = 5mA to 1.5A	-	12	100	
Load Regulation (Note1)	Regload	$I_O = 5mA$ to 1.0A		-	12	100	mV
		IO = 250mA to 750mA		-	5	50	
Quiescent Current	lq	TJ =+25°C		-	5.2	6.0	mA
		VI = 17.5V to 30V, TJ =+25°C		-	-	0.8	
Quiescent Current Change	ΔlQ	VI = 17.5V to 30V, IO = 500mA		-	-	0.8	mA
		IO = 5mA to 1.0A		-	-	0.5	1
Output Voltage Drift (Note2)	$\Delta V / \Delta T$	IO = 5mA		-	-1.0	-	mV/°C
Output Noise Voltage	VN	f = 10Hz to 100kHz TA =+25°C		-	10	-	μV/Vo
Ripple Rejection (Note2)	RR	f = 120Hz, IO = 500mA VI = 18.5V to 28.5V		-	58	-	dB
Dropout Voltage	VDrop	IO = 1A, TJ =+25°C		-	2.0	-	V
Output Resistance (Note2)	rO	f = 1kHz		-	19	-	mΩ
Short Circuit Current	Isc	VI = 35V, TA =+25°C		-	250	-	mA
Peak Current (Note2)	lрк	TJ =+25°C		-	2.2	-	А

### Note:

1. Load and line regulation are specified at constant junction temperature. Change in V<sub>O</sub> due to heating effects must be taken into account separately. Pulse testing with low duty is used.

# Electrical Characteristics (MC7818AE) (Continued)

(Refer to the test circuits.  $0^{\circ}C < T_J < 125^{\circ}C$ ,  $I_0 = 1A$ ,  $V_I = 27V$ ,  $C_I = 0.33\mu$ F,  $C_O = 0.1\mu$ F, unless otherwise specified)

Parameter	Symbol	Conditions		Min.	Тур.	Max.	Unit
		$\begin{array}{l} T_J =+25^{\circ}C \\ I_O = 5mA \mbox{ to } 1A, \mbox{ P}_O \leq 15W \\ V_I = 21V \mbox{ to } 33V \end{array}$		17.64	18	18.36	V
Output Voltage	Vo			17.3	18	18.7	
		VI= 21V to 33	VI= 21V to 33V, IO = 500mA		15	180	
Line Regulation (Note1)	Regline	VI= 21V to 33	V	-	5	180	mV
	negime	TJ =+25°C	VI= 20.6V to 33V	-	15	180	111 V
		15=+25 0	VI= 24V to 30V	-	5	90	1
		TJ =+25°C, IC	) = 5mA to 1.5A	-	15	100	
Load Regulation (Note1)	Regload	$I_O = 5mA$ to 1.0A		-	15	100	mV
		I <sub>O</sub> = 250mA to 750mA		-	7	50	
Quiescent Current	lQ	TJ =+25°C		-	5.2	6.0	mA
		VI = 21V to 33	3V, TJ=+25°C	-	-	0.8	
Quiescent Current Change	ΔlQ	VI = 21V to 33V, IO = 500mA		-	-	0.8	mA
		IO = 5mA to 1.0A		-	-	0.5	
Output Voltage Drift (Note2)	$\Delta V / \Delta T$	IO = 5mA		-	-1.0	-	mV/°C
Output Noise Voltage	VN	f = 10Hz to 100kHz TA =+25°C		-	10	-	μV/Vo
Ripple Rejection (Note2)	RR	f = 120Hz, IO = 500mA VI = 22V to 32V		-	57	-	dB
Dropout Voltage	VDrop	IO = 1A, TJ =+25°C		-	2.0	-	V
Output Resistance (Note2)	rO	f = 1kHz		-	19	-	mΩ
Short Circuit Current	ISC	VI= 35V, TA =+25°C		-	250	-	mA
Peak Current (Note2)	lрк	TJ=+25°C		-	2.2	-	Α

### Note:

1. Load and line regulation are specified at constant junction temperature. Change in V<sub>O</sub> due to heating effects must be taken into account separately. Pulse testing with low duty is used.

# Electrical Characteristics (MC7824AE) (Continued)

(Refer to the test circuits.  $0^{\circ}C < T_J < 125^{\circ}C$ ,  $I_0 = 1A$ ,  $V_I = 33V$ ,  $C_I = 0.33\mu$ F,  $C_O = 0.1\mu$ F, unless otherwise specified)

Parameter	Symbol	Conditions		Min.	Тур.	Max.	Unit
		$T_J =+25^{\circ}C$ IO = 5mA to 1A, PO ≤15W VI = 27.3V to 38V		23.5	24	24.5	V
Output Voltage	Vo			23	24	25	
		VI= 27V to 38	VI= 27V to 38V, IO = 500mA		18	240	
Line Regulation (Note1)	Regline	VI= 21V to 33	V	-	6	240	mV
	negime	TJ =+25°C	VI= 26.7V to 38V	-	18	240	111 V
		15=+25 0	VI= 30V to 36V	-	6	120	1
		TJ =+25°C, IC	) = 5mA to 1.5A	-	15	100	
Load Regulation (Note1)	Regload	$I_O = 5mA$ to 1.0A		-	15	100	mV
		I <sub>O</sub> = 250mA to 750mA		-	7	50	
Quiescent Current	lq	TJ =+25°C		-	5.2	6.0	mA
		VI = 27.3V to	38V, TJ =+25°C	-	-	0.8	
Quiescent Current Change	ΔlQ	VI = 27.3V to 38V, IO = 500mA		-	-	0.8	mA
		IO = 5mA to 1.0A		-	-	0.5	1
Output Voltage Drift (Note2)	$\Delta V / \Delta T$	IO = 5mA		-	-1.5	-	mV/°C
Output Noise Voltage	VN	f = 10Hz to 100kHz TA = 25°C		-	10	-	μV/Vo
Ripple Rejection (Note2)	RR	f = 120Hz, IO = 500mA VI = 28V to 38V		-	54	-	dB
Dropout Voltage	VDrop	IO = 1A, TJ =+25°C		-	2.0	-	V
Output Resistance (Note2)	rO	f = 1kHz		-	20	-	mΩ
Short Circuit Current	ISC	VI = 35V, TA =+25°C		-	250	-	mA
Peak Current (Note2)	lрк	TJ =+25°C		-	2.2	-	Α

### Note:

1. Load and line regulation are specified at constant junction temperature. Change in VO due to heating effects must be taken into account separately. Pulse testing with low duty is used.



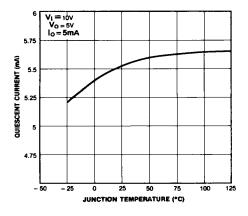


Figure 1. Quiescent Current

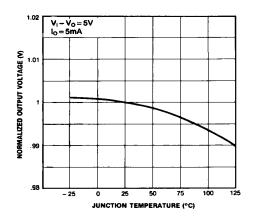


Figure 3. Output Voltage

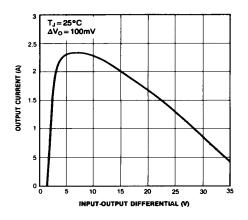


Figure 2. Peak Output Current

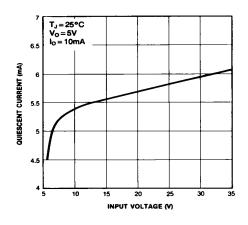
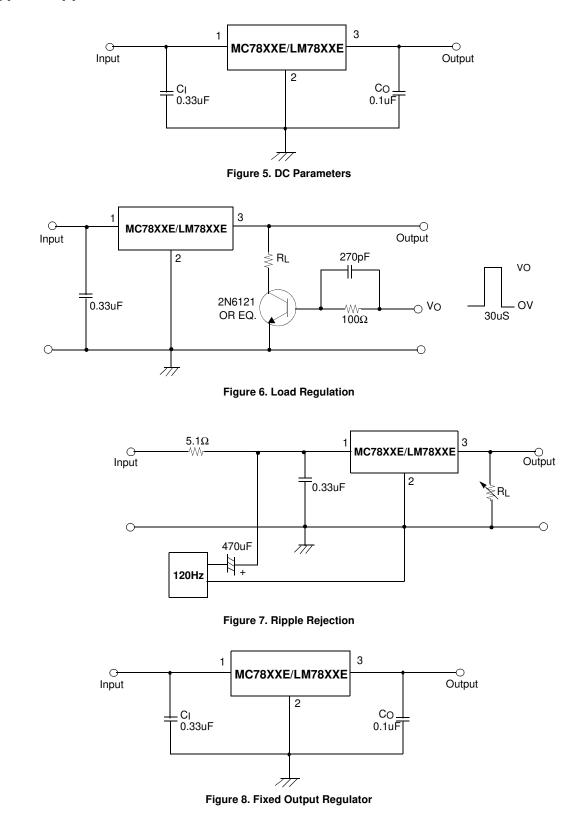
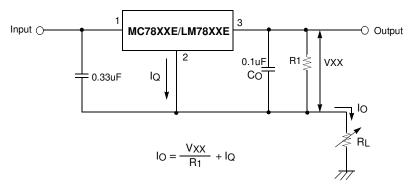


Figure 4. Quiescent Current

# **Typical Applications**





### Figure 9. Constant Current Regulator

### Notes:

- (1) To specify an output voltage. substitute voltage value for "XX." A common ground is required between the input and the Output voltage. The input voltage must remain typically 2.0V above the output voltage even during the low point on the input ripple voltage.
- (2) CI is required if regulator is located an appreciable distance from power Supply filter.
- (3) Co improves stability and transient response.

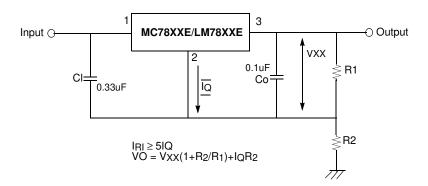


Figure 10. Circuit for Increasing Output Voltage

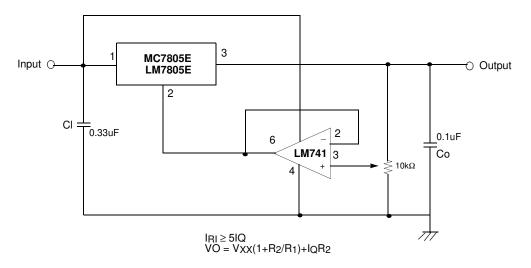
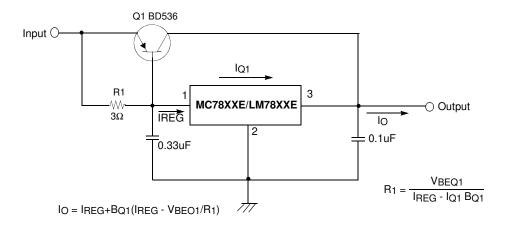
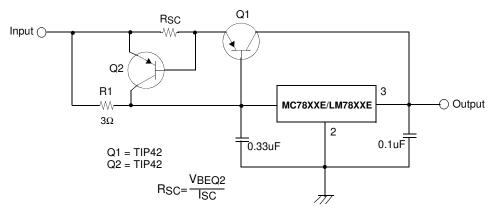
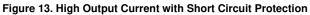


Figure 11. Adjustable Output Regulator (7 to 30V)









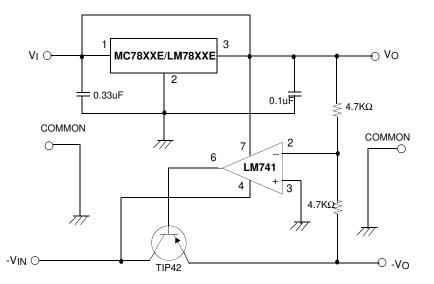


Figure 14. Tracking Voltage Regulator

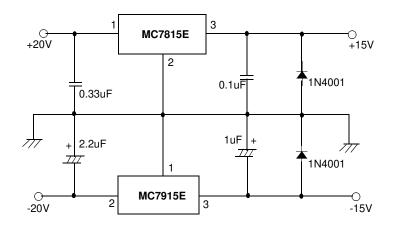


Figure 15. Split Power Supply (±15V-1A)

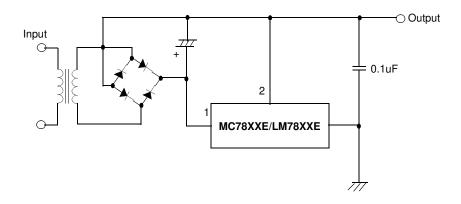


Figure 16. Negative Output Voltage Circuit

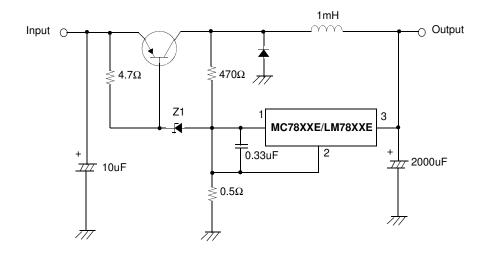


Figure 17. Switching Regulator

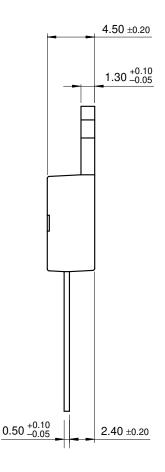
### **Mechanical Dimensions**

### Package

### **Dimensions in millimeters**

**TO-220**  $9.90{\scriptstyle~\pm 0.20}$ 1.30 ±0.10 (8.70) **2.80** ±0.10 (1.70) ø3.60 ±0.10 (3.70) 18.95MAX.  $15.90 \pm 0.20$ **9.20** ±0.20 (1.46) (3.00) (A5°) (1.00) **13.08** ±0.20  $10.08 \pm 0.30$ 1.52 ±0.10 1.27 ±0.10 0.80 ±0.10 2.54TYP 2.54TYP [2.54 ±0.20] [2.54 ±0.20] <u>\_\_\_\_</u> 

 $10.00 \pm 0.20$ 

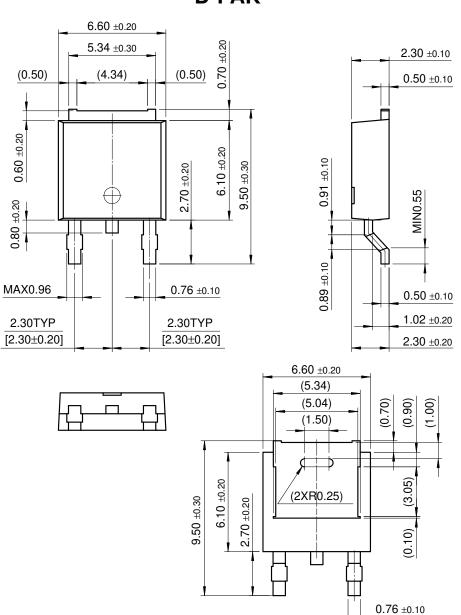


23

# Mechancal Dimensions (Continued)

### Package

**Dimensions in millimeters** 



D-PAK

# **Ordering Information**

Product Number	Output Voltage Tolerance	Package	Operating Temperature			
LM7805ECT	±4%	TO-220	0 ~ +125°C			
Product Number	Output Voltage Tolerance	Package	Operating Temperature			
MC7805ECT						
MC7806ECT						
MC7808ECT						
MC7809ECT		TO-220				
MC7812ECT		10-220				
MC7815ECT						
MC7818ECT	±4%					
MC7824ECT						
MC7805ECDT						
MC7806ECDT						
MC7808ECDT		D-PAK	0 ~ +125°C			
MC7809ECDT						
MC7812ECDT						
MC7805AECT						
MC7806AECT						
MC7808AECT		TO-220				
MC7809AECT	+2%					
MC7812AECT	<u> </u>	10-220				
MC7815AECT						
MC7818AECT						
MC7824AECT						

#### DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

#### LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF THE PRESIDENT OF FAIRCHILD SEMICONDUCTOR CORPORATION. As used herein:

- Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, and (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury of the user.
- A critical component in any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

www.fairchildsemi.com