Low Noise Silicon Bipolar RF Transistor
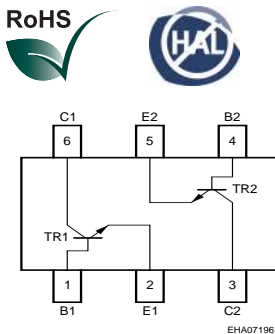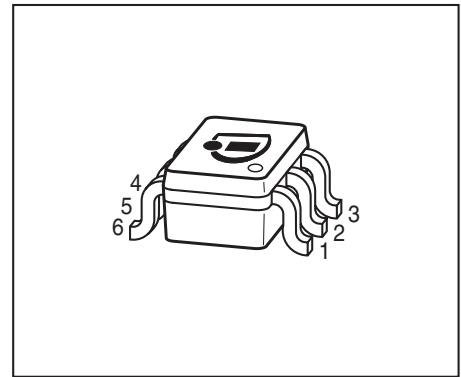
- For low noise, high-gain broadband amplifiers at collector currents from 2 mA to 30 mA

- f_T = 8 GHz, NF_{min} = 0.9 dB at 900 MHz

- Two (galvanic) internal isolated Transistor in one package

- For orientation in reel see package information below

- Pb-free (RoHS compliant) and halogen-free package with visible leads

- Qualification report according to AEC-Q101 available

ESD (**E**lectro**s**tatic **d**ischarge) sensitive device, observe handling precaution!

Type	Marking	Pin Configuration						Package
BFS483	RHs	1=B	2=E	3=C	4=B	5=E	6=C	SOT363

Maximum Ratings at T_A = 25 °C, unless otherwise specified

Parameter	Symbol	Value	Unit
Collector-emitter voltage	V_{CEO}	12	V
Collector-emitter voltage	V_{CES}	20	
Collector-base voltage	V_{CBO}	20	
Emitter-base voltage	V_{EBO}	2	
Collector current	I_C	65	mA
Base current	I_B	5	
Total power dissipation[1) $T_S \leq 40$ °C	P_{tot}	450	mW
Junction temperature	T_J	150	°C
Ambient temperature	T_A	-65 ... 150	
Storage temperature	T_{Stg}	-65 ... 150	

Thermal Resistance

Parameter	Symbol	Value	Unit
Junction - soldering point[2)	R_{thJS}	245	K/W

Electrical Characteristics at T_A = 25 °C, unless otherwise specified

Parameter	Symbol	Values			Unit
		min.	typ.	max.	
DC Characteristics					
Collector-emitter breakdown voltage I_C = 1 mA, I_B = 0	$V_{(BR)CEO}$	12	-	-	V
Collector-emitter cutoff current V_{CE} = 20 V, V_{BE} = 0	I_{CES}	-	-	100	µA
Collector-base cutoff current V_{CB} = 10 V, I_E = 0	I_{CBO}	-	-	100	nA
Emitter-base cutoff current V_{EB} = 1 V, I_C = 0	I_{EBO}	-	-	1	µA
DC current gain I_C = 15 mA, V_{CE} = 8 V, pulse measured	h_{FE}	70	100	140	-

[1]T_S is measured on the collector lead at the soldering point to the pcb

[2]For the definition of R_{thJS} please refer to Application Note AN077 (Thermal Resistance Calculation)

2013-07-25

**Electrical Characteristics** at T_A = 25 °C, unless otherwise specified

Parameter	Symbol	Values			Unit
		min.	typ.	max.	
AC Characteristics (verified by random sampling)					
Transition frequency I_C = 25 mA, V_{CE} = 8 V, f = 500 MHz	f_T	6	8	-	GHz
Collector-base capacitance V_{CB} = 10 V, f = 1 MHz, V_{BE} = 0 , emitter grounded	C_{cb}	-	0.34	0.54	pF
Collector emitter capacitance V_{CE} = 10 V, f = 1 MHz, V_{BE} = 0 , base grounded	C_{ce}	-	0.13	-	
Emitter-base capacitance V_{EB} = 0.5 V, f = 1 MHz, V_{CB} = 0 , collector grounded	C_{eb}	-	1.1	-	
Minimum noise figure I_C = 5 mA, V_{CE} = 8 V, Z_S = Z_{Sopt}, f = 900 MHz I_C = 5 mA, V_{CE} = 8 V, Z_S = Z_{Sopt}, f = 1.8 GHz	NF_{min}	 - -	 0.9 1.4	 - -	dB
Power gain, maximum stable[1)] I_C = 15 mA, V_{CE} = 8 V, Z_S = Z_{Sopt}, Z_L = Z_{Lopt} , f = 900 MHz	G_{ms}	-	19	-	dB
Power gain, maximum available[2)] I_C = 15 mA, V_{CE} = 8 V, Z_S = Z_{Sopt}, Z_L = Z_{Lopt} , f = 1.8 GHz	G_{ma}	-	12.5	-	dB
Transducer gain I_C = 15 mA, V_{CE} = 8 V, Z_S = Z_L = 50 Ω, f = 900 MHz I_C = 15 mA, V_{CE} = 8 V, Z_S = Z_L = 50 Ω, f = 1.8 MHz	$|S_{21e}|^2$	 - -	 15.5 10	 - -	dB

[1]$G_{ms} = |S_{21} / S_{12}|$

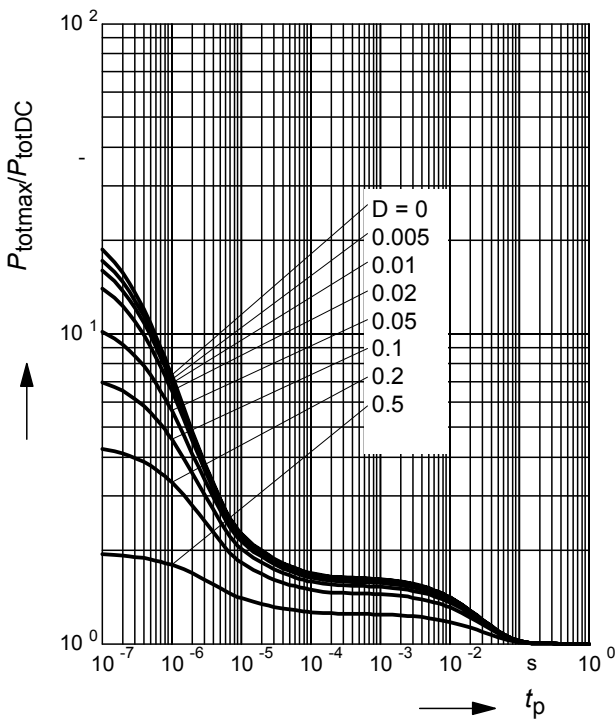[2]$G_{ma} = |S_{21e} / S_{12e}| (k-(k^2-1)^{1/2})$

Total power dissipation $P_{tot} = f(T_S)$

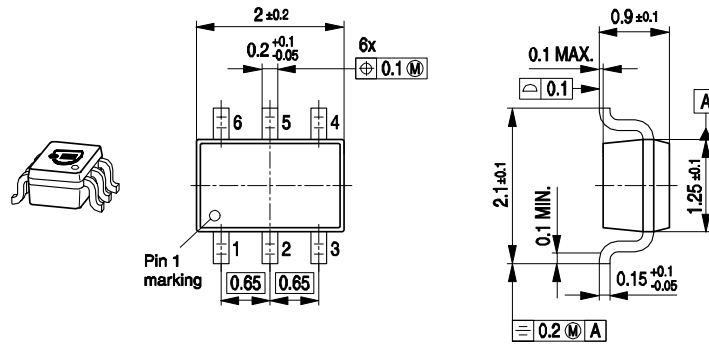**Permissible Pulse Load** $R_{thJS} = f(t_p)$
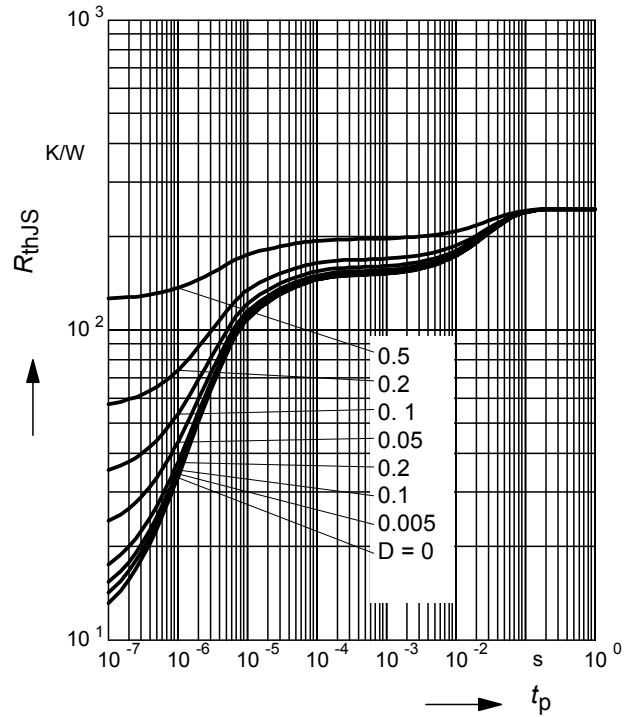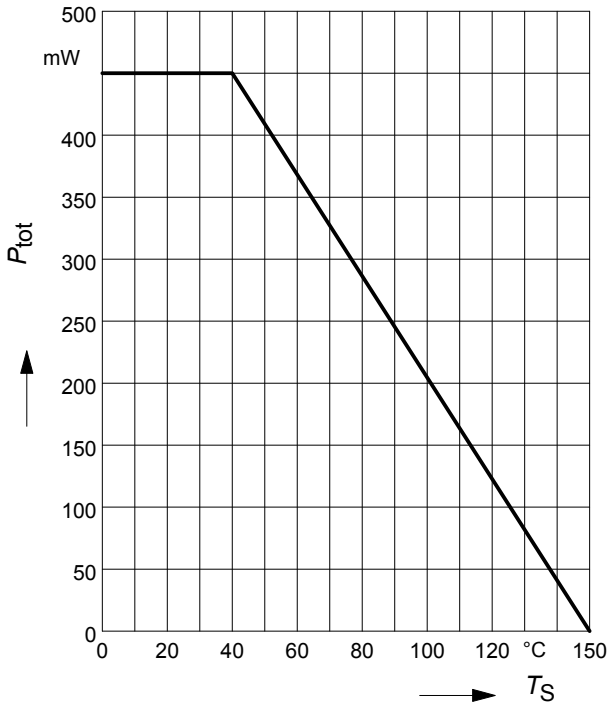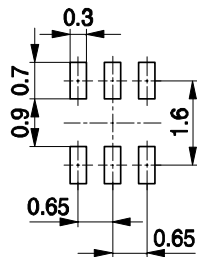

Permissible Pulse Load

$P_{totmax}/P_{totDC} = f(t_p)$

2013-07-25

Package Outline

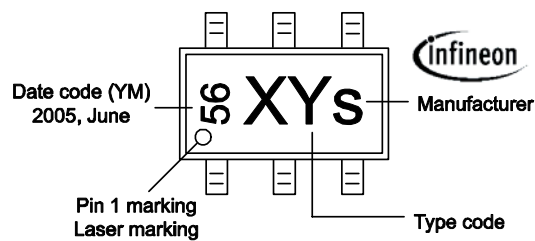## Foot Print

Marking Layout (Example)

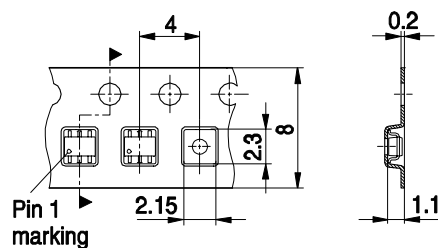Small variations in positioning of
Date code, Type code and Manufacture are possible.

Date code (YM)
2005, June

56 XYs

Manufacturer

Pin 1 marking
Laser marking

Type code

Standard Packing

Reel ø180 mm = 3.000 Pieces/Reel
Reel ø330 mm = 10.000 Pieces/Reel

For symmetric types no defined Pin 1 orientation in reel.

Pin 1
marking

Edition 2009-11-16

Published by
Infineon Technologies AG
81726 Munich, Germany

Legal Disclaimer

The information given in this document shall in no event be regarded as a guarantee of conditions or characteristics. With respect to any examples or hints given herein, any typical values stated herein and/or any information regarding the application of the device, Infineon Technologies hereby disclaims any and all warranties and liabilities of any kind, including without limitation, warranties of non-infringement of intellectual property rights of any third party.

Information

For further information on technology, delivery terms and conditions and prices, please contact the nearest Infineon Technologies Office (<www.infineon.com>).

Warnings

Due to technical requirements, components may contain dangerous substances. For information on the types in question, please contact the nearest Infineon Technologies Office.
Infineon Technologies components may be used in life-support devices or systems only with the express written approval of Infineon Technologies, if a failure of such components can reasonably be expected to cause the failure of that life-support device or system or to affect the safety or effectiveness of that device or system. Life support devices or systems are intended to be implanted in the human body or to support and/or maintain and sustain and/or protect human life. If they fail, it is reasonable to assume that the health of the user or other persons may be endangered.

2013-07-25