

PJQ5410

30V N-Channel Enhancement Mode MOSFET

Voltage

Current 80A

Features

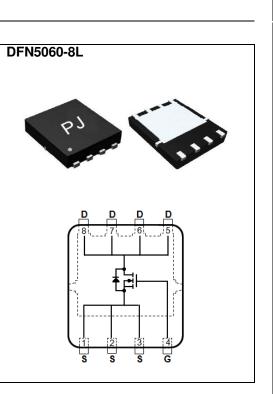
- $R_{DS(ON)}$, $V_{GS}@10V$, $I_D@20A < 6m\Omega$
- R_{DS(ON)}, V_{GS}@4.5V,I_D@10A<9mΩ
- High switching speed
- Improved dv/dt capability
- Low reverse transfer capacitance
- Lead free in compliance with EU RoHS 2.0

30 V

Green molding compound as per IEC 61249 standard

Mechanical Data

- Case: DFN5060-8L Package
- Terminals: Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 0.0028 ounces, 0.08 grams



Maximum Ratings and Thermal Characteristics ($T_A=25^{\circ}C$ unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS	
Drain-Source Voltage		V _{DS}	30	V	
Gate-Source Voltage		V_{GS}	<u>+</u> 20	V	
Continuous Drain Current	T _C =25°C	I _D	80		
	T _C =100°C		50	А	
Pulsed Drain Current ^(Note 1)	T _C =25°C	I _{DM}	320		
Power Dissipation	T _C =25°C		62	14/	
	T _c =100°C	PD	25	W	
Continuous Drain Current	T _A =25°C	I _D	15	А	
	T _A =70°C		12	А	
Power Dissipation	T _A =25°C	D	2.0	14/	
Power Dissipation	T _A =70°C	Po	1.3	W	
Single Pulse Avalanche Energy ^(Note 6)		E _{AS}	80	mJ	
Operating Junction and Storage Temperature Range		T _J ,T _{STG}	-55~150	°C	
Typical Thermal Resistance ^(Note 4,5)	Junction to Case	$R_{\theta JC}$	2.0	°0.00	
	Junction to Ambient	$R_{\theta JA}$	62.5	°C/W	

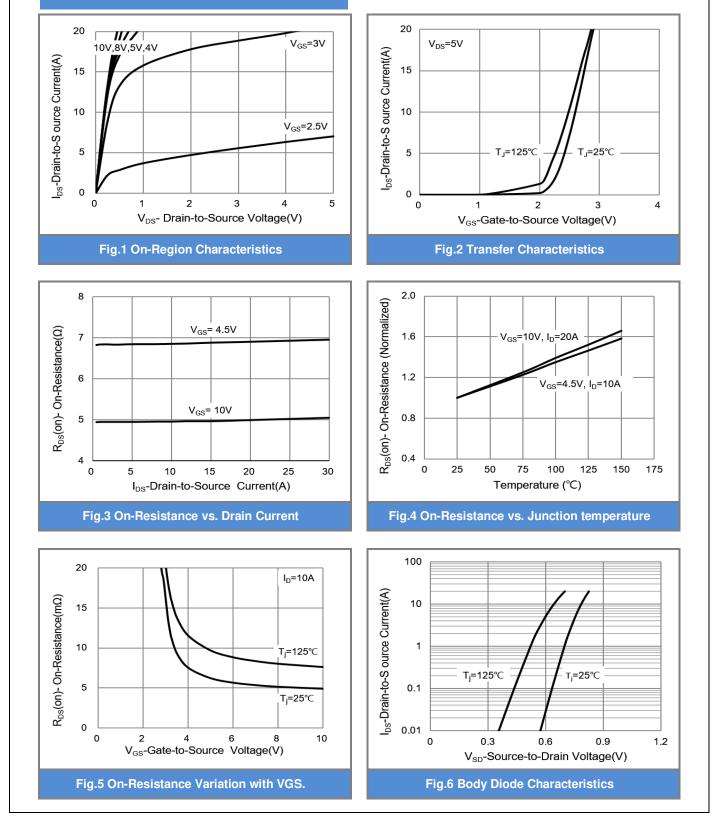


Electrical Characteristics (T_A=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static			1			
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V,I _D =250uA	30	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$, $I_{D}=250$ uA	1.0	1.6	2.5	V
Drain-Source On-State Resistance	R _{DS(on)}	V_{GS} =10V,I _D =20A	-	5.0	6	mΩ
		V _{GS} =4.5V,I _D =10A	-	6.6	9	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =30V,V _{GS} =0V	-	-	1.0	uA
Gate-Source Leakage Current	I _{GSS}	V _{GS} = <u>+</u> 20V,V _{DS} =0V	-	-	<u>+</u> 100	nA
Dynamic (Note 7)						
Total Gate Charge	Qg	V _{DS} =15V, I _D =20A, V _{GS} =4.5V ^(Note 2,3)	-	12	-	nC
Gate-Source Charge	Q_gs		-	3.8	-	
Gate-Drain Charge	Q_gd		-	4.3	-	
Input Capacitance	Ciss		-	1323	-	pF
Output Capacitance	Coss	$V_{DS}=25V, V_{GS}=0V,$	-	219	-	
Reverse Transfer Capacitance	Crss	f=1.0MHZ	-	136	-	
Turn-On Delay Time	td _(on)	$\label{eq:VDS} \begin{split} V_{DS} = & 15V, RL = & 1\Omega, \\ V_{GS} = & 10V, \ R_G = & 3.3\Omega \\ & \text{(Note 2,3)} \end{split}$	-	5.0	-	ns
Turn-On Rise Time	tr		-	42	-	
Turn-Off Delay Time	td _(off)		-	36	-	
Turn-Off Fall Time	t _f		-	5.5	-	
Drain-Source Diode						
Maximum Continuous Drain-Source			-	-	80	А
Diode Forward Current	I _S					
Diode Forward Voltage	V_{SD}	I _S =1A,V _{GS} =0V	-	0.83	1.0	V

NOTES :

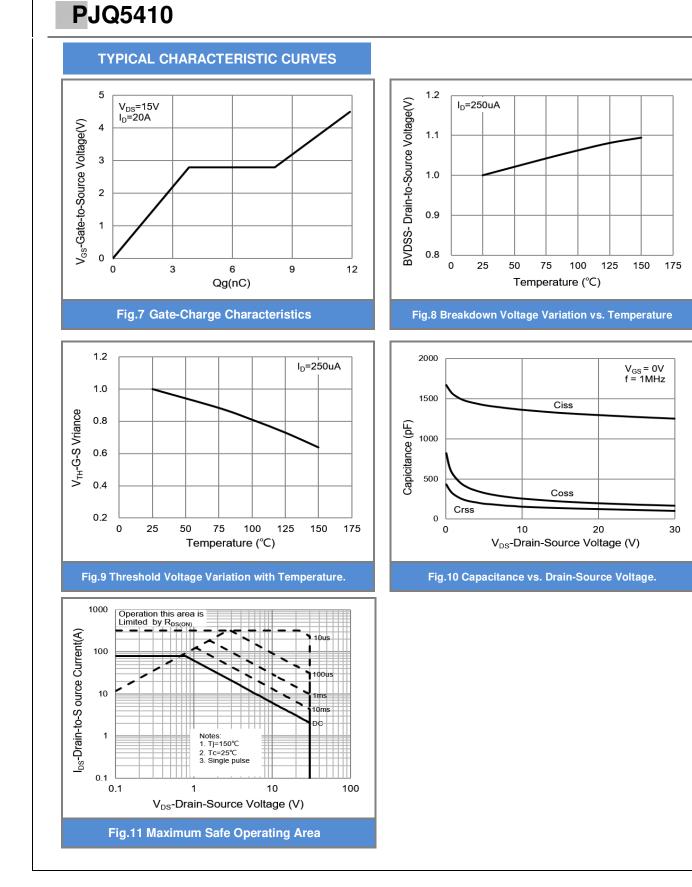
- 1. Pulse width
- 2. Essentially independent of operating temperature typical characteristics
- 3. Repetitive rating, pulse width limited by junction temperature $T_{J(MAX)}=150$ °C. Ratings are based on low frequency and duty cycles to keep initial $T_J = 25$ °C.
- 4. The maximum current rating is package limited
- 5. $R_{\Theta JA}$ is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch² with 2oz.square pad of copper.
- 6. The test condition is L=0.1mH, $I_{\text{AS}}{=}40\text{A},\,V_{\text{DD}}{=}25\text{V},\,V_{\text{GS}}{=}10\text{V}$
- 7. Guaranteed by design, not subject to production testing





TYPICAL CHARACTERISTIC CURVES



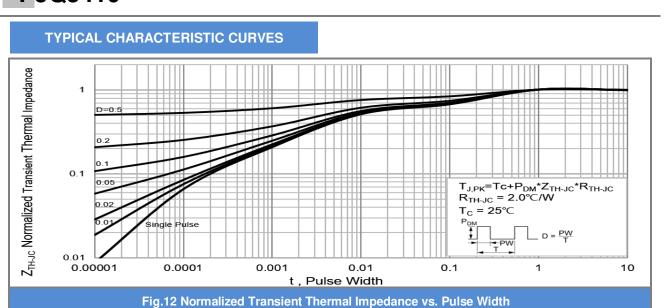






PANJIT

SEMI CONDUCTOR





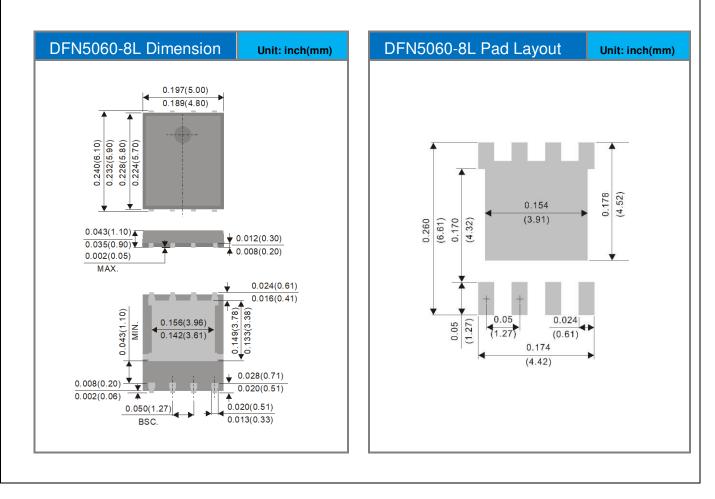




Part No Packing Code Version

Part No Packing Code	Package Type	Packing Type	Marking	Version	
PJQ5410_R2_00001	DFN5060-8L	3000pcs / 13" reel	Q5410	Halogen free	

Packaging Information & Mounting Pad Layout







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