

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D Max T _A = +25°C
-40V	13mΩ @ V _{GS} = -10V	-10.3A
	18mΩ @ V _{GS} = -4.5V	-8.8A

Description and Applications

This MOSFET is designed to meet the stringent requirements of automotive applications. It is qualified to AEC-Q101, supported by a PPAP and is ideal for use in:

- Reverse-polarity protections
- Power-management functions
- DC-DC converters

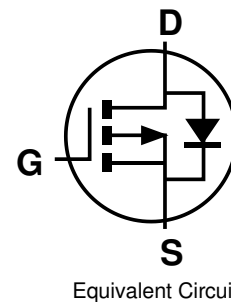
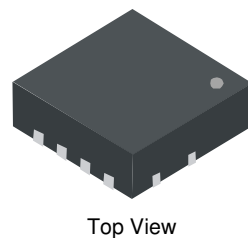
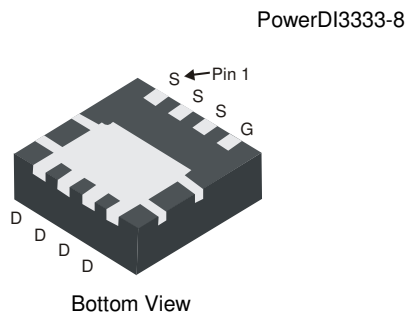
Features and Benefits

- Low R_{DS(ON)} – Ensures On-State Losses are Minimized
- Small Form Factor Thermally Efficient Package Enables Higher Density End Products
- Occupies 33% of the Board Area Occupied by SO-8, Enabling Smaller End Product
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **The DMP4013LFGQ is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.**

<https://www.diodes.com/quality/product-definitions/>

Mechanical Data

- Package: PowerDI[®]3333-8
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See Diagram
- Terminals: Finish – Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208
- Weight: 0.034 grams (Approximate)



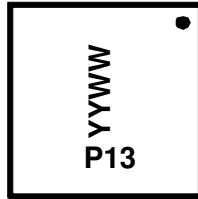
Ordering Information (Note 4)

Part Number	Package	Packing	
		Qty.	Carrier
DMP4013LFGQ-7	PowerDI3333-8	2000	Tape & Reel
DMP4013LFGQ-13	PowerDI3333-8	3000	Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
 2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

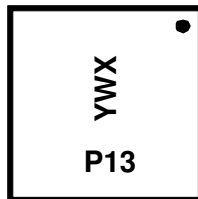
Marking Information

Site 1



P13 = Product Type Marking Code
 YYWW = Date Code Marking
 YY = Last Two Digits of Year (ex: 23 = 2023)
 WW = Week Code (01 to 53)

Site2



P13 = Product Type Marking Code
 YWX = Date Code Marking
 Y = Year (ex: 3 = 2023)
 W = Week (ex: a = Week 27; z Represents Week 52 and 53)
 X = Internal Code (ex: U = Monday)

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Units
Drain-Source Voltage			V _{DSS}	-40	V
Gate-Source Voltage			V _{GSS}	±20	V
Continuous Drain Current (Note 6) V _{GS} = -10V	Steady State	T _A = +25°C T _A = +70°C	I _D	-10.3 -8.3	A
	t < 10s	T _A = +25°C T _A = +70°C	I _D	-13.7 -11	A
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)			I _{DM}	-80	A
Maximum Continuous Body Diode Forward Current (Note 6)			I _S	-10.3	A
Avalanche Current, L = 0.1mH			I _{AS}	-34	A
Avalanche Energy, L = 0.1mH			E _{AS}	58	mJ

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Units
Total Power Dissipation (Note 5)			P _D	1	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State		R _{θJA}	123	°C/W
	t < 10s			69	
Total Power Dissipation (Note 6)			P _D	2.1	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State		R _{θJA}	60	°C/W
	t < 10s			34	
Thermal Resistance, Junction to Case (Note 6)			R _{θJC}	3.3	
Operating and Storage Temperature Range			T _J , T _{STG}	-55 to +150	°C

Notes: 5. Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
 6. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV _{DSS}	-40	—	—	V	V _{GS} = 0V, I _D = -250μA
Zero Gate Voltage Drain Current T _J = +25°C	I _{DSS}	—	—	-1	μA	V _{DS} = -40V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	—	—	±100	nA	V _{GS} = ±20V, V _{DS} = 0V
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(TH)}	-1	—	-3	V	V _{DS} = V _{GS} , I _D = -250μA
Static Drain-Source On-Resistance	R _{DS(ON)}	—	9.4	13	mΩ	V _{GS} = -10V, I _D = -10A
		—	12.3	18		V _{GS} = -4.5V, I _D = -8A
Diode Forward Voltage	V _{SD}	—	-0.7	-1.2	V	V _{GS} = 0V, I _S = -1A
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C _{iss}	—	3426	—	pF	V _{DS} = -20V, V _{GS} = 0V, f = 1MHz
Output Capacitance	C _{oss}	—	283	—	pF	
Reverse Transfer Capacitance	C _{rss}	—	235	—	pF	
Gate Resistance	R _g	—	4.7	—	Ω	V _{DS} = 0V, V _{GS} = 0V, f = 1MHz
Total Gate Charge (V _{GS} = -4.5V)	Q _g	—	32.5	—	nC	V _{DS} = -20V, I _D = -10A
Total Gate Charge (V _{GS} = -10V)	Q _g	—	68.6	—	nC	
Gate-Source Charge	Q _{gs}	—	8.2	—	nC	
Gate-Drain Charge	Q _{gd}	—	9.9	—	nC	
Turn-On Delay Time	t _{D(ON)}	—	5.3	—	ns	V _{DD} = -20V, V _{GEN} = -10V, R _G = 3Ω, I _D = -10A
Turn-On Rise Time	t _r	—	20	—	ns	
Turn-Off Delay Time	t _{D(OFF)}	—	126	—	ns	
Turn-Off Fall Time	t _f	—	83	—	ns	
Body Diode Reverse-Recovery Time	t _{RR}	—	19.5	—	ns	I _F = -10A, di/dt = 100A/μs
Body Diode Reverse-Recovery Charge	Q _{RR}	—	9.8	—	nC	

Notes: 7. Short duration pulse test used to minimize self-heating effect.
8. Guaranteed by design. Not subject to product testing.

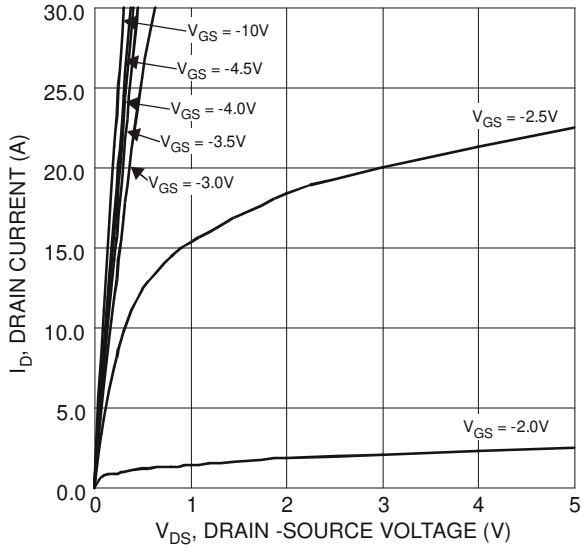


Figure 1 Typical Output Characteristics

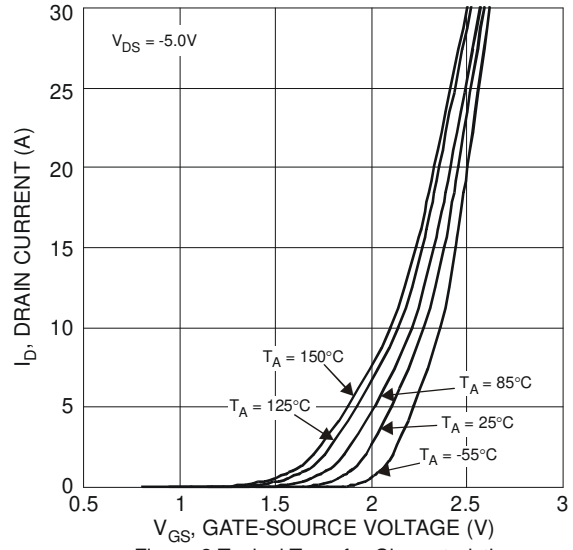


Figure 2 Typical Transfer Characteristics

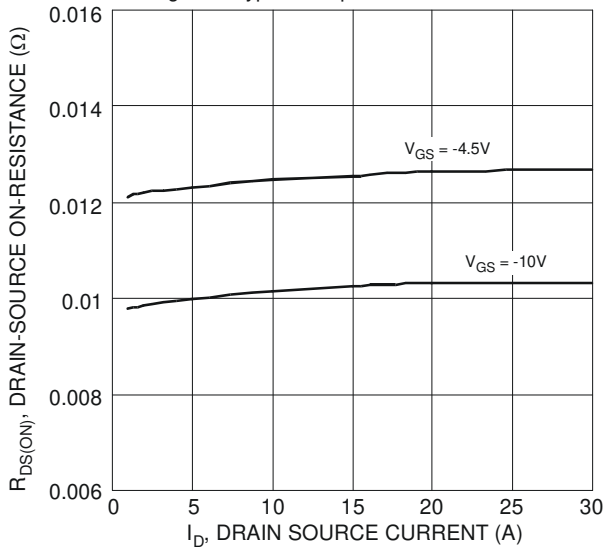


Figure 3 Typical On-Resistance vs. Drain Current and Gate Voltage

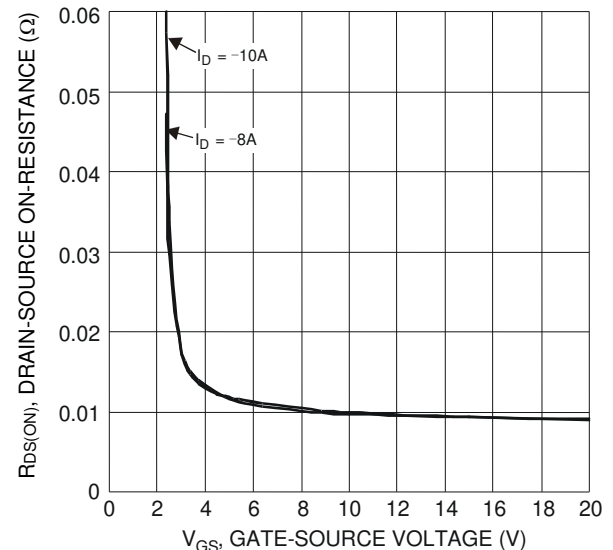


Figure 4 Typical Transfer Characteristics

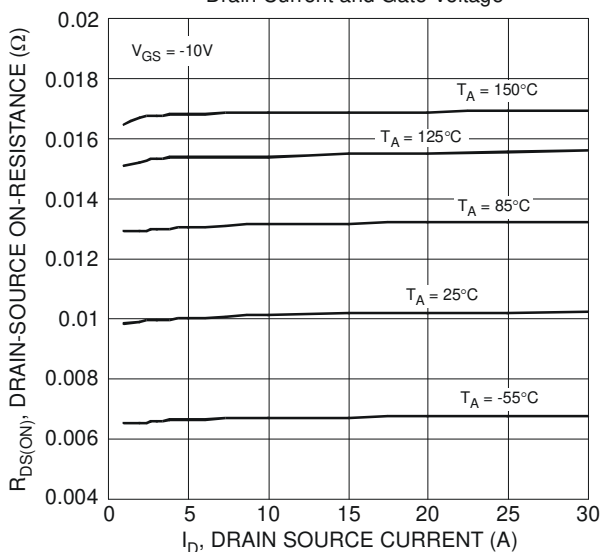


Figure 5 Typical On-Resistance vs. Drain Current and Temperature

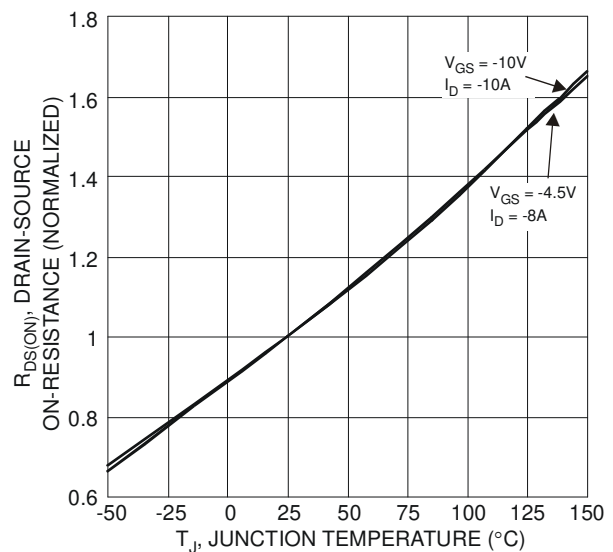


Figure 6 On-Resistance Variation with Temperature

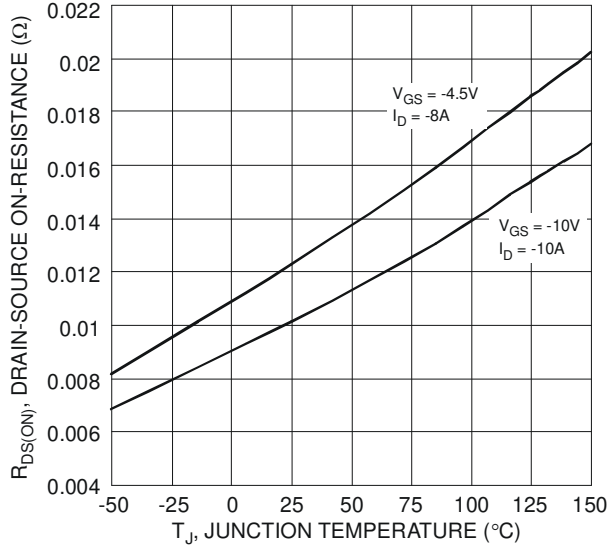


Figure 7 On-Resistance Variation with Temperature

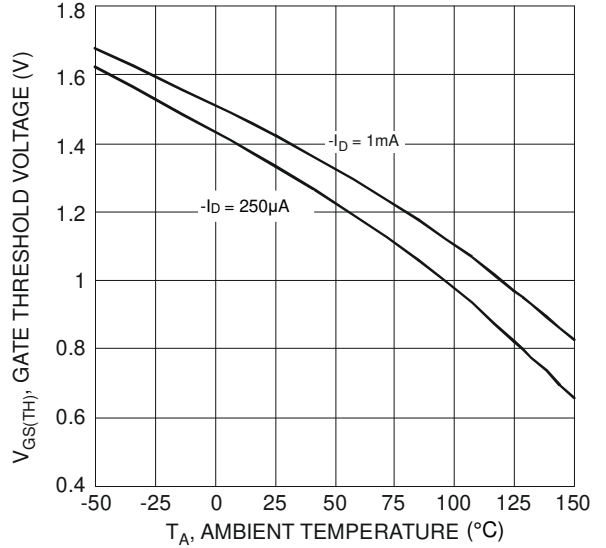


Figure 8 Gate Threshold Variation vs. Ambient Temperature

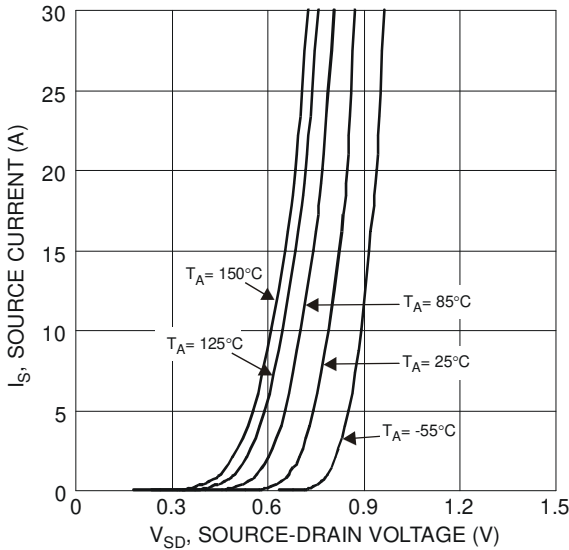


Figure 9 Diode Forward Voltage vs. Current

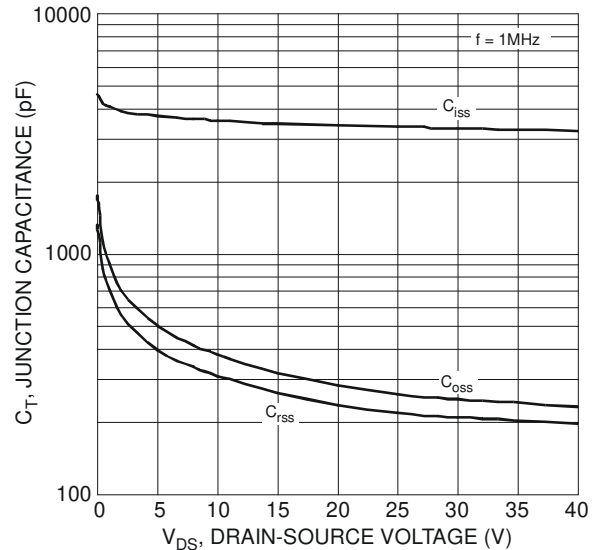


Figure 10 Typical Junction Capacitance

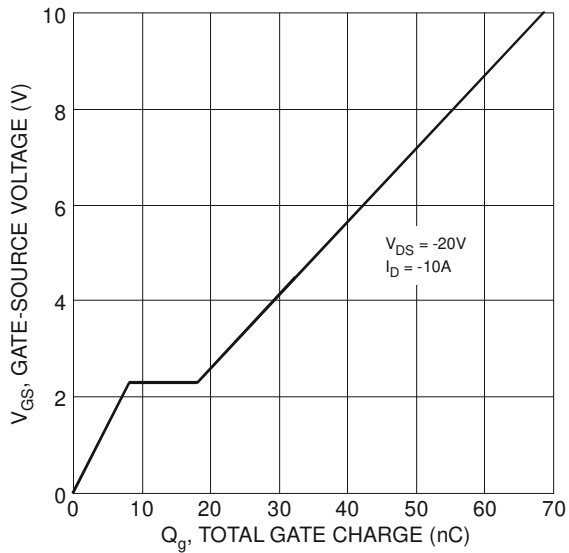


Figure 11 Gate-Charge Characteristics

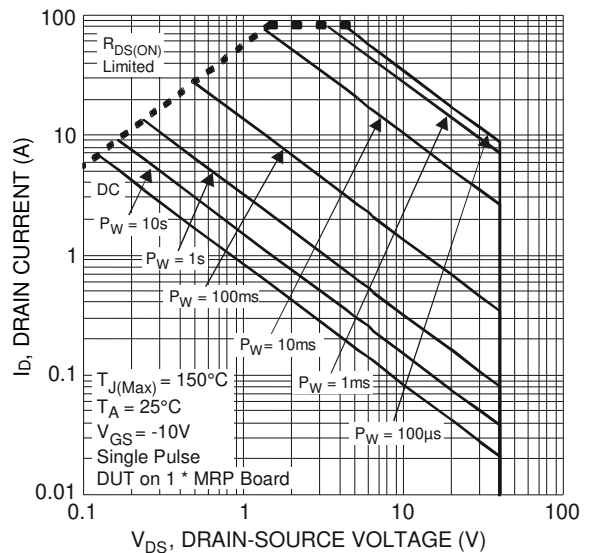
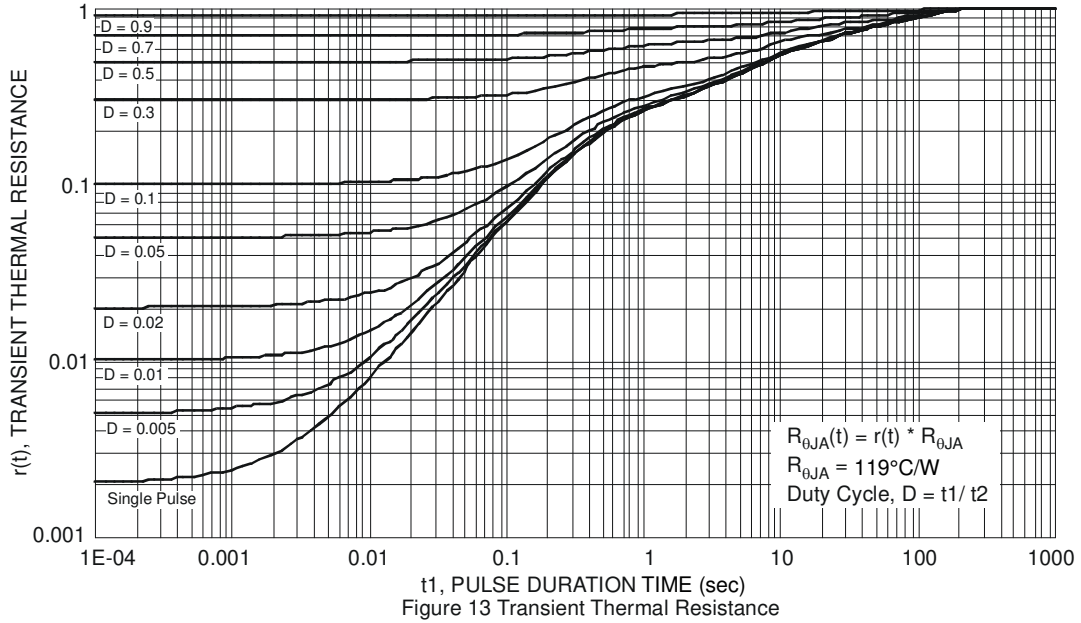


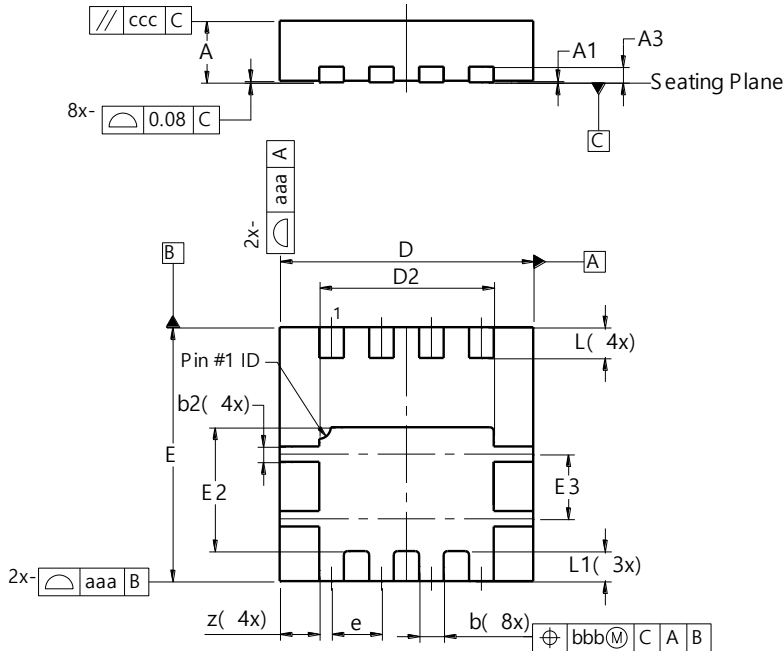
Figure 12 SOA, Safe Operation Area



Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

PowerDI3333-8

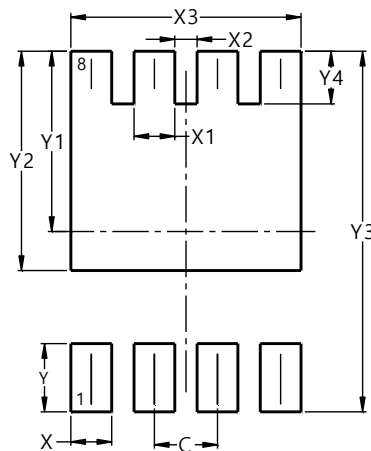


PowerDI3333-8			
Dim	Min	Max	Typ
A	0.75	0.85	0.80
A1	0.00	0.05	0.02
A3	-	-	0.203
b	0.27	0.37	0.32
b2	-	-	0.20
D	3.25	3.35	3.30
D2	2.22	2.32	2.27
E	3.25	3.35	3.30
E2	1.56	1.66	1.61
E3	0.79	0.89	0.84
e	-	-	0.65
L	0.35	0.45	0.40
L1	-	-	0.39
z	-	-	0.515
aaa	0.25		
bbb	0.10		
ccc	0.10		
All Dimensions in mm			

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

PowerDI3333-8



Dimensions	Value (in mm)
C	0.650
X	0.420
X1	0.420
X2	0.230
X3	2.370
Y	0.700
Y1	1.850
Y2	2.250
Y3	3.700
Y4	0.540

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