



# OM13039



Keil MCB1857 Evaluation Board

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OM13039,598

Region

Distributor  In Stock

OR:

## Demo board description

The Keil MCB1857 Evaluation Board enables you to create and test working programs based on the LPC1800 family of ARM Cortex™-M3 based devices. The MCB1857 Evaluation Board and Starter Kit includes the MDK-ARM Lite Edition Tools. These tools help you get started writing programs and testing the microcontroller and its capabilities. Sample applications that run on the MCB1857 evaluation board, and a Quickstart guide are included.

## Features

- Full-speed USB 2.0 Host/Device interface (USB host + micro USB Device connectors)
- CAN interfaces
- 180MHz ARM Cortex-M3 processor-based MCU in LBG256
- 136KB On-Chip SRAM
- 1MB dual bank On-Chip Flash
- On-Board Memory: 16MB NOR Flash, 4MB Quad-SPI Flash, 16 MB SDRAM, & 16KB EEPROM (I2C)
- Color QVGA TFT LCD with touchscreen
- 10/100 Ethernet Port
- High-speed USB 2.0 Host/Device/OTG interface (USB host + Micro USB Device/OTG connectors)
- Serial/UART Port
- MicroSD Card Interface
- 4 user push-buttons + reset
- Digital Temperature Sensor (I2C)
- Analog Voltage Control for ACD Input
- Audio CODEC with Line-In/Out and Microphone/headphone connector + Speaker
- Debug Interface Connectors:
  - 20-pin JTAG (0.1 inch)
  - 10-pin Cortex debug (0.05 inch)
  - 20-pin Cortex debug + ETM Trace (0.05 inch)

## Descriptive summary

### Keil MCB1857 Evaluation Board

The Keil MCB1857 Evaluation Board enables you to create and test working programs based on the LPC1800 family of ARM Cortex™-M3 based devices.

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## Demo boards

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OM13039	9352 982 11598	OM13039,598	Volume production	NA	MOUSER ELECTRONICS	8		12/11/2012	<a href="#">Buy</a>
				NA	DIGI-KEY CORPORATION	1		12/11/2012	<a href="#">Buy</a>

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Type number	Description	Status	Quick access
<a href="#">LPC1810FBD144</a>	Cortex-M3, 136 kB SRAM, CAN, AES, SPIFI, SCT	<a href="#">Qualification</a>	<a href="#">Download datasheet</a> <a href="#">Order sample</a>
<a href="#">LPC1810FET100</a>	Cortex-M3, 136 kB SRAM, CAN, AES, SPIFI, SCT	<a href="#">Production</a>	<a href="#">Download datasheet</a> <a href="#">Order sample</a>
<a href="#">LPC1820FBD100</a>	Cortex-M3, 168 kB SRAM, HS USB with on-chip PHY, CAN, AES, SPIFI, SCT	<a href="#">Development</a>	<a href="#">Download datasheet</a>
<a href="#">LPC1820FBD144</a>	Cortex-M3, 168 kB SRAM, HS USB with on-chip PHY, CAN, AES, SPIFI, SCT	<a href="#">Qualification</a>	<a href="#">Download datasheet</a> <a href="#">Order sample</a>
<a href="#">LPC1820FET100</a>	Cortex-M3, 168 kB SRAM, HS USB with on-chip PHY, CAN, AES, SPIFI, SCT	<a href="#">Production</a>	<a href="#">Download datasheet</a> <a href="#">Order sample</a>
<a href="#">LPC1830FBD144</a>	Cortex-M3, 200 kB SRAM, 2 HS USB with on-chip PHY, Ethernet, CAN, AES, SPIFI, SCT	<a href="#">Qualification</a>	<a href="#">Download datasheet</a> <a href="#">Order sample</a>
<a href="#">LPC1830FET100</a>	Cortex-M3, 200 kB SRAM, 2 HS USB with on-chip PHY, Ethernet, CAN, AES, SPIFI, SCT	<a href="#">Production</a>	<a href="#">Download datasheet</a> <a href="#">Order sample</a>
<a href="#">LPC1830FET180</a>	Cortex-M3, 200 kB SRAM, 2 HS USB with on-chip PHY, Ethernet, CAN, AES, SPIFI, SCT	<a href="#">Development</a>	<a href="#">Download datasheet</a>
<a href="#">LPC1830FET256</a>	Cortex-M3, 200 kB SRAM, 2 HS USB with on-chip PHY, Ethernet, CAN, AES, SPIFI, SCT	<a href="#">Production</a>	<a href="#">Download datasheet</a> <a href="#">Order sample</a>
<a href="#">LPC1833FBD144</a>	32-bit ARM Cortex-M3 MCU; up to 1 MB flash and 136 kB SRAM; Ethernet, two High-speed USB, LCD, EMC	<a href="#">Development</a>	
<a href="#">LPC1833FET100</a>	32-bit ARM Cortex-M3 MCU; up to 1 MB flash and 136 kB SRAM; Ethernet, two High-speed USB, LCD, EMC	<a href="#">Development</a>	
<a href="#">LPC1833FET180</a>	32-bit ARM Cortex-M3 MCU; up to 1 MB flash and 136 kB SRAM; Ethernet, two High-speed USB, LCD, EMC	<a href="#">Development</a>	
<a href="#">LPC1833FET256</a>	32-bit ARM Cortex-M3 MCU; up to 1 MB flash and 136 kB SRAM; Ethernet, two High-speed USB, LCD, EMC	<a href="#">Qualification</a>	<a href="#">Download datasheet</a> <a href="#">Order sample</a>
<a href="#">LPC1837FET100</a>	32-bit ARM Cortex-M3 MCU; up to 1 MB flash and 136 kB SRAM; Ethernet, two High-speed USB, LCD, EMC	<a href="#">Development</a>	
<a href="#">LPC1837FET180</a>	32-bit ARM Cortex-M3 MCU; up to 1 MB	<a href="#">Development</a>	

flash and 136 kB SRAM; Ethernet, two High-speed USB, LCD, EMC

<a href="#">LPC1837FET256</a>	32-bit ARM Cortex-M3 MCU; up to 1 MB flash and 136 kB SRAM; Ethernet, two High-speed USB, LCD, EMC	<a href="#">Qualification</a>	<a href="#">Download datasheet</a> <a href="#">Order sample</a>
<a href="#">LPC1850FBD208</a>	Cortex-M3, 200 kB SRAM, 2 HS USB with on-chip PHY, Ethernet, LCD, CAN, AES, SPIFI, SCT	<a href="#">Development</a>	<a href="#">Download datasheet</a>
<a href="#">LPC1850FET180</a>	Cortex-M3, 200 kB SRAM, 2 HS USB with on-chip PHY, Ethernet, LCD, CAN, AES, SPIFI, SCT	<a href="#">Development</a>	<a href="#">Download datasheet</a>
<a href="#">LPC1850FET256</a>	Cortex-M3, 200 kB SRAM, 2 HS USB with on-chip PHY, Ethernet, LCD, CAN, AES, SPIFI, SCT	<a href="#">Production</a>	<a href="#">Download datasheet</a> <a href="#">Order sample</a>
<a href="#">LPC1853FBD208</a>	Cortex-M3, 512 kB dual-bank flash, 136 kB SRAM, 2 HS USB with on-chip PHY, Ethernet, LCD, CAN, AES, SPIFI, SCT	<a href="#">Development</a>	
<a href="#">LPC1853FET180</a>	Cortex-M3, 512 kB dual-bank flash, 136 kB SRAM, 2 HS USB with on-chip PHY, Ethernet, LCD, CAN, AES, SPIFI, SCT	<a href="#">Development</a>	
<a href="#">LPC1853FET256</a>	Cortex-M3, 512 kB dual-bank flash, 136 kB SRAM, 2 HS USB with on-chip PHY, Ethernet, LCD, CAN, AES, SPIFI, SCT	<a href="#">Qualification</a>	<a href="#">Order sample</a>
<a href="#">LPC1857FBD208</a>	Cortex-M3, 1 MB dual-bank flash, 136 kB SRAM, 2 HS USB with on-chip PHY, Ethernet, LCD, CAN, AES, SPIFI, SCT	<a href="#">Development</a>	
<a href="#">LPC1857FET180</a>	Cortex-M3, 1 MB dual-bank flash, 136 kB SRAM, 2 HS USB with on-chip PHY, Ethernet, LCD, CAN, AES, SPIFI, SCT	<a href="#">Development</a>	
<a href="#">LPC1857FET256</a>	Cortex-M3, 1 MB dual-bank flash, 136 kB SRAM, 2 HS USB with on-chip PHY, Ethernet, LCD, CAN, AES, SPIFI, SCT	<a href="#">Qualification</a>	<a href="#">Download datasheet</a> <a href="#">Order sample</a>

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<a href="#">om13000,598</a>	12/012	1

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