



DMP1012USS

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D Max T _A = +25°C
	15mΩ @ V _{GS} = -4.5V	-8.5A
	20mΩ @ V _{GS} = -3.7V	-7.3A
-12V	25mΩ @ V _{GS} = -3.3V	-6.6A
	30mΩ @ V _{GS} = -2.5V	-6.0A
	40mΩ @ V _{GS} = -1.8V	-5.2A

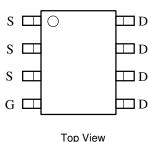
Description and Applications

This MOSFET is designed to minimize the on-state resistance $(R_{DS(ON)})$ and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- Motor Control
- Backlighting
- Power Management Functions
- DC-DC Converters



Top View





Equivalent Circuit

Ordering Information (Note 4)

Part Number	Case	Packaging
DMP1012USS-13	SO-8	2,500/Tape & Reel

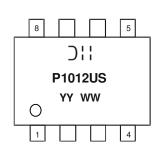
Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



)|| = Manufacturer's Marking P1012US = Product Type Marking Code YYWW = Date Code Marking YY or \overline{YY} = Year (ex: 18 = 2018) WW = Week (01 to 53)

12V P-CHANNEL ENHANCEMENT MODE MOSFET

Features and Benefits

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

Mechanical Data

- Case: SO-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 3 per J-STD-020
- Terminal Connections Indicator: See Diagram Below
- Terminals: Finish Matte Tin Annealed over Copper Lead Frame. Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.074 grams (Approximate)



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V _{DSS}	-12	V
Gate-Source Voltage			V _{GSS}	±8	V
Continuous Drain Current (Note 6) V_{GS} = -4.5V	Steady State	T _A = +25°C T _A = +70°C	ID	-8.5 -6.8	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	I _{DM}	-40	A		
Maximum Continuous Body Diode Forward Current (Note 6)			ls	-2	A
Pulsed Body Diode Forward Current (10µs Pulse, Duty Cycle = 1%)			I _{SM}	-40	A
Avalanche Current (Note 7) L = 0.1mH			IAS	-21	A
Avalanche Energy (Note 7) L = 0.1mH			E _{AS}	22	mJ

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Total Power Dissipation (Note 5)	Steady State	$T_A = +25^{\circ}C$	PD	1.3	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady state	$R_{\theta JA}$	99	°C/W	
Total Power Dissipation (Note 6)	Steady State	T _A = +25°C	PD	1.6	W
Thermal Resistance, Junction to Ambient (Note 6) St		Steady state	R _{0JA}	77	°C/W
Thermal Resistance, Junction to Case (Note 6)			R _{0JC}	13	-C/W
Operating and Storage Temperature Range			T _{J,} T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Мах	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)							
Drain-Source Breakdown Voltage	BV _{DSS}	-12	_	_	V	$V_{GS} = 0V, I_D = -250\mu A$	
Zero Gate Voltage Drain Current	I _{DSS}	-	—	-1	μA	$V_{DS} = -9.6V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	—	_	±100	nA	$V_{GS} = \pm 8V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)							
Gate Threshold Voltage	V _{GS(TH)}	-0.4	—	-1.0	V	$V_{DS}=V_{GS},I_{D}=-250\mu A$	
			9	15		$V_{GS} = -4.5V, I_D = -9A$	
			10	20	mΩ	$V_{GS} = -3.7V, I_D = -7A$	
Static Drain-Source On-Resistance	R _{DS(ON)}	—	11	25		$V_{GS} = -3.3V, I_D = -7A$	
			13	30		$V_{GS} = -2.5V, I_D = -6A$	
			18	40		$V_{GS} = -1.8V, I_D = -4A$	
Diode Forward Voltage	V _{SD}	_	-0.8	-1.2	V	$V_{GS} = 0V, I_{S} = -1A$	
DYNAMIC CHARACTERISTICS (Note 9)							
Input Capacitance	Ciss	—	1344	—			
Output Capacitance	Coss	—	342		pF	VDS = -10V, VGS = 0V, f = 1.0MHz	
Reverse Transfer Capacitance	Crss	—	297	—			
Gate Resistance	Rg	—	15		Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge (V _{GS} = -4.5V)	Qg	—	19.5	—			
Total Gate Charge (V _{GS} = -8V)	Qg	_	31	—	nC	$V_{DS} = -6V, I_D = -10A$	
Gate-Source Charge	Q _{gs}	_	2.1	—	110		
Gate-Drain Charge	Q _{gd}	-	7.9				
Turn-On Delay Time	t _{D(ON)}	—	6.0	_			
Turn-On Rise Time	t _R	—	32	_		$\label{eq:VDS} \begin{array}{l} V_{DS} = -6V, \ V_{GS} = -4.5V, \\ R_g = 1\Omega, \ I_D = -8A \end{array}$	
Turn-Off Delay Time	t _{D(OFF)}	—	71	_	ns		
Turn-Off Fall Time	tF	—	85	—			
Reverse Recovery Time	t _{RR}	—	46	—	ns	1 100 di/dt 5000/u-	
Reverse Recovery Charge	Q _{RR}	—	44	_	nC	I _F = -12A, di/dt = 500A/µs	

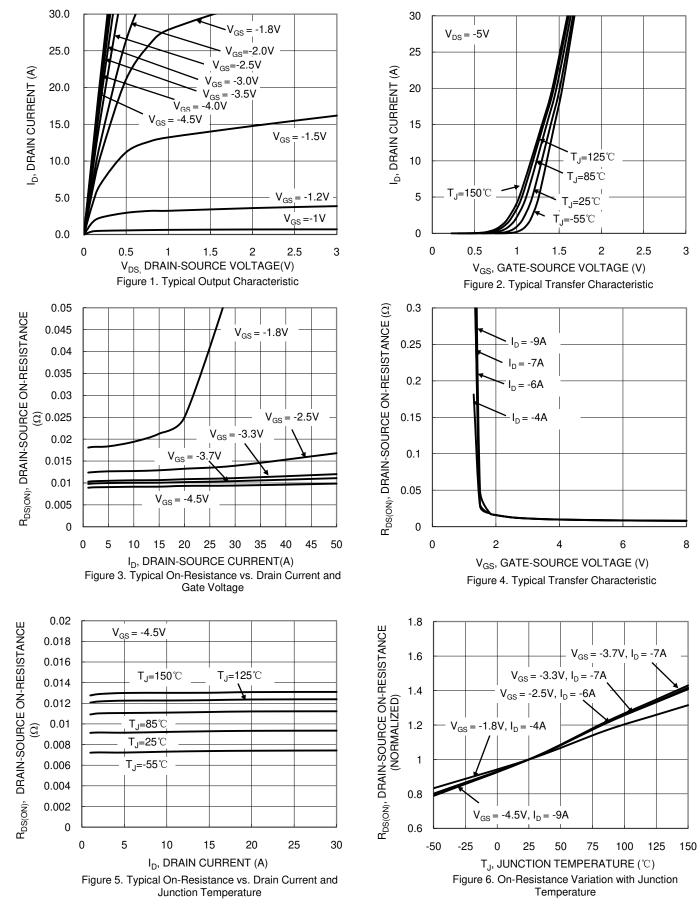
Notes: 5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.

6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

7. I_{AS} and E_{AS} ratings are based on low frequency and duty cycles to keep $T_J = +25^{\circ}C$. 8. Short duration pulse test used to minimize self-heating effect. 9. Guaranteed by design. Not subject to product testing.



DMP1012USS





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 $I_D = -1mA$

25

C_{rss}

4

Pw =1s

1

P_w =10s

DĊ

10

50

Temperature

Ciss

C_{oss}

6

8

75

100

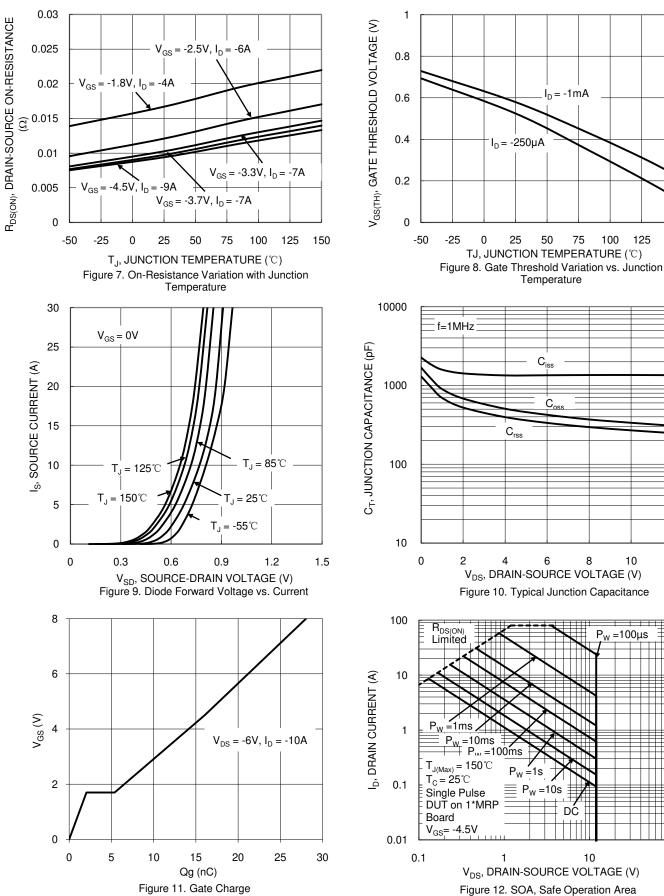
125

10

P_w =100μs

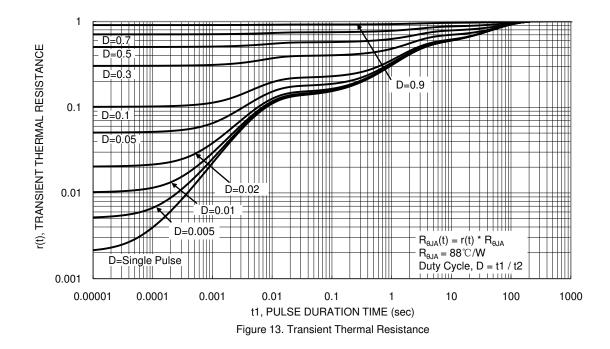
12

150



100

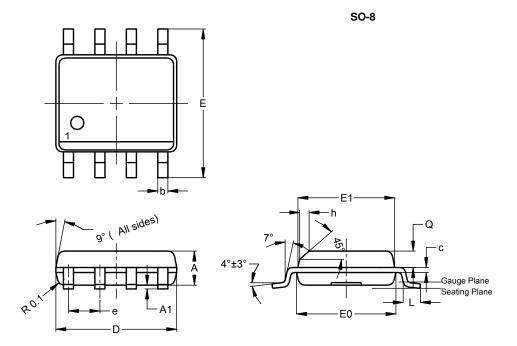






Package Outline Dimensions

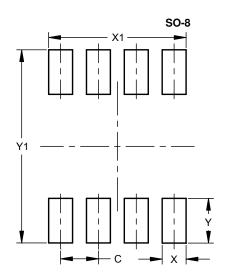
Please see http://www.diodes.com/package-outlines.html for the latest version.



SO-8					
Dim	Min	Max	Тур		
Α	1.40	1.50	1.45		
A1	0.10	0.20	0.15		
b	0.30	0.50	0.40		
С	0.15	0.25	0.20		
D	4.85	4.95	4.90		
Е	5.90	6.10	6.00		
E1	3.80	3.90	3.85		
E0	3.85	3.95	3.90		
е			1.27		
h	-		0.35		
L	0.62	0.82	0.72		
q	0.60	0.70	0.65		
All	All Dimensions in mm				

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



Dimensions	Value (in mm)
С	1.27
Х	0.802
X1	4.612
Y	1.505
Y1	6.50



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