ΡΛΝ	JIT
	SEMI
	CONDUCTOR

### 30V N-Channel Enhancement Mode MOSFET

30 V Current 6.8A

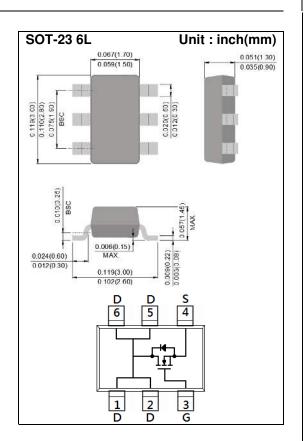
### Features

Voltage

- RDS(ON) , VGS@10V, ID@6.8A<32mΩ
- RDS(ON) , VGS@4.5V,ID@4.3A<47mΩ
- Advanced Trench Process Technology
- Specially Designed for Switch Load, PWM Application, etc..
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

### **Mechanical Data**

- Case: SOT-23 6L Package
- Terminals: Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 0.0005 ounces, 0.014 grams
- Marking: S04



### Maximum Ratings and Thermal Characteristics (TA=25°C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V <sub>DS</sub>	30	V
Gate-Source Voltage		V <sub>GS</sub>	<u>+</u> 20	V
Continuous Drain Current		ID	6.8	А
Pulsed Drain Current		ldм	27.2	А
Power Dissipation	T <sub>a</sub> =25°C	PD	2	w
	Derate above 25°C		16	mW/°C
Operating Junction and Storage Temperature Range		T <sub>J</sub> ,Т <sub>STG</sub>	-55~150	°C
Typical Thermal Resistance - Junction to Ambient <sup>(Note 3)</sup>		R <sub>0JA</sub>	62.5	°C/W



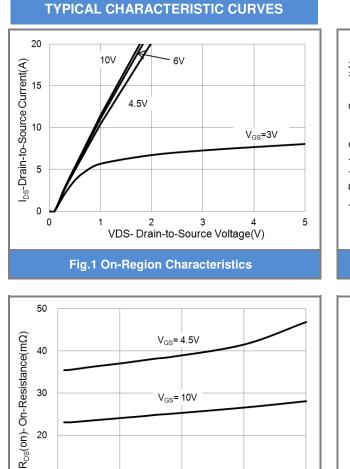
## Electrical Characteristics (T<sub>A</sub>=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA	30	-	-	V
Gate Threshold Voltage	$V_{\text{GS(th)}}$	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250uA	1.0	1.4	2.1	V
Drain-Source On-State Resistance	$R_{\text{DS(on)}}$	V <sub>GS</sub> =10V, I <sub>D</sub> =6.8A	-	26	32	mΩ
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =4.3A	-	38	47	
Zero Gate Voltage Drain Current	IDSS	V <sub>DS</sub> =30V, V <sub>GS</sub> =0V	-	0.01	1	uA
Gate-Source Leakage Current	lgss	V <sub>GS=<u>+</u>20V, V<sub>DS</sub>=0V</sub>	-	<u>+</u> 10	<u>+</u> 100	nA
Dynamic						
Total Gate Charge	Qg	$V_{DS}=15V, I_{D}=6.8A,$ $V_{GS}=10V^{(Note 1,2)}$	-	7.8	-	nC
Gate-Source Charge	Qgs		-	1.2	-	
Gate-Drain Charge	$Q_{gd}$		-	1.5	-	
Input Capacitance	Ciss	V <sub>DS</sub> =15V, V <sub>GS</sub> =0V,	-	343	-	pF
Output Capacitance	Coss		-	48	-	
Reverse Transfer Capacitance	Crss	f=1.0MHZ	-	34	-	
Switching						
Turn-On Delay Time	td <sub>(on)</sub>	$V_{DD}=15V, I_{D}=6.8A, V_{GS}=10V, Q_{S}=10V, Q_{S}=$	-	3.1	-	
Turn-On Rise Time	tr			40	-	
Turn-Off Delay Time	td <sub>(off)</sub>			38	-	ns
Turn-Off Fall Time	tf	$R_G=6\Omega^{(Note 1,2)}$	-	39	-	
Drain-Source Diode						
Maximum Continuous Drain-Source					2.0	٨
Diode Forward Current	ls		-	-	2.0	A
Diode Forward Voltage	V <sub>SD</sub>	Is=1.0A, V <sub>GS</sub> =0V	-	0.75	1.2	v

NOTES :

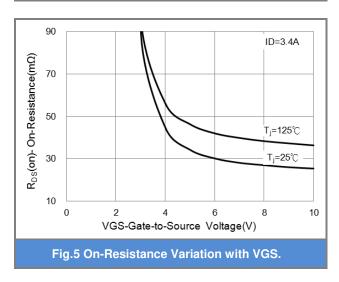
- 1. Pulse width <300us, Duty cycle <2%
- 2. Essentially independent of operating temperature typical characteristics.
- 3. ReJA is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper
- 4. The maximum current rating is package limited

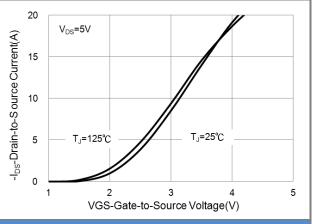




20 10 0 5 10 15 20 IDS-Drain-to-Source Current(A)

#### Fig.3 On-Resistance vs. Drain Current





#### **Fig.2 Transfer Characteristics**

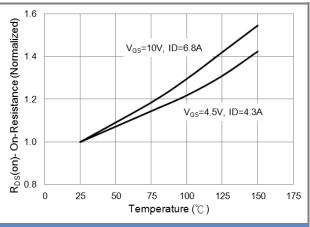
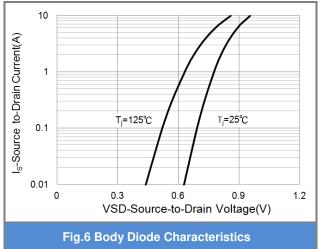


Fig.4 On-Resistance vs. Junction temperature





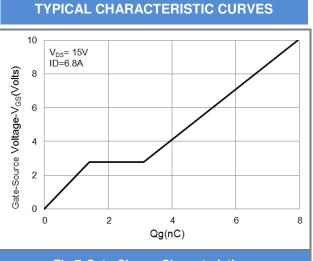


Fig.7 Gate-Charge Characteristics

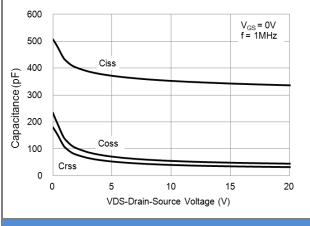


Fig.9 Capacitance vs. Drain-Source Voltage.

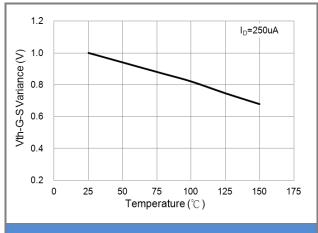


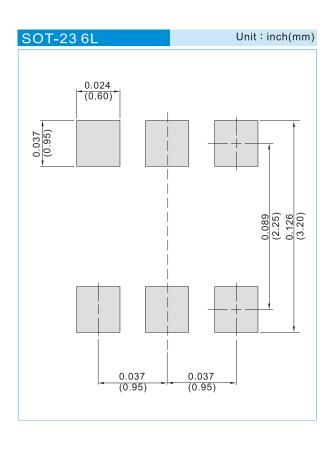
Fig.8 Threshold Voltage Variation with Temperature.



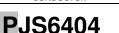
### PART NO. PACKING CODE VERSION

Part No. Packing Code	Package Type	Packing Type	Marking	Version
PJS6404_S1_00001	SOT-23 6L	3K pcs / 7" reel	S04	Halogen free RoHS compliant

## **MOUNTING PAD LAYOUT**







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